★ ESP-IDF Programming Guide **SPRESSIF** ESP32-S3 master (latest)

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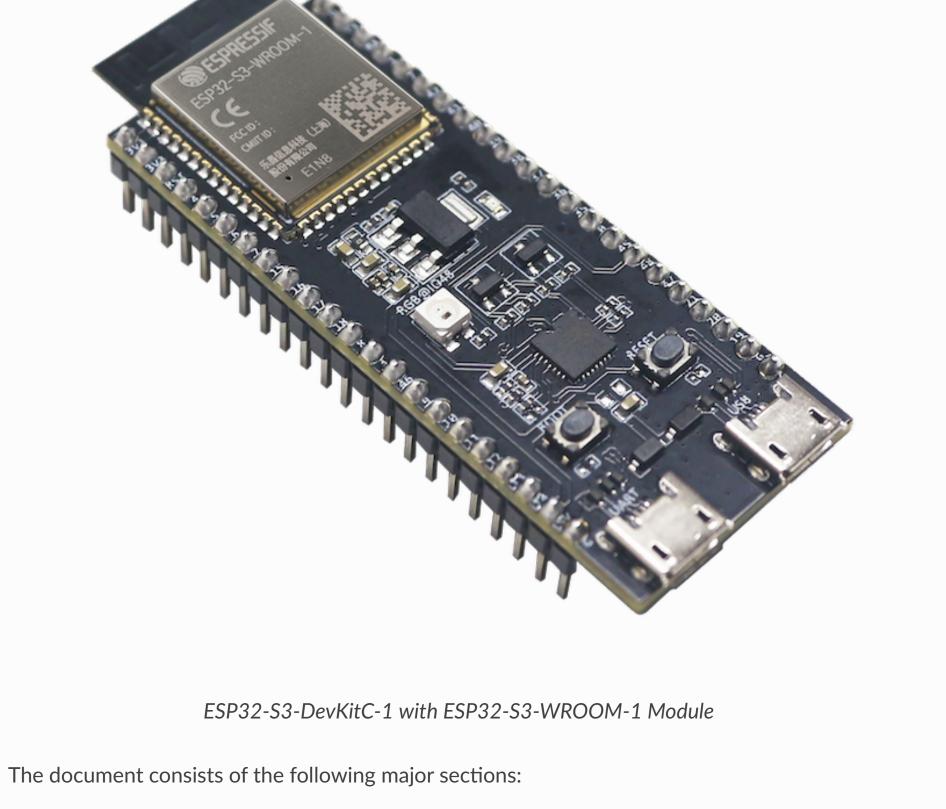
This user guide will help you get started with ESP32-S3-DevKitC-1 and will also provide more indepth information.

G Edit on GitHub

The ESP32-S3-DevKitC-1 is an entry-level development board equipped with ESP32-S3-WROOM-1, ESP32-S3-WROOM-1U, or ESP32-S3-WROOM-2, a general-purpose Wi-Fi + Bluetooth® LE MCU module that integrates complete Wi-Fi and Bluetooth LE functions.

Most of the I/O pins on the module are broken out to the pin headers on both sides of this board

for easy interfacing. Developers can either connect peripherals with jumper wires or mount ESP32-S3-DevKitC-1 on a breadboard.



• Hardware Revision Details: Revision history, known issues, and links to user guides for previous versions (if any) of the board.

• Hardware Reference: More detailed information about the board's hardware.

• Getting started: Overview of the board and hardware/software setup instructions to get started.

• Related Documents: Links to related documentation.

- **Getting Started**
- This section provides a brief introduction of ESP32-S3-DevKitC-1, instructions on how to do the initial hardware setup and how to flash firmware onto it.
- **Description of Components**

ESP32-S3-WROOM-1/1U/2

RGB LED

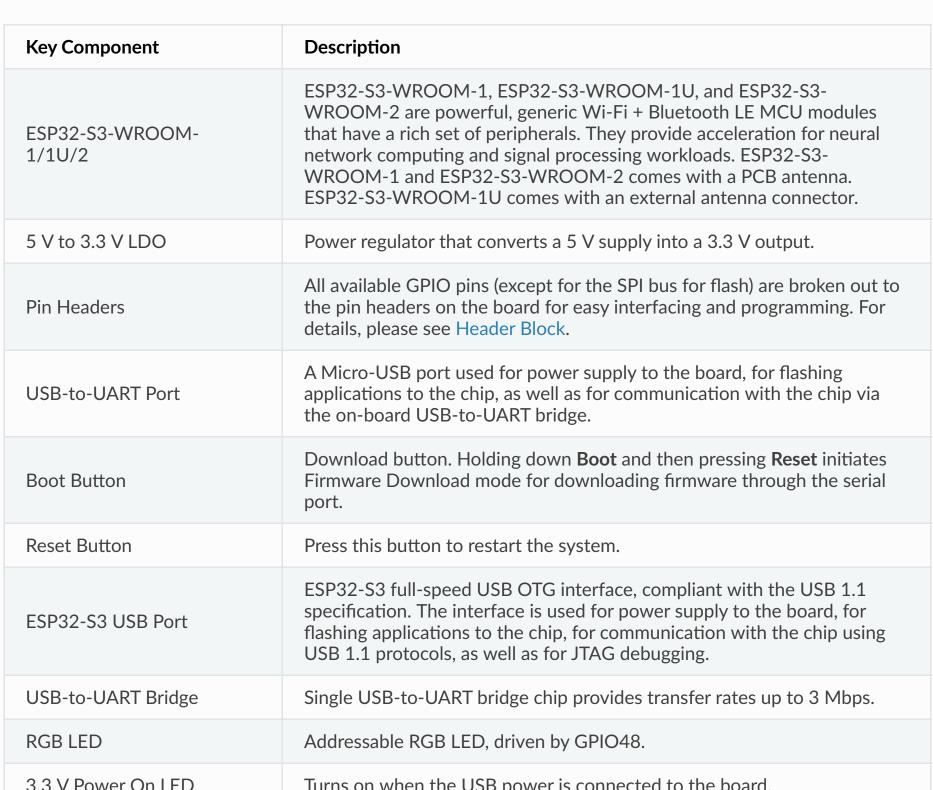
USB-to-UART

ESP32-S3 USB Port

USB-to-UART Port

Reset Button

3.3 V Power On LED



Hardware Setup

default.

the needed data lines nor work for programming the boards.

Software Setup Please proceed to Get Started, where Section Installation will quickly help you set up the

development environment and then flash an application example onto your board.

Connect the board with the computer using USB-to-UART Port. Connection using ESP32-S3 USB

Port is not fully implemented in software. In subsequent steps, USB-to-UART Port will be used by

N8R8

Contents and Packaging

ESP32-S3-DevKitC-1-ESP32-S3-WROOM-1-8 MB QD 2 MB QD 3.3 V N8R2 N8R2 ESP32-S3-WROOM-1-ESP32-S3-DevKitC-1-8 MB QD 8 MB OT 3.3 V

N8R8

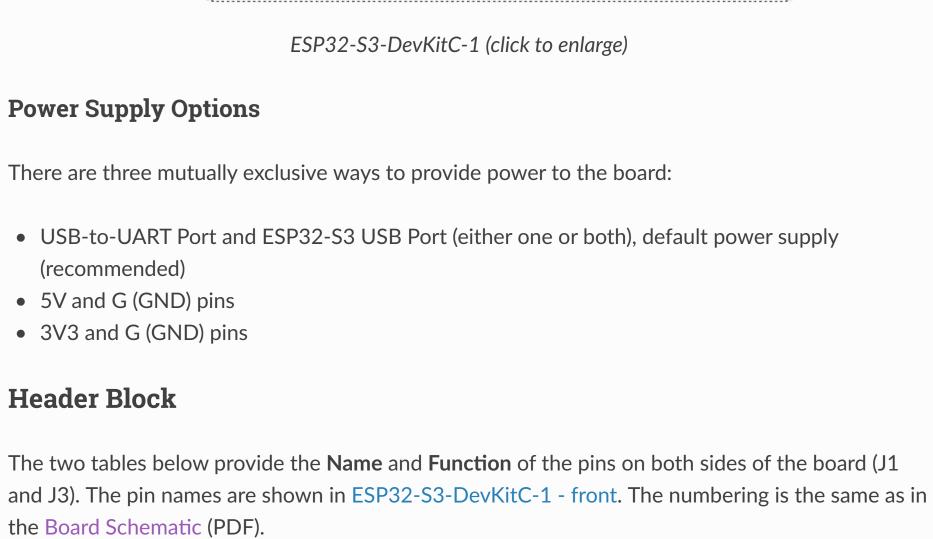
11201101	1110100				
ESP32-S3-DevKitC-1- N32R8V	ESP32-S3-WROOM-2- N32R8V	32 MB OT	8 MB OT	1.8 V	
ESP32-S3-DevKitC- 1U-N8	ESP32-S3-WROOM- 1U-N8	8 MB QD	_	3.3 V	
ESP32-S3-DevKitC- 1U-N8R2	ESP32-S3-WROOM- 1U-N8R2	8 MB QD	2 MB QD	3.3 V	
ESP32-S3-DevKitC- 1U-N8R8	ESP32-S3-WROOM- 1U-N8R8	8 MB QD	8 MB OT	3.3 V	
Note					
In the table above, QD stands for Quad SPI and OT stands for Octal SPI.					
Retail Orders					
If you order a few samples, each board comes in an individual package in either antistatic bag or any packaging depending on your retailer.					
For retail orders, please go to https://www.espressif.com/en/contact-us/get-samples.					

For wholesale orders, please go to https://www.espressif.com/en/contact-us/sales-questions. **Hardware Reference**

Power Supply/ Programming

Wholesale Orders

RGB LED



6 6 7 7 15 8

J1

No.

4

5

9

10

Name

4

5

16

17

37

36

35

0

45

11

12

13

14

15

I/O/T

I/O/T

I/O/T

I/O/T

I/O/T

Type ¹

I/O/T

I/O/T

I/O/T

I/O/T

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I/O/T

GPIO48, SPICLK_N, SUBSPICLK_N_DIFF, RGB LED 16 48 I/O/T GPIO47, SPICLK_P, SUBSPICLK_P_DIFF 47 17 I/O/T RTC_GPIO21, GPIO21 21 I/O/T 18 RTC_GPIO20, GPIO20, U1CTS, ADC2_CH9, CLK_OUT1, USB_D+ 20 I/O/T 19 RTC_GPIO19, GPIO19, U1RTS, ADC2_CH8, CLK_OUT2, USB_D-19 I/O/T 20 21 G Ground 22 G G Ground [1]: P: Power supply; I: Input; O: Output; T: High impedance.

SPIDQS, GPIO37, FSPIQ, SUBSPIQ

SPIIO6, GPIO35, FSPID, SUBSPID

RTC_GPIO0, GPIO0

GPIO45

SPIIO7, GPIO36, FSPICLK, SUBSPICLK

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Built with Sphinx using a theme based on Read the Docs Sphinx Theme.

5 V to 3.3 V LDO Pin Headers ESP32-S3-DevKitC-1 - front The key components of the board are described in a counter-clockwise direction.

3.3 V Power On LED Turns on when the USB power is connected to the board. • Note For boards with ESP32-S3-WROOM-2 modules, the pins GPIO35, GPIO36 and GPIO37 are used for the internal communication between ESP32-S3 and SPI flash/PSRAM memory, thus not availablt for external use.

Start Application Development Before powering up your board, please make sure that it is in good condition with no obvious signs of damage. **Required Hardware** • ESP32-S3-DevKitC-1 USB 2.0 cable (Standard-A to Micro-B) Computer running Windows, Linux, or macOS

• Note Be sure to use an appropriate USB cable. Some cables are for charging only and do not provide

Ordering Information The development board has a variety of variants to choose from, as shown in the table below. **Ordering Code** Module Integrated Flash **PSRAM SPI Voltage** ESP32-S3-DevKitC-1-ESP32-S3-WROOM-1-8 MB QD 3.3 V

ESP32-S3-DevKitC-1- N16R8V	ESP32-S3-WROOM-2- N16R8V	16 MB OT	8 MB OT	1.8 V	
ESP32-S3-DevKitC-1- N32R8V	ESP32-S3-WROOM-2- N32R8V	32 MB OT	8 MB OT	1.8 V	
ESP32-S3-DevKitC- 1U-N8	ESP32-S3-WROOM- 1U-N8	8 MB QD	_	3.3 V	
ESP32-S3-DevKitC- 1U-N8R2	ESP32-S3-WROOM- 1U-N8R2	8 MB QD	2 MB QD	3.3 V	
ESP32-S3-DevKitC- 1U-N8R8	ESP32-S3-WROOM- 1U-N8R8	8 MB QD	8 MB OT	3.3 V	
Note					
In the table above, QD stands for Quad SPI and OT stands for Octal SPI.					
Retail Orders					

Block Diagram The block diagram below shows the components of ESP32-S3-DevKitC-1 and their interconnections.

If you order in bulk, the boards come in large cardboard boxes.

USB-to-UART Bridge

ESP32-S3-WROOM -1/1U/2 Module 3.3 V LDO Header Block x 2 ESP32-S3 USB Port ESP32-S3-DevKitC-1

3V3 3.3 V power supply 1 3V3 3.3 V power supply 2 Р 1 ΕN RST 3

Function

18 I/O/T 11 RTC_GPIO18, GPIO18, U1RXD, ADC2_CH7, CLK_OUT3 DTC CDIOO CDIOO TOLICLIO ADC1 CLIZ CLIDCDICC1

RTC_GPIO4, GPIO4, TOUCH4, ADC1_CH3

RTC_GPIO5, GPIO5, TOUCH5, ADC1_CH4

RTC_GPIO6, GPIO6, TOUCH6, ADC1_CH5

RTC_GPIO7, GPIO7, TOUCH7, ADC1_CH6

RTC_GPIO17, GPIO17, U1TXD, ADC2_CH6

RTC_GPIO15, GPIO15, U0RTS, ADC2_CH4, XTAL_32K_P

RTC_GPIO16, GPIO16, U0CTS, ADC2_CH5, XTAL_32K_N

12	8	I/O/T	RTC_GPIO8, GPIO8, TOUCH8, ADC1_CH7, SUBSPICS1
13	3	I/O/T	RTC_GPIO3, GPIO3, TOUCH3, ADC1_CH2
14	46	I/O/T	GPIO46
15	9	I/O/T	RTC_GPIO9, GPIO9, TOUCH9, ADC1_CH8, FSPIHD, SUBSPIHD
16	10	I/O/T	RTC_GPIO10, GPIO10, TOUCH10, ADC1_CH9, FSPICS0, FSPIIO4, SUBSPICS0
17	11	I/O/T	RTC_GPIO11, GPIO11, TOUCH11, ADC2_CH0, FSPID, FSPIIO5, SUBSPID
18	12	I/O/T	RTC_GPIO12, GPIO12, TOUCH12, ADC2_CH1, FSPICLK, FSPIIO6, SUBSPICLK
19	13	I/O/T	RTC_GPIO13, GPIO13, TOUCH13, ADC2_CH2, FSPIQ, FSPIIO7, SUBSPIQ
20	14	I/O/T	RTC_GPIO14, GPIO14, TOUCH14, ADC2_CH3, FSPIWP, FSPIDQS, SUBSPIWP
21	5V	Р	5 V power supply
22	G	G	Ground
J3	G	G	Ground
	G Name	Туре	Function
J3			
J3 No.	Name	Туре	Function
J3 No. 1	Name G	Type G	Function Ground
No. 1 2	Name G TX	Type G I/O/T	Function Ground U0TXD, GPIO43, CLK_OUT1
No. 1 2 3	Name G TX RX	Type G I/O/T I/O/T	Function Ground U0TXD, GPIO43, CLK_OUT1 U0RXD, GPIO44, CLK_OUT2
J3 No. 1 2 3 4	Name G TX RX	Type G I/O/T I/O/T	Function Ground U0TXD, GPIO43, CLK_OUT1 U0RXD, GPIO44, CLK_OUT2 RTC_GPIO1, GPIO1, TOUCH1, ADC1_CH0
J3 No. 1 2 3 4 5	Name G TX RX 1	Type G I/O/T I/O/T I/O/T	Function Ground U0TXD, GPIO43, CLK_OUT1 U0RXD, GPIO44, CLK_OUT2 RTC_GPIO1, GPIO1, TOUCH1, ADC1_CH0 RTC_GPIO2, GPIO2, TOUCH2, ADC1_CH1
J3 No. 1 2 3 4 5	Name G TX RX 1 2 42	Type G I/O/T I/O/T I/O/T I/O/T	Function Ground U0TXD, GPIO43, CLK_OUT1 U0RXD, GPIO44, CLK_OUT2 RTC_GPIO1, GPIO1, TOUCH1, ADC1_CH0 RTC_GPIO2, GPIO2, TOUCH2, ADC1_CH1 MTMS, GPIO42
J3 No. 1 2 3 4 5 6 7	Name G TX RX 1 2 42 41	Type G I/O/T I/O/T I/O/T I/O/T I/O/T	Function Ground U0TXD, GPIO43, CLK_OUT1 U0RXD, GPIO44, CLK_OUT2 RTC_GPIO1, GPIO1, TOUCH1, ADC1_CH0 RTC_GPIO2, GPIO2, TOUCH2, ADC1_CH1 MTMS, GPIO42 MTDI, GPIO41, CLK_OUT1

For description of function names, please refer to Chip Datasheet (PDF). **Pin Layout SPRESSIF** ESP32-S3-DevKitC-1

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─ PWM Capable Pin ESP32-S3 Specs GPIOX GPIO Input and Output JTAG/USB JTAG for Debugging and USB 32-bit Xtensa® dual-core @240MHz ADCX_CH Analog-to-Digital Converter Wi-Fi IEEE 802.11 b/g/n 2.4GHz + BLE 5 Mesh TOUCHX Touch Sensor Input Channel 512 KB SRAM (16 KB SRAM in RTC) THER Other Related Functions 384 KB ROM SERIAL Serial for Debug/Programmin STRAP Strapping Pin Functions 45 GPIOs, 4x SPI, 3x UART, 2x I2C, RTC Power Domain (VDD3P3_RTC) 14x Touch, 2x I2S, RMT, LED PWM, USB-OTG, MISC Miscellaneous/SPI functions TWAI®, 2x 12-bit ADC, 1x LCD interface, DVP CLK_OUTX Clock Output PWD Power Rails (3V3 and 5V) ESP32-S3-DevKitC-1 Pin Layout (click to enlarge) **Hardware Revision Details** This is the first revision of this board released. **Related Documents** • ESP32-S3 Datasheet (PDF) • ESP32-S3-WROOM-1 & ESP32-S3-WROOM-1U Datasheet (PDF) ESP32-S3-WROOM-2 Datasheet (PDF) ESP32-S3-DevKitC-1 Schematic (PDF) ESP32-S3-DevKitC-1 PCB layout (PDF) • ESP32-S3-DevKitC-1 Dimensions (PDF) • ESP32-S3-DevKitC-1 Dimensions source file (DXF) - You can view it with Autodesk Viewer online For further design documentation for the board, please contact us at sales@espressif.com. Provide feedback about this document