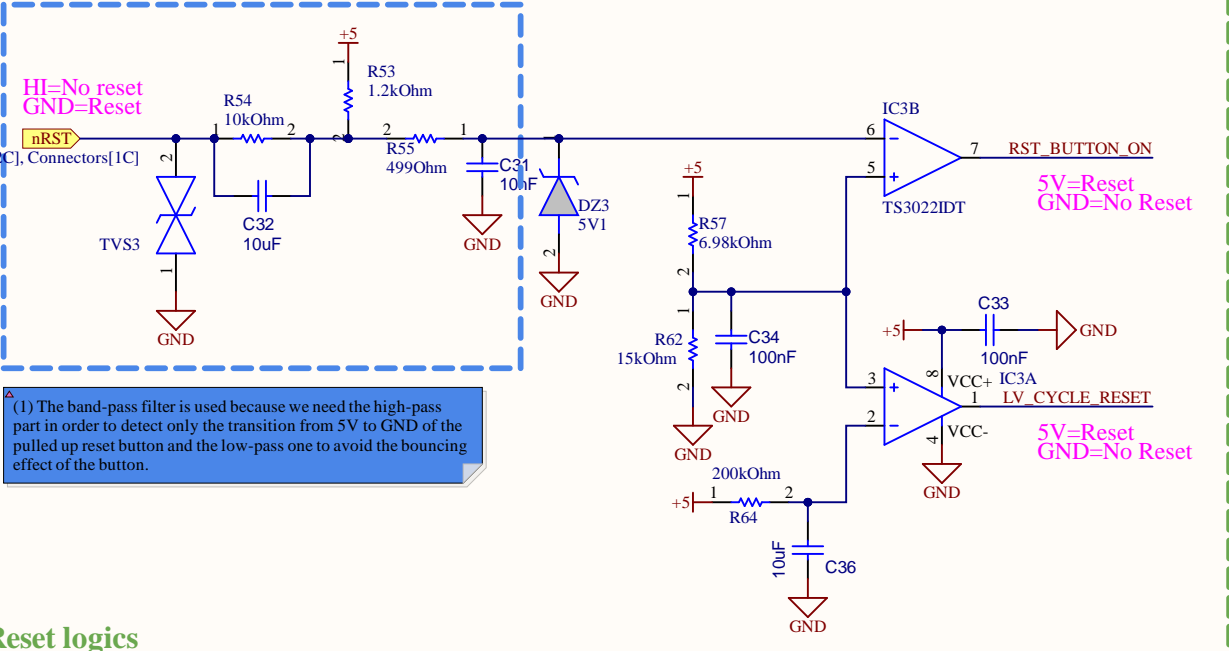
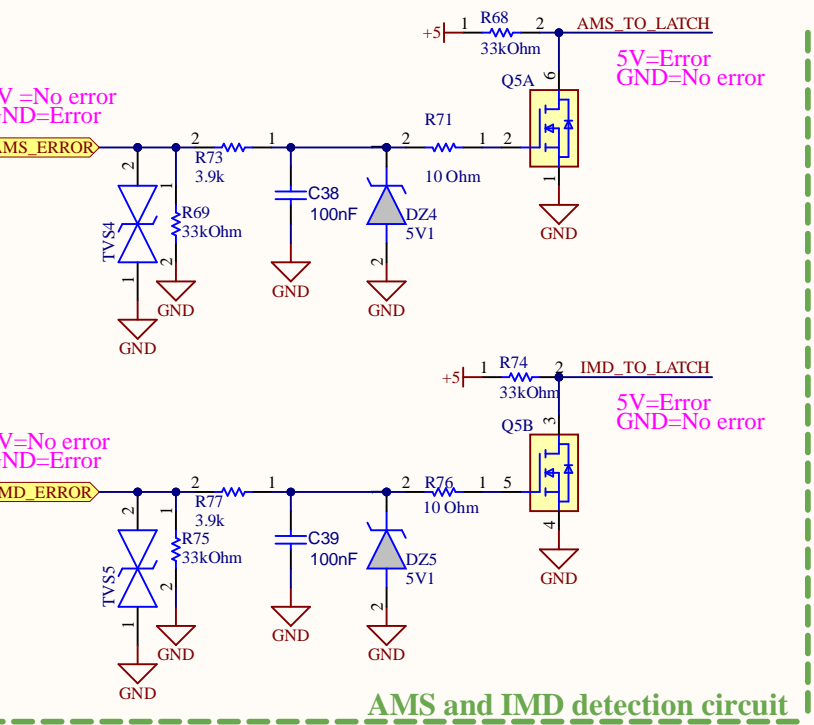


Title			Revision	
TLB_battery			V3.1	
Size	Number		Revision	
A4				
Date:	7/04/2024		Sheet of	
File:	Block scheme.SchDoc		Drawn By:	

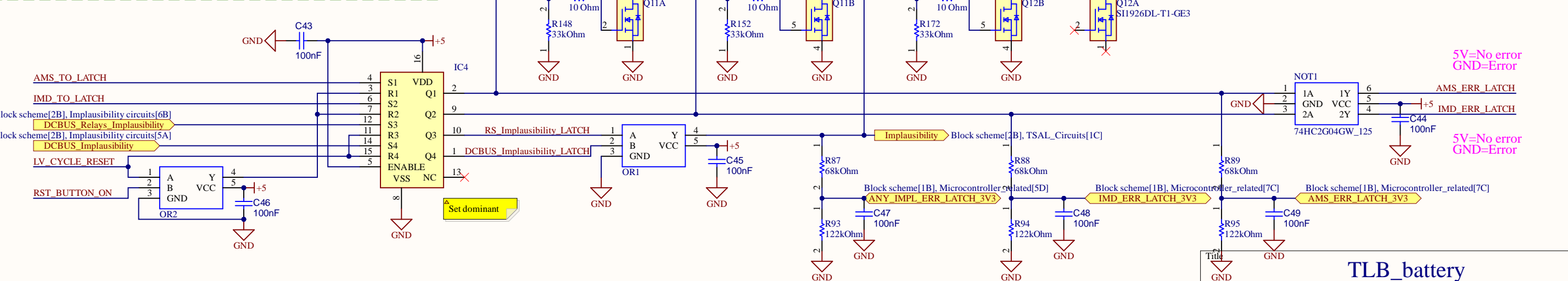
Band-pass filter (1)



Reset logics

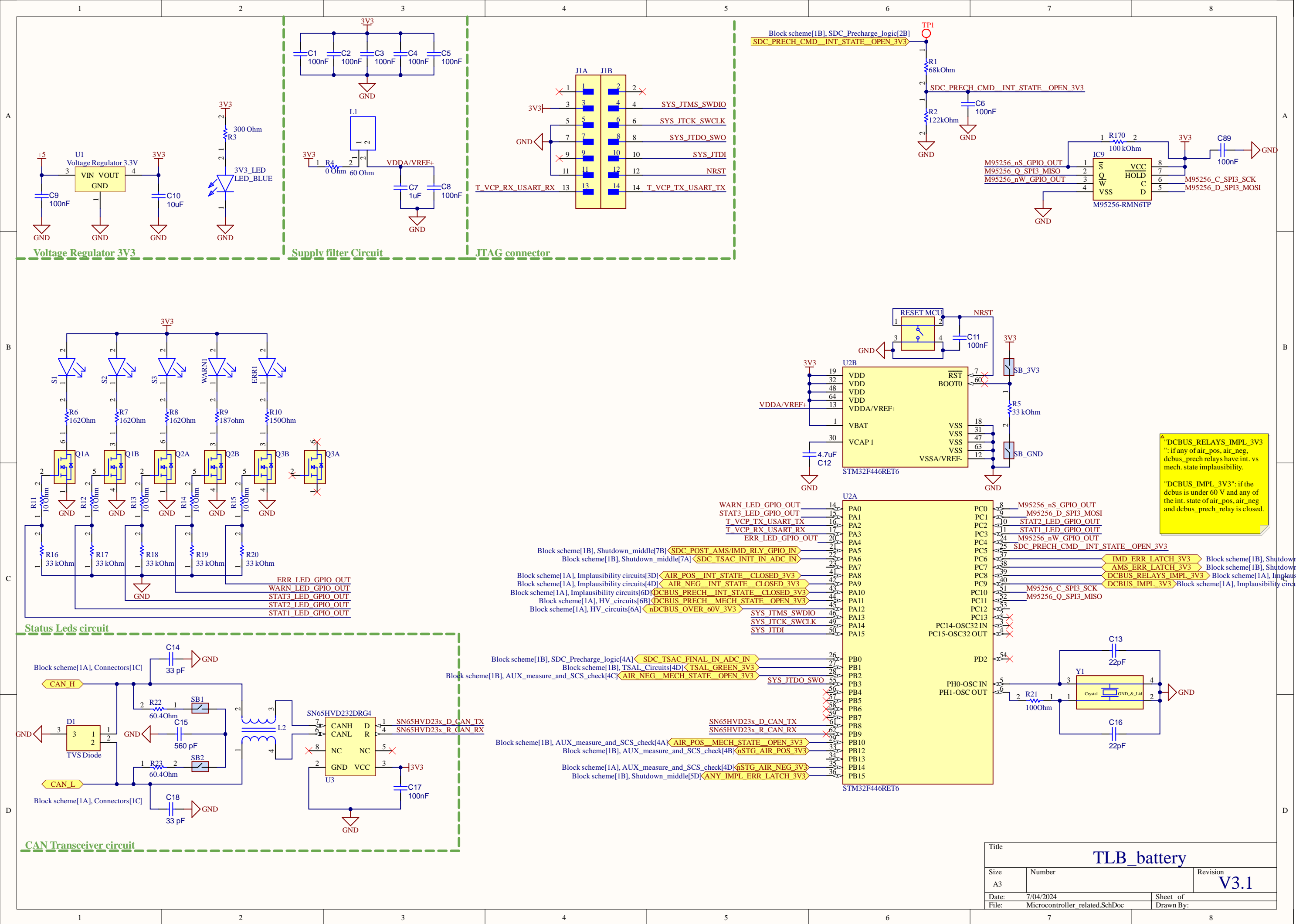


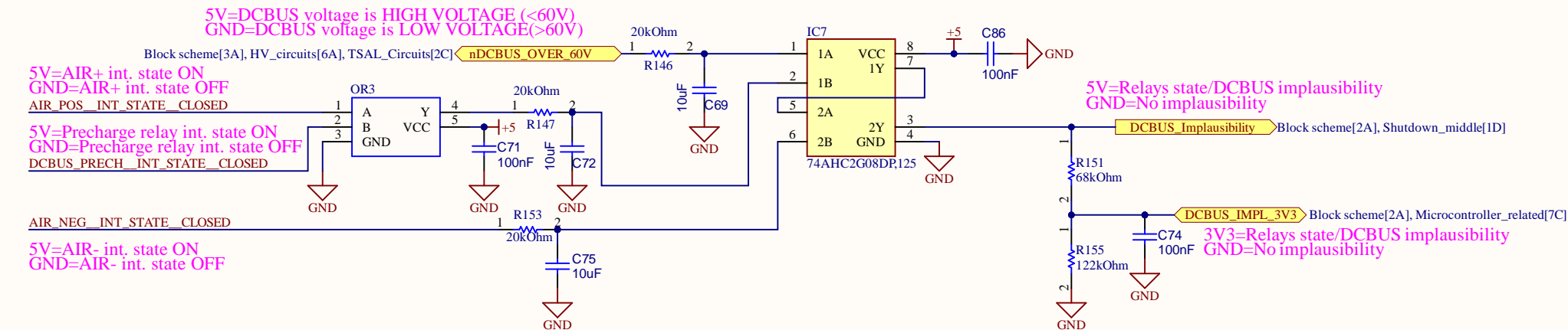
AMS and IMD detection circuit



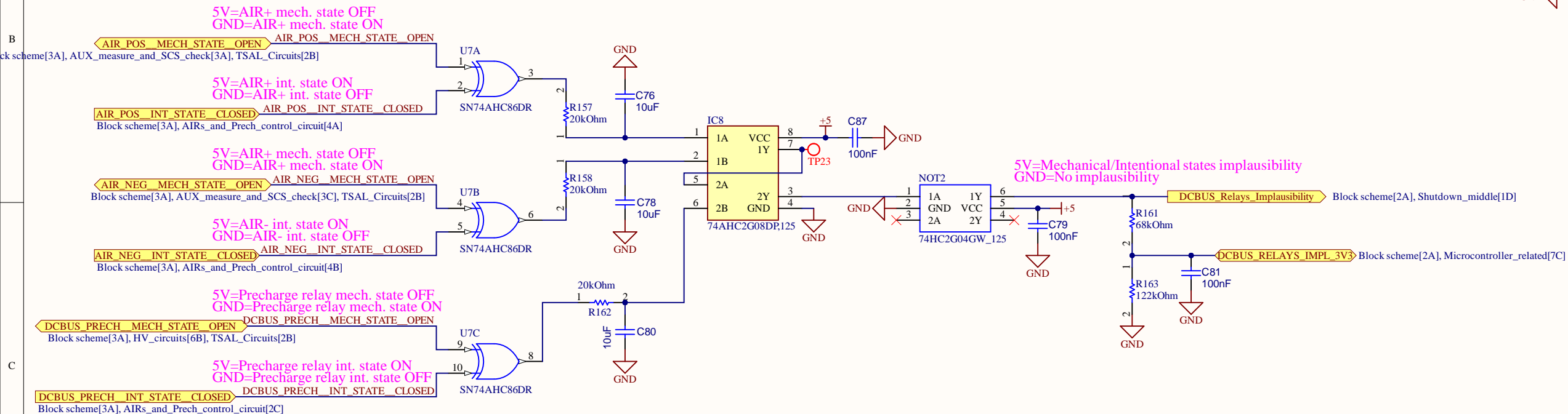
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Size	A3	Number	Revision	
Date:		7/04/2024	Sheet of	
File:		Shutdown_middle.SchDoc	Drawn By:	

V3.1

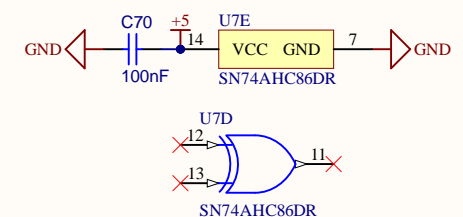
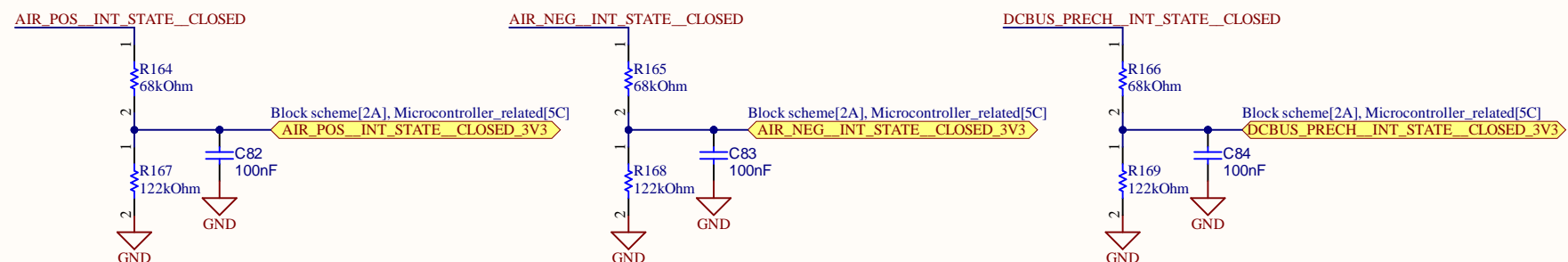




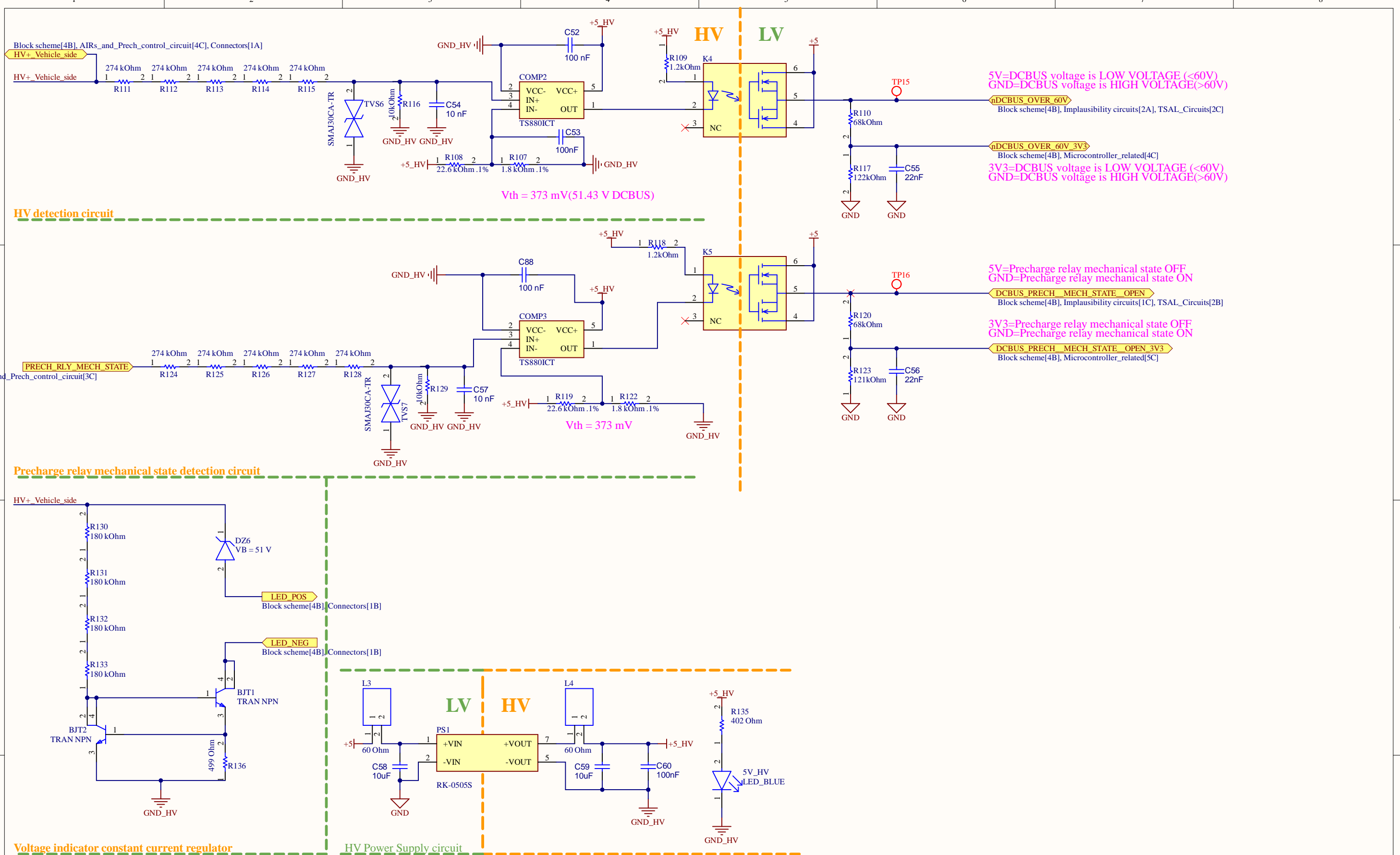
DCBUS voltage implausibility detection circuit



Relays state implausibility detection circuit



Title			TLB_battery	
Size	A3	Number	Revision	
Date:	7/04/2024	Sheet of	V3.1	
File:	Implausibility circuits.SchDoc	Drawn By:		



Title			
TLB_battery			
Size	Number	Revision	
A3		V3.1	
Date:	7/04/2024	Sheet of	
File:	HV_circuits.SchDoc	Drawn By:	

