

# Secure Compilation: Formal Foundations and (Some) Applications

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Marco Patrignani<sup>1</sup>

03 April 2024

# **Who Am I ?**

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# Marco Patrignani



Bsc, Msc



Postdoc



JRGL



PhD



VAP

Asst. Prof.



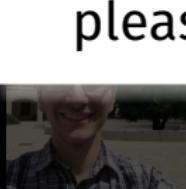
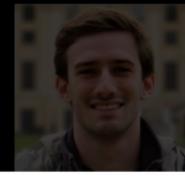
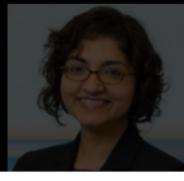
# Special Thanks to:

(wrt the contents of this talk)



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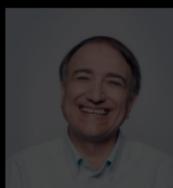
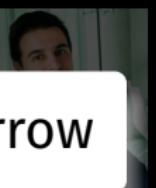
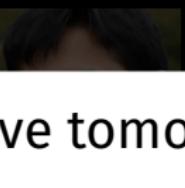
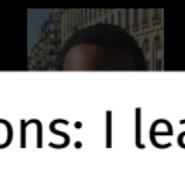
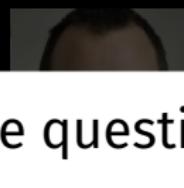
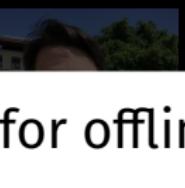


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for offline questions: I leave tomorrow



# **Foundations of Secure Compilation**

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# Programming Languages: Pros and Cons

Good PLs (, , , , ...) provide:

- helpful **abstractions** to write **secure** code

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Good PLs (, , , , ...) provide:

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but

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# Programming Languages: Pros and Cons

Good PLs (, , , , ...) provide:

- helpful **abstractions** to write **secure** code

but

- when compiled (`[[·]]`) and **linked** with adversarial target code
- these abstractions are **NOT** enforced

# Secure Compilation: Example

ChaCha20

Poly1305

...

F\*

HAACL\*. Zinzindohouè *et al.*, CCS'17

Asm

[[ChaCha20]]

[[Poly1305]]

[[...]]

# Secure Compilation: Example

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[[ChaCha20]]

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160x C/C++ code (unsafe)

# Secure Compilation: Example

Preserve the security of

ChaCha20

Poly1305

...

F\*

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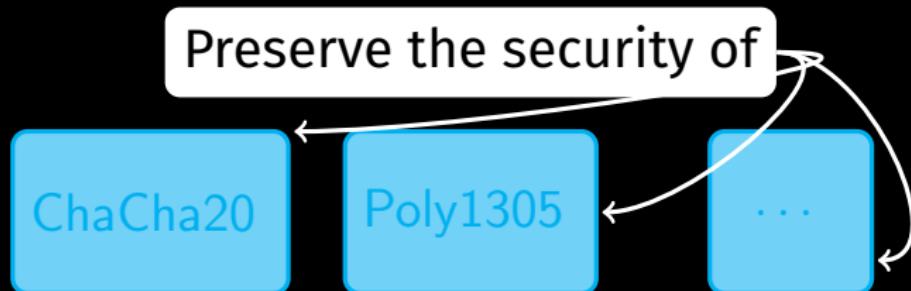
[[ChaCha20]]

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[[...]]



# Secure Compilation: Example



F\*  
HACL\*. Zinzindohouè et al., CCS'17

Asm



when interoperating with

# Secure Compilation: Example

Correct compilation

ChaCha20

Poly1305

...

F\*

HAACL\*. Zinzindohouè et al., CCS'17

Asm

[[ChaCha20]]

[[Poly1305]]

[[...]]

# Secure Compilation: Example

Secure compilation

ChaCha20

Poly1305

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F\*

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[[ChaCha20]]

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# Secure Compilation: Example

Enable source-level security reasoning

ChaCha20

Poly1305

...

F\*

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Asm

[[ChaCha20]]

[[Poly1305]]

[[...]]



## Quest for Foundations

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What does it mean  
for a compiler to  
be secure?

## Quest for Foundations

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What does it mean  
for a compiler to  
be secure?

Analogous questions are answered for type  
systems, correct compilation, ...

# Once Upon a Time in Process Algebra

## Secure Implementation of Channel Abstractions

Martín Abadi

[ma@pa.dec.com](mailto:ma@pa.dec.com)

Digital Equipment Corporation  
Systems Research Center

Cédric Fournet

[Cedric.Fournet@inria.fr](mailto:Cedric.Fournet@inria.fr)

INRIA Rocquencourt

Georges Gonthier

[Georges.Gonthier@inria.fr](mailto:Georges.Gonthier@inria.fr)

INRIA Rocquencourt

### Abstract

*Communication in distributed systems often relies on useful abstractions such as channels, remote procedure calls, and remote method invocations. The implementations of these abstractions sometimes provide security properties, in particular through encryption. In this*

spaces are on the same machine, and that a centralized operating system provides security for them. In reality, these address spaces could be spread across a network, and security could depend on several local operating systems and on cryptographic protocols across machines.

For example, when an application requires secure

Challenge: define that their implementation of secure channels via **cryptography** was secure

# Once Upon a Time in Process Algebra

## Fully Abstract Compilation (FAQ)

**Theorem 1** *The compositional translation is fully-abstract, up to observational equivalence: for all join-calculus processes  $P$  and  $Q$ ,*

$$P \approx Q \quad \text{if and only if} \quad \mathcal{E}\text{nv}[\llbracket P \rrbracket] \approx \mathcal{E}\text{nv}[\llbracket Q \rrbracket]$$

*C*

*useful abstractions such as channels, remote procedure calls, and remote method invocations. The implementations of these abstractions sometimes provide security properties, in particular through encryption. In this*

*scenario, the execution of processes could be spread across a network, and security could depend on several local operating systems and on cryptographic protocols across machines.*

*For example, when an application requires secure*

**Challenge:** define that their implementation of secure channels via **cryptography** was secure

# Fully Abstract Compilation Influence

ACM CSUR'19

## Fully Abstract Compilation to JavaScript

J.-Chen<sup>1</sup>, Pierre-Evariste Dagand<sup>2</sup>, Pierre-Yves Strub<sup>1</sup>, Benj<sup>1</sup>,  
... and MSR-INRIA<sup>1</sup>

## Secure Implementations for Typed Session Abstraction

Ricardo Corin<sup>1,2,3</sup>, Pierre-Malo Deniéou<sup>1,2</sup>, Cédric Fournet<sup>1,2</sup>,  
Karthikeyan Bhargavan<sup>1,2</sup>, James Leifer<sup>1</sup>

Amal Ahmed<sup>1</sup>, Matthias Blume<sup>2</sup>,  
Toyota Technological Institute at Chicago  
[amal.blume@ttic.org](mailto:{amal.blume}@ttic.org)

Authentication primitives and their compilation

Martín Abadi<sup>\*</sup>  
Bell Labs Research  
Lucent Technologies

Cédric Fournet  
Microsoft Research

Georges G.  
INRIA Rocquencourt

## On Protection by Layout Randomization

MARTÍN ABADI<sup>1</sup>, Microsoft Research, Silicon Valley,  
Santa Cruz; Collège de France  
GORDON D. PLOTKIN<sup>2</sup>,  
University of Edinburgh

## Beyond Good and Evil

Formalizing the Security Guarantees of Compartmentalizing Compilation

Yannis Juglani<sup>1,2</sup>, Cătălin Hritcu<sup>1</sup>, Arthur Azevedo de Amorim<sup>4</sup>, Boris Eng<sup>1,3</sup>, Benjamin C. Pierce<sup>4</sup>,  
<sup>1</sup>Inria Paris, <sup>2</sup>Université Paris Diderot (Paris 7), <sup>3</sup>Université Paris 8, <sup>4</sup>University of Pennsylvania

## A Secure Compiler for ML Modules

Marco Patrignani, Dave Clarke, and Frank Piessens<sup>\*</sup>

iMinds-DistriNet, Dept. Computer Science  
[{first.last}@mim.distrinet.be](mailto:{first.last}@mim.distrinet.be)

An Equivalence-Preserving CPS Translation  
via Multi-Language Semantics\*

Local Memory via Layout Randomization

Marco Patrignani  
Dept. Computer Science  
and Dave C.

On Modular and Fully-Abstract Compilers

Matthias Blume  
Google  
[blume@google.com](mailto:blume@google.com)

## Fully Abstract Compilation via Universal Embedding\*

Dominique Devriese

Typed Closure Conversion Preserves Observational Equivalence

## Fully-Abstract Compilation by Approximate Back-Translation

Dominique Devriese<sup>1</sup>, Marco Patrignani<sup>2</sup>, Frank Piessens<sup>3</sup>,  
<sup>1</sup>iMinds-DistriNet, Computer Science dept., KU Leuven  
[first.last@cs.kuleuven.be](mailto:first.last@cs.kuleuven.be)

Secure Compilation  
of Object-Oriented Components  
to Protected Module Architectures

Marco Patrignani, Dave Clarke, and Frank Piessens<sup>\*</sup>

Corin Pitcher<sup>1</sup>, Julian Rathke<sup>2</sup>,  
University of Southampton  
Amal Ahmed<sup>3</sup>, Raoul Strackx and Bart Jacobs<sup>4</sup>,  
... and iMinds-D

An Equivalence-Preserving CPS Translation  
via Multi-Language Semantics\*

On Modular and Fully-Abstract Compilers

Marco Patrignani

# Fully Abstract

- FAC: useful for **language expressiveness**  
but complex and with an unclear security implication

CSUR'19

Typed Closure

Authentication

Martín Abadi<sup>\*</sup>  
Bell Labs Research  
Lucent Technologies

Secure  
of Object-C  
o Protected

Marco Patrignani,

iMinds-DistriNet, L

{first, last}arr

ion Abstraction

Cédric Fournet<sup>1,2</sup>  
James Leifer<sup>1</sup>

<sup>3</sup> University of T

x-Translation

Pierce<sup>4</sup>  
sylvania

LL Modules

and Dave Clark

Marco Patrignani  
Dept. Comput.  
and Dave C

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Matthias Blume  
Google  
blume@google.com

7/35

Local Memory via Layout  
Corin Pitcher  
University of Southampton

Secure Compilation to Protected Module Architectures  
Marco Patrignani and Raoul Strackx and Bart Jacobs,<sup>i</sup>  
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Fully Abstract Compilation via Universal Embedding\*

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- FAC: useful for **language expressiveness**  
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- **Challenge:** easier/more efficient/more precise alternatives

CSUR'19

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Fully Abstract Compilation via Universal Embedding\*

On Modular and Fully-Abstract Compil.

Marcos Patrignani

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Google  
blume@google.com

# Fully Abstract

- FAC: useful for **language expressiveness** but complex and with an unclear security implication
- **Challenge:** easier/more efficient/more precise alternatives

preserve classes of  
**(hyper)properties**

Clarkson & Schneider JCS '10

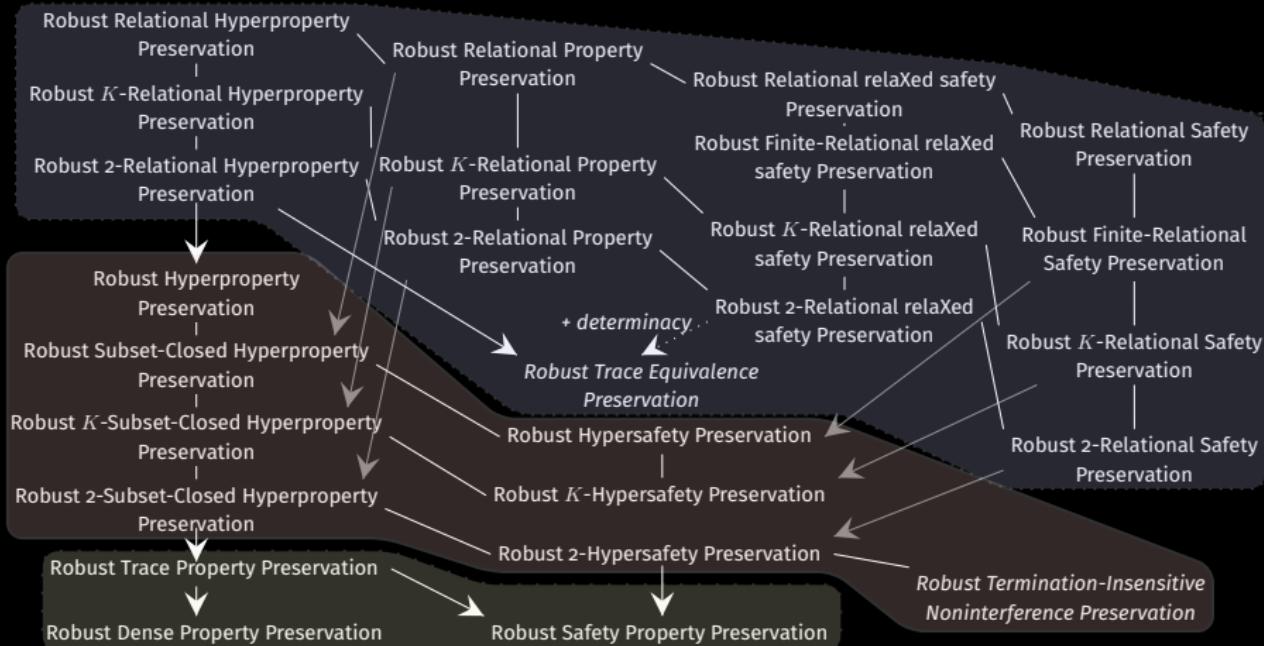
# Robust Compilation (RC) Criteria

CSF'19, ESOP'20, Toplas'21

Relational  
Hyperproperties

Hyperproperties

Trace  
Properties



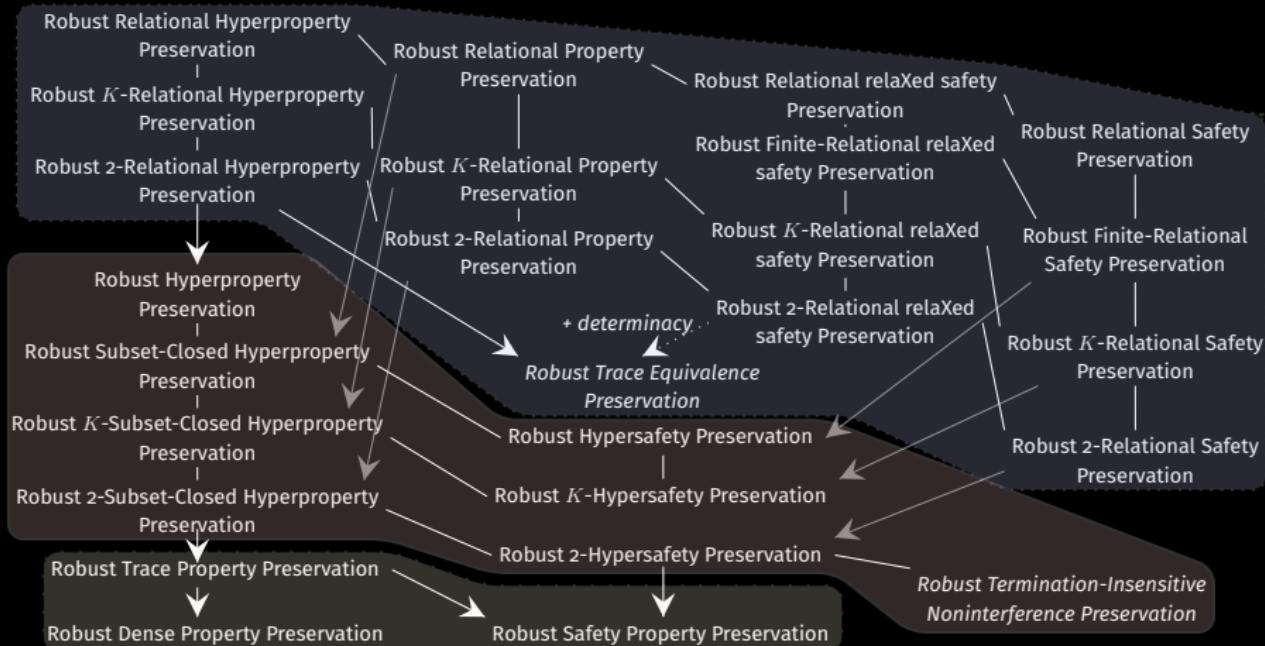
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Tradeoffs for code efficiency, security guarantees, proof complexity

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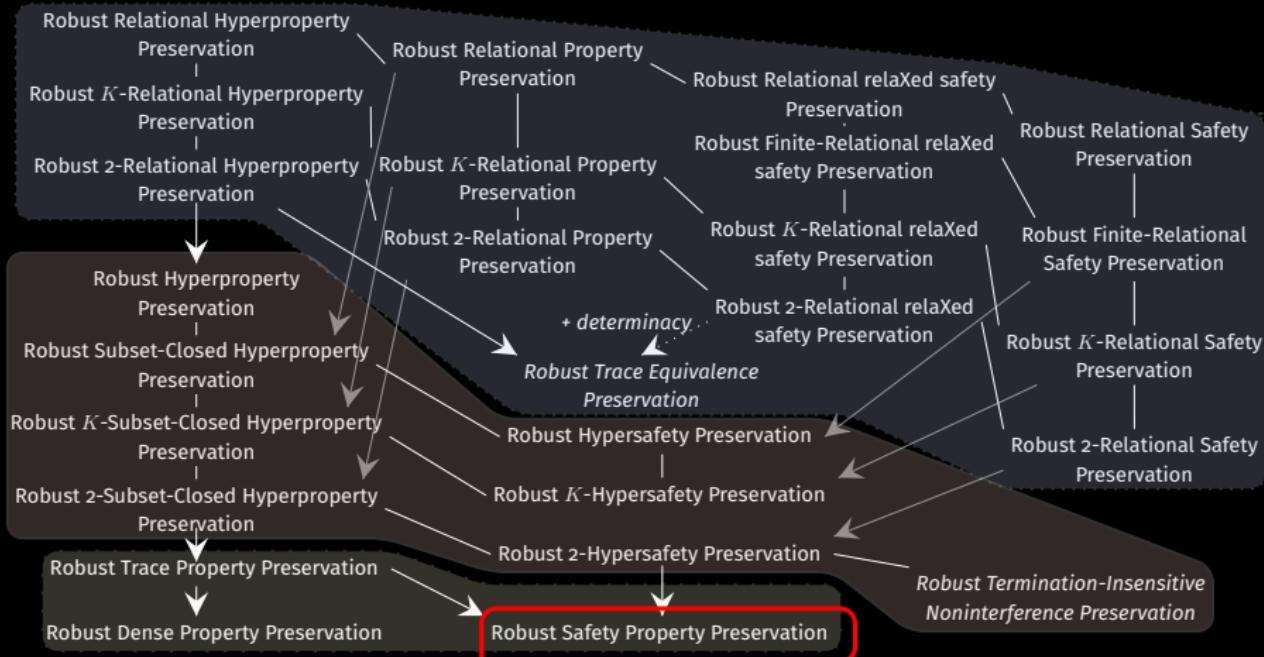
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# Robust Criteria: Intuition

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Each point has two **equivalent** criteria:

- **Property – ful :**
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# Robust Criteria: Intuition

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Each point has two **equivalent** criteria:

- **Property – ful :**
  - + clearly tells what class it preserves
  - harder to prove
- **Property – free :**
  - + **easier** to prove
  - unclear what security classes are preserved

# In Depth Example: RSC

ESOP'19, TOPLAS'21

$\llbracket \cdot \rrbracket = \text{compiler}$      $\llbracket \cdot \rrbracket : \text{RSP} \stackrel{\text{def}}{=}$

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ESOP'19, TOPLAS'21

$\llbracket \cdot \rrbracket = \text{compiler}$      $\llbracket \cdot \rrbracket : \text{RSP} \stackrel{\text{def}}{=} \forall \pi \approx \pi \in \text{Safety}.$   
 $\pi / \pi = \text{set of traces}$

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$$\llbracket \cdot \rrbracket : \text{RSP} \stackrel{\text{def}}{=} \forall \pi \approx \tau \in \text{Safety}. \ \forall P.$$

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$\pi / \tau$  = set of traces

P = partial program

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$t / t$  = trace of events

$\text{if } (\forall A, t.$

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$$\text{if } (\forall A, t. A[P] \rightsquigarrow t \Rightarrow t \in \pi)$$

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$\llbracket \cdot \rrbracket : RSP \stackrel{\text{def}}{=} \forall \pi \approx \pi' \in Safety. \forall P.$

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$m / m'$  = prefix of a trace

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if  $(\forall A, t. A[P] \rightsquigarrow t \Rightarrow t \in \pi)$

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$\llbracket \cdot \rrbracket : RSC \stackrel{\text{def}}{=} \forall P, A, m.$

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$\llbracket \cdot \rrbracket : RSP \stackrel{\text{def}}{=} \forall \pi \approx \pi' \in Safety. \forall P.$

**if** ( $\forall A, t. A[P] \rightsquigarrow t \Rightarrow t \in \pi$ )

**then** ( $\forall A, t. A[\llbracket P \rrbracket] \rightsquigarrow t \Rightarrow t \in \pi$ )

$\llbracket \cdot \rrbracket : RSC \stackrel{\text{def}}{=} \forall P, A, m.$

**if**  $A[\llbracket P \rrbracket] \rightsquigarrow m$

**then**  $\exists A,$

# In Depth Example: RSC

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if  $A[\llbracket P \rrbracket] \rightsquigarrow m$

then  $\exists A, m \approx m.$

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if  $A[\llbracket P \rrbracket] \rightsquigarrow m$

then  $\exists A, m \approx m'. A[P] \rightsquigarrow m$

# Secure Compilation Threat Model

---

- robust, active attacker ( $\forall A$ )

robust safety works, e.g., Swasey *et al.* OOPSLA'17, Sammler *et al.* POPL'20

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- in-language expressible attacker

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robust safety works, e.g., Swasey *et al.* OOPSLA'17, Sammler *et al.* POPL'20

- in-language expressible attacker
- trace-based security behaviour ( $m/m$ )

# Secure Compilation Threat Model

- robust, active attacker ( $\forall A$ )

What can we do with these foundations?

- 

- trace-based security behaviour ( $m/m$ )

20

# Talk Outline

Robust Memory Safety

POPL'23

Robust Cryptographic Constant Time

(wip)

Micro-architectural Attacks (Spectre)

CCS'21

Security Architectures

(e.g., Cheri/ARM Morello, Sancus/Intel SGX, ...) Toplas'15, CSF'21, ...

Mechanise Cryptographic Proofs

CSF'24 + wip

Conclusion

# Robust Memory Safety

POPL'23

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Memarian *et al.* POPL'19, Azevedo de Amorim *et al.* POST'18

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F	F	F	F	F	F	F
---	---	---	---	---	---	---

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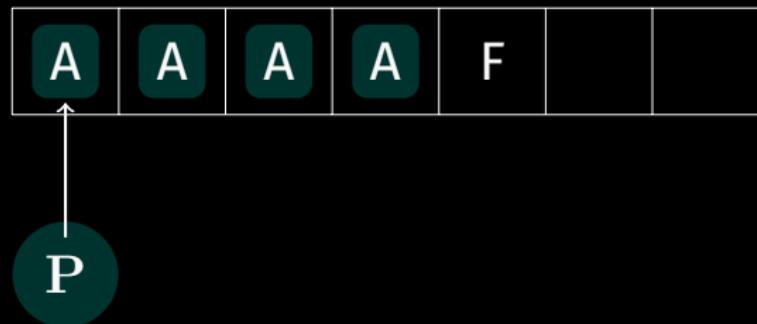


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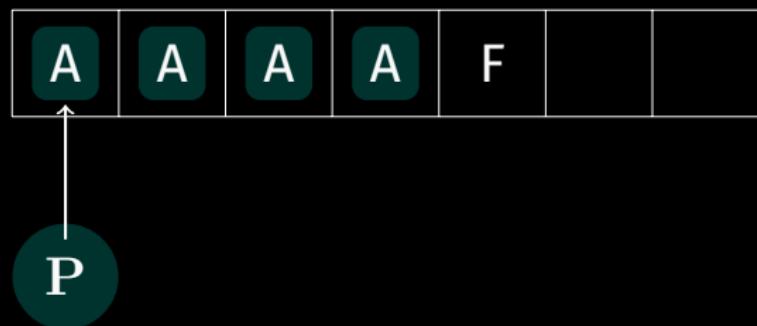


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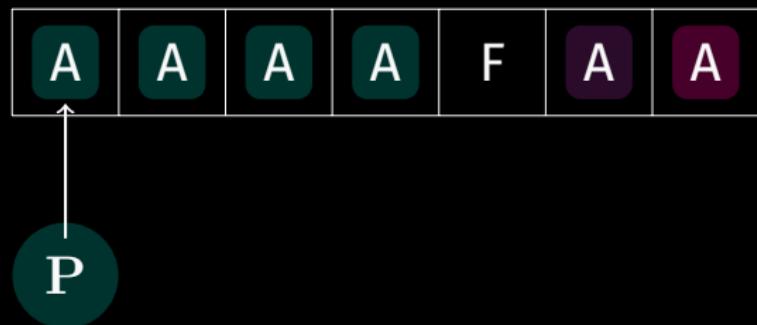


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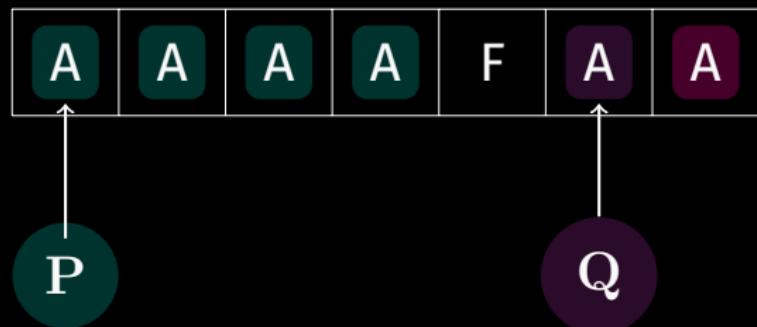


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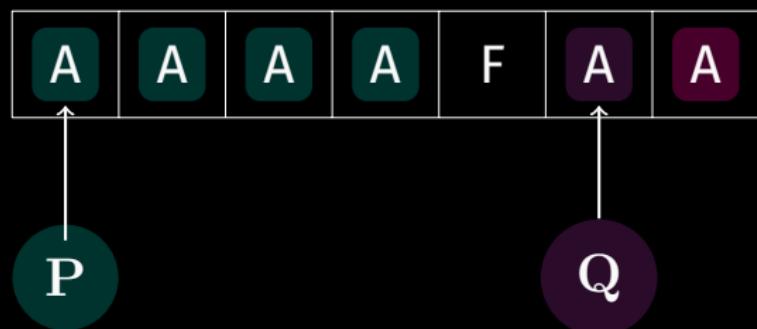


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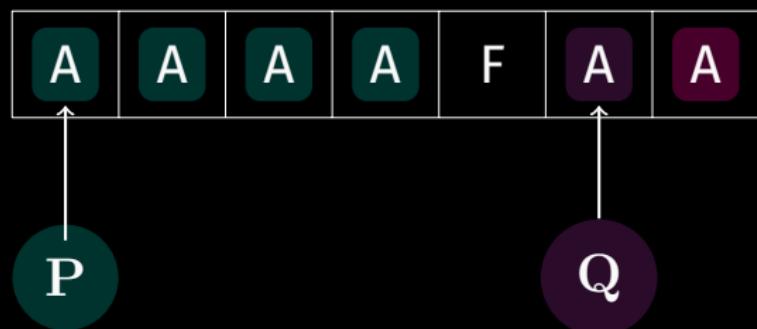
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ok

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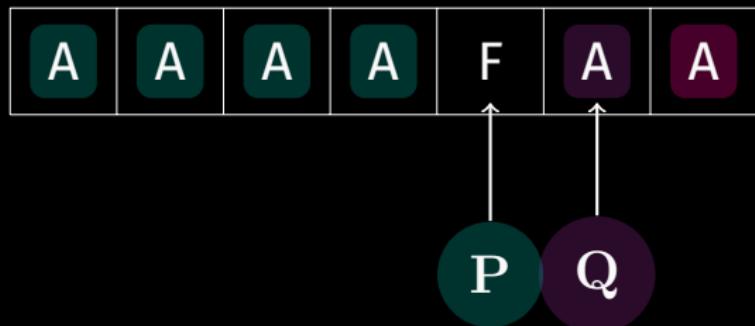
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NO

```
alloc(4)
alloc(1+1)
read(P)
read(Q)
```

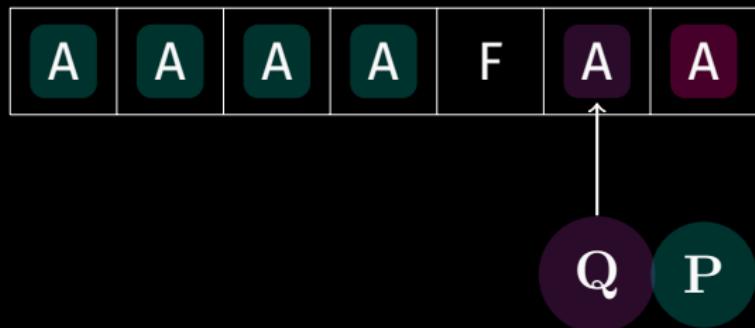


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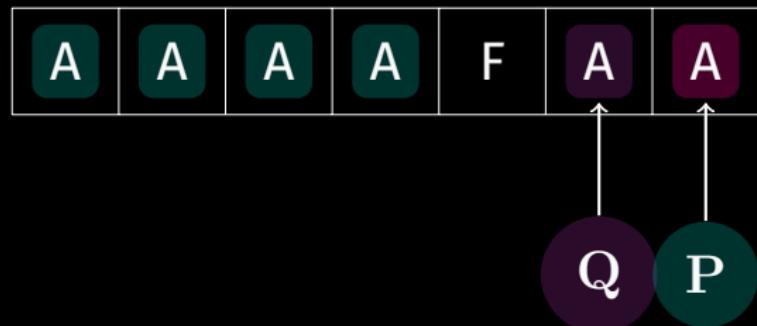
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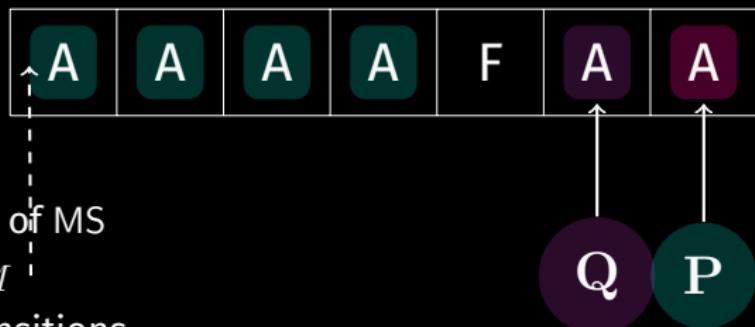
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Monitor encoding of MS  
with state  $M$   
and actions for transitions



# Memory-Safe WebAssembly (MSWAsm)

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  - `handle_add`, `handle_slice`

Watson et al. S&P'15

# Compiling from C to MSWAsm

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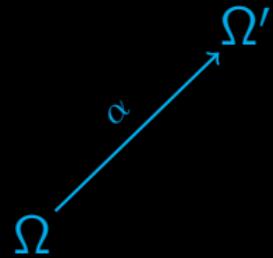
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$\Omega$  = source state

$\alpha/\alpha$  = trace action

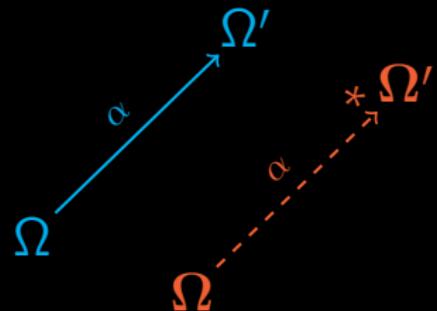


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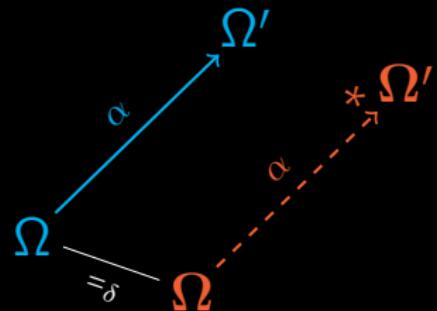
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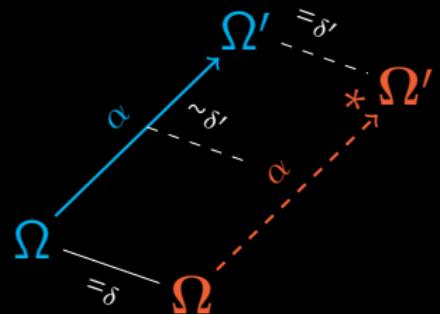
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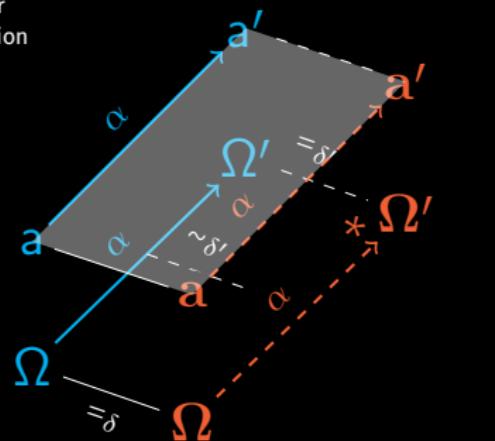
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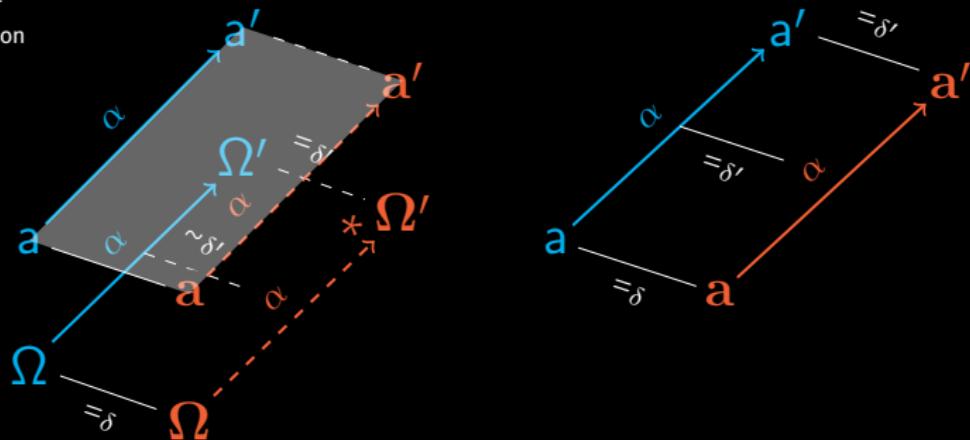
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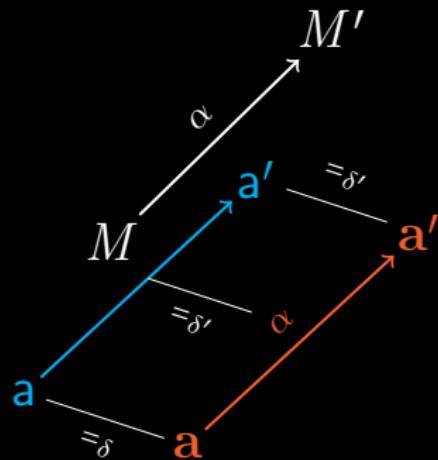
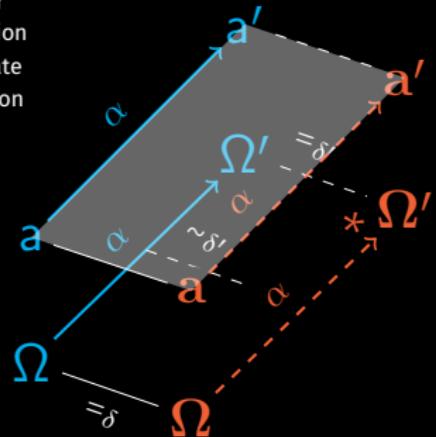
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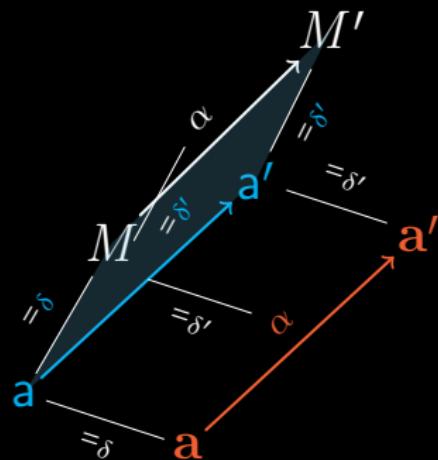
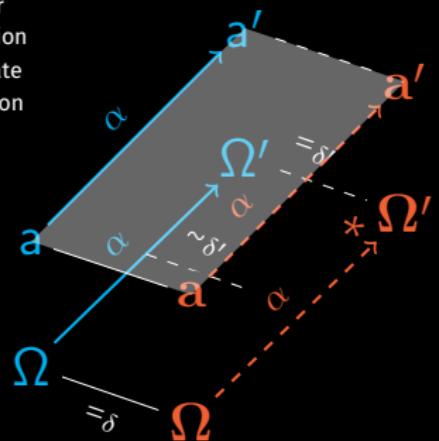
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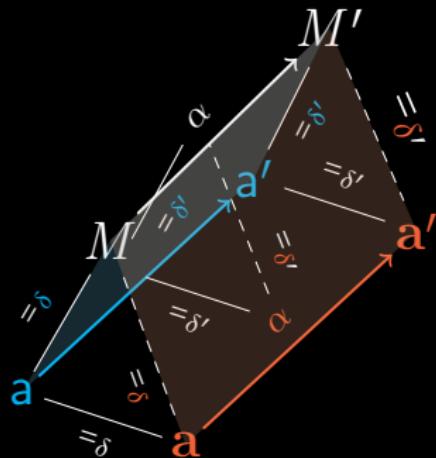
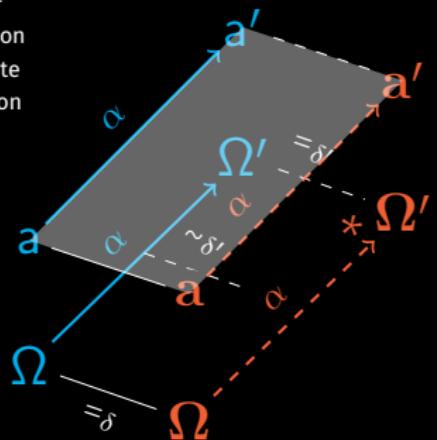
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PRO: proved MS preservation, MS enforcement

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Challenge: how to ensure  $A$  actions do not affect MS?

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# **Robust Cryptographic Constant Time**

(wip)

---

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- (in)formally RCT: ...  
no secret-dependent operations

Bernstein '15, Barbosa *et al.* S&P'21

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- **Challenge:** crypto devs do not know where their code is used
- **Solution:** use a compiler that preserves RCT

# **Micro-architectural Attacks (Spectre)**

---

CCS'21

# Speculative Semantics & SNI

Guarnieri et al. S&P'21

```
void f (int x) ↪ if(x < A.size) {y = B[A[x]]}  
run 1: A.size = 16, A[128] = 3
```

call f 128

# Speculative Semantics & SNI

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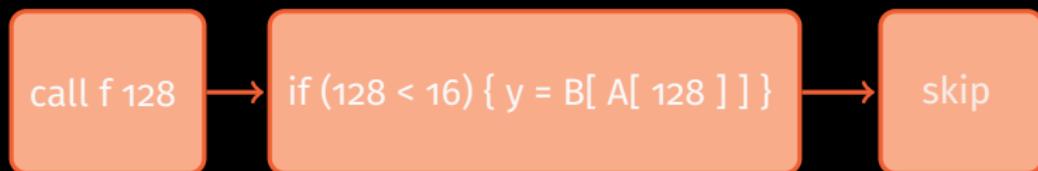
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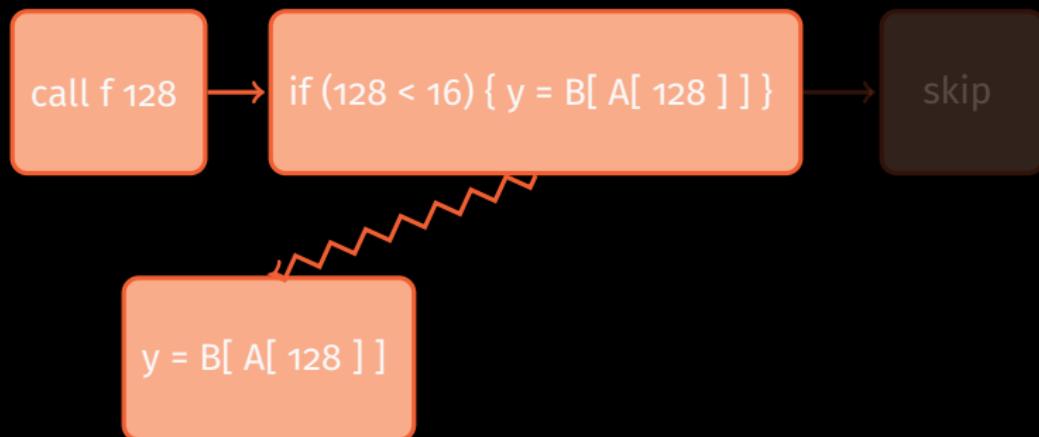
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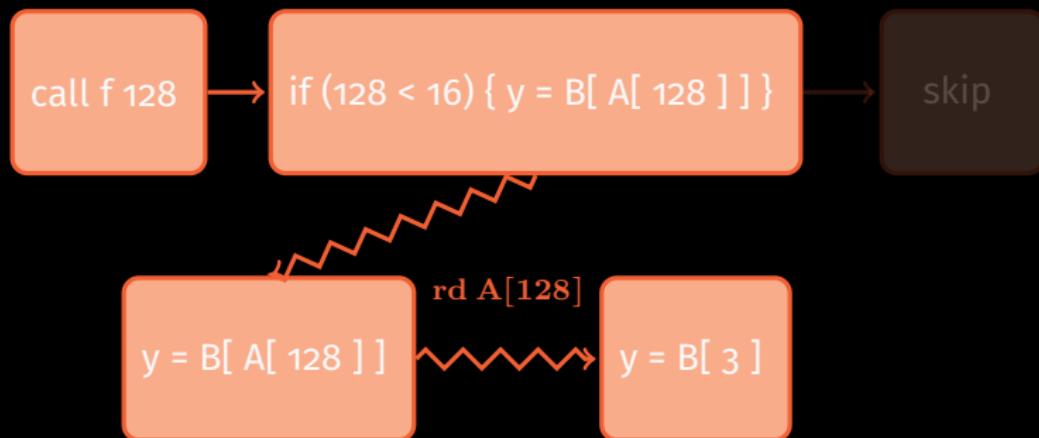
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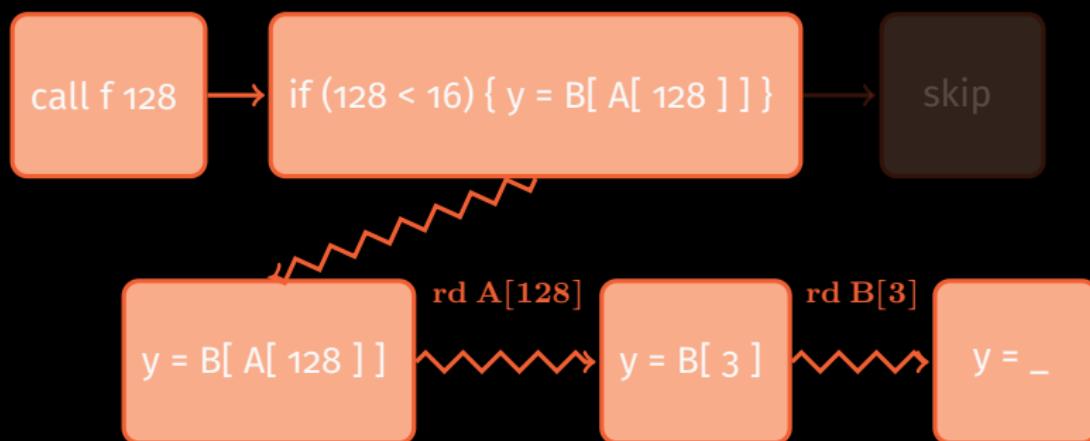
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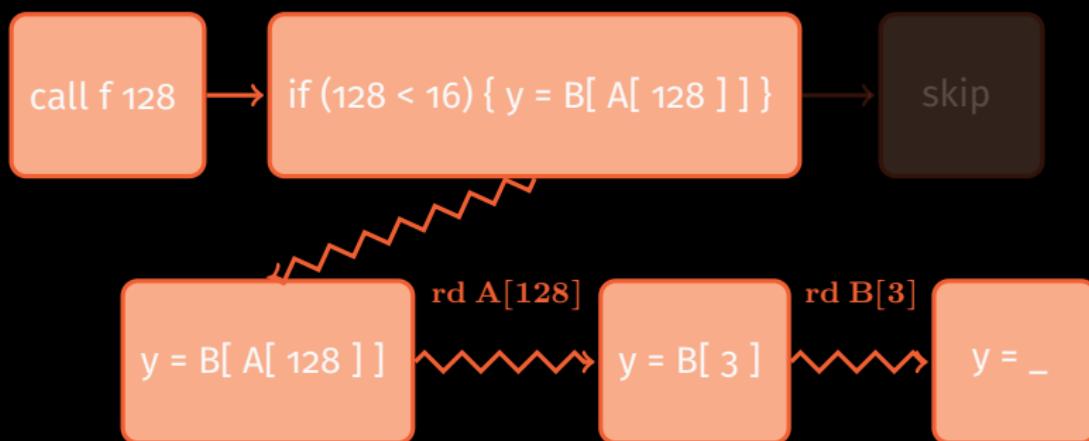
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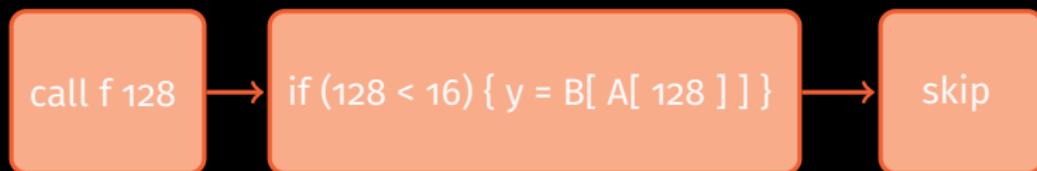
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# Speculative Semantics & SNI

Guarnieri et al. S&P'21

A program is **SNI** ( $\vdash P : \text{SNI}$ ) if, given two runs from low-equivalent states:

- assuming the non-speculative traces are low-equivalent
- then the **speculative traces are also low-equivalent**

call f

trace 1:    rd A[128]  
trace 2:    rd A[128]

rd B[3] different traces  
rd B[7]  $\Rightarrow$  SNI violation

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# Problems Problems Problems ...

Problem: Proving compiler preserves SNI is hard

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**Solution:** overapproximate SNI with a  
novel property: speculative safety (SS)

# Speculative Safety ( $SS$ ): Taint Tracking

void f (int x)  $\mapsto$  if( $x < A.size$ ) { $y = B[A[x]]$ }

only 1 run needed:  $A.size=16$ ,  $A[128]=3$

integrity lattice:  $S \subset U \quad S \sqcap U = S \quad U$  does not flow to  $S$

call f 128

pc : S

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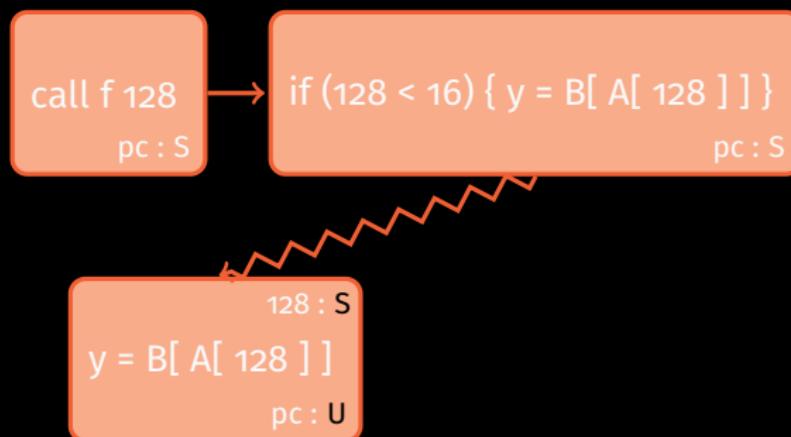


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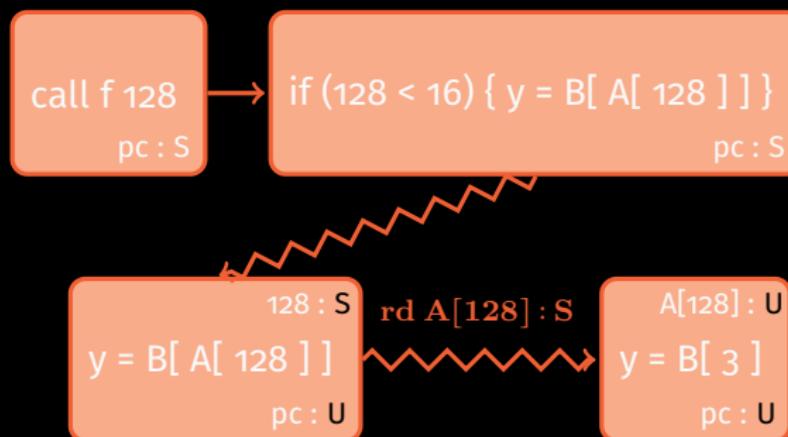


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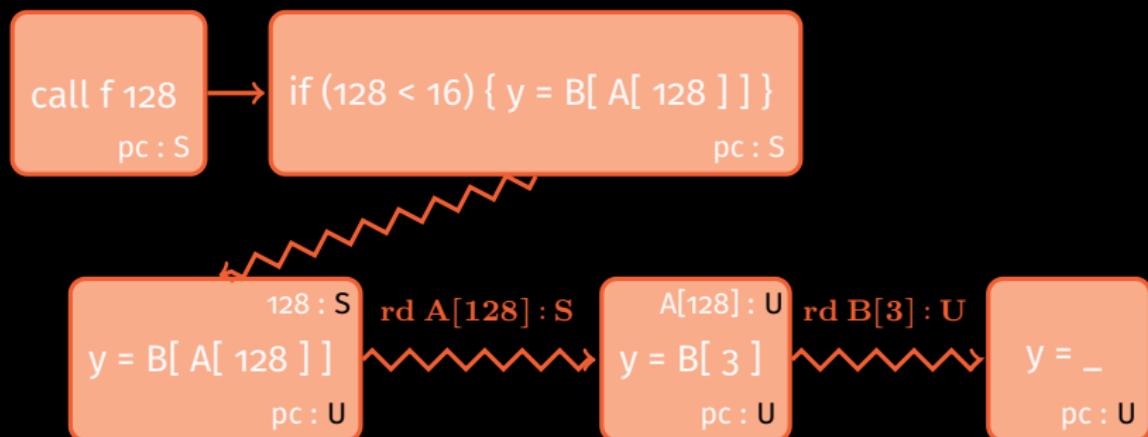


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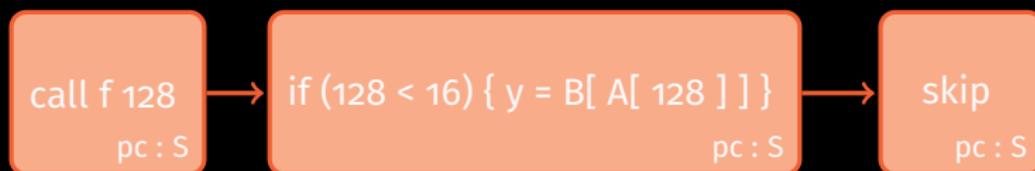


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rd  $A[128] : S$

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A program is  $SS$  ( $\vdash P : SS$ ) if its traces do not contain  $U$  actions

call f 128 → if (128 < 16) { y = B[ A[ 128 ] ] }  
pc : S

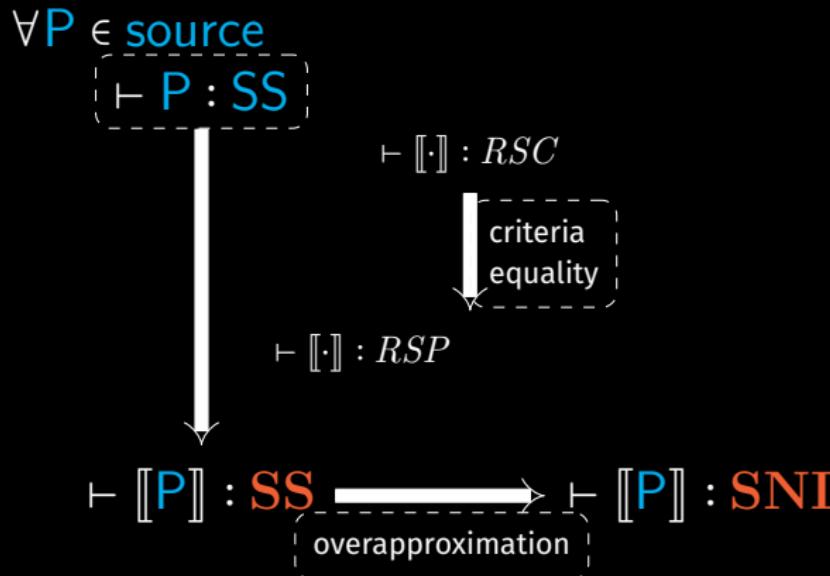
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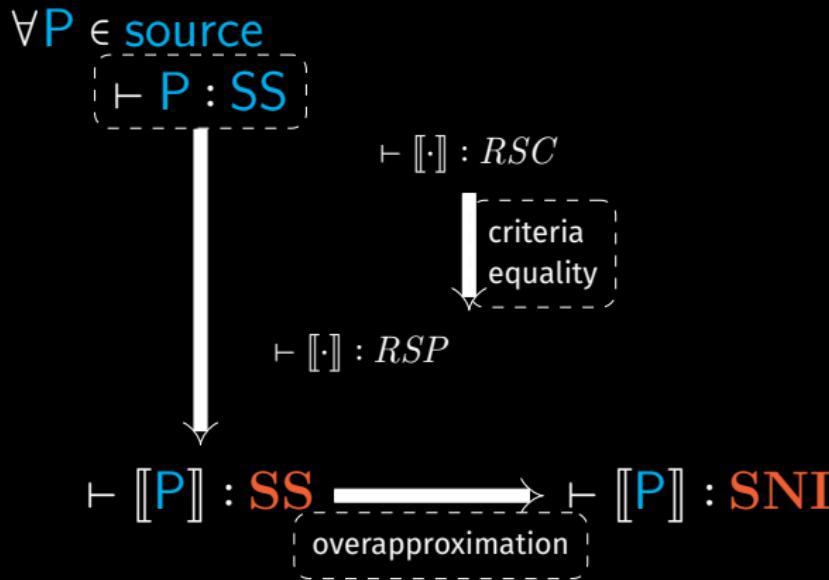
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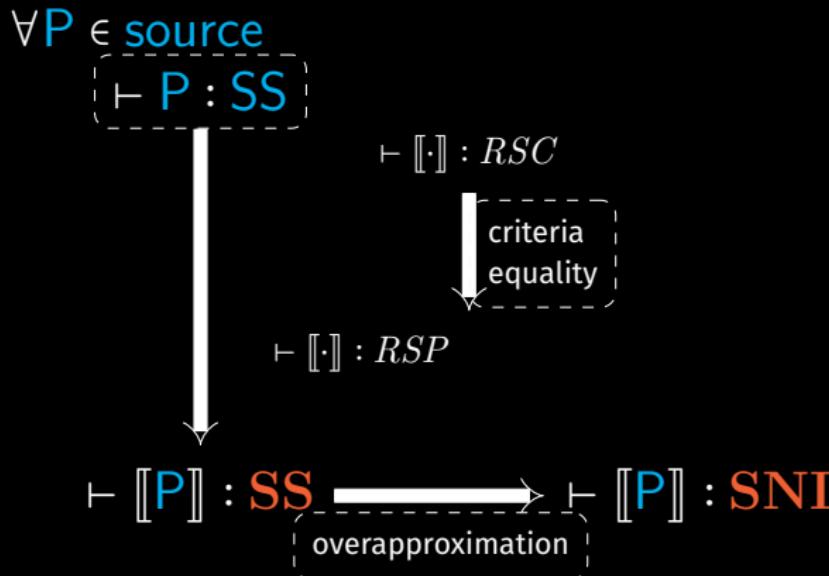


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- dashed premises are already discharged
- to show security: **simply prove  $RSC$**

# RSSC for lfence

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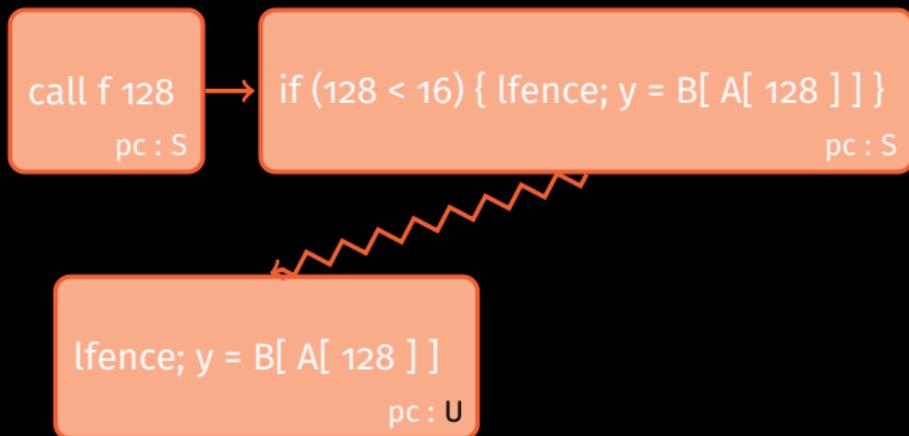
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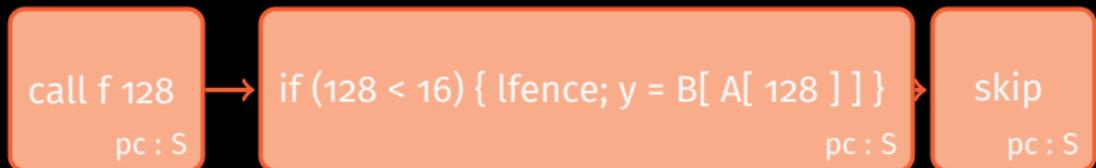
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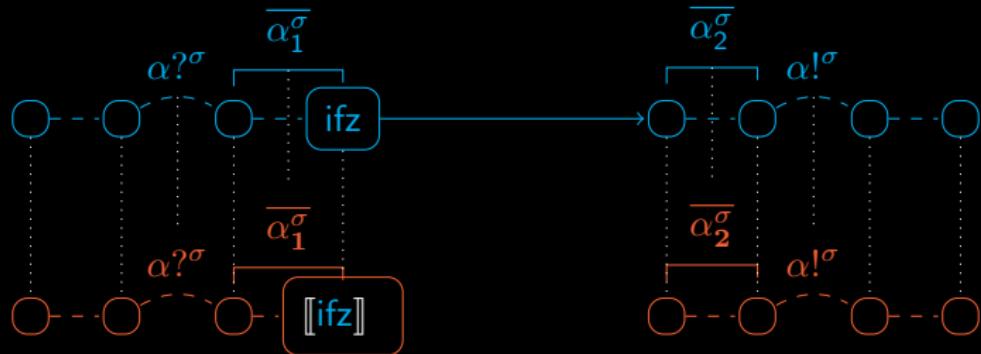
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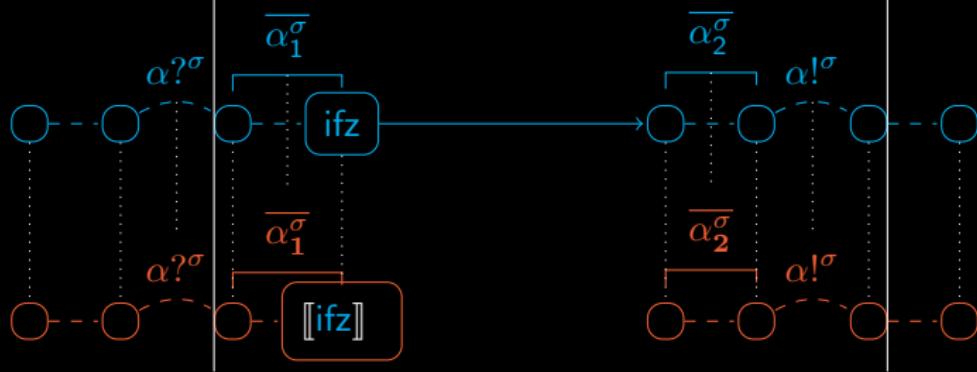


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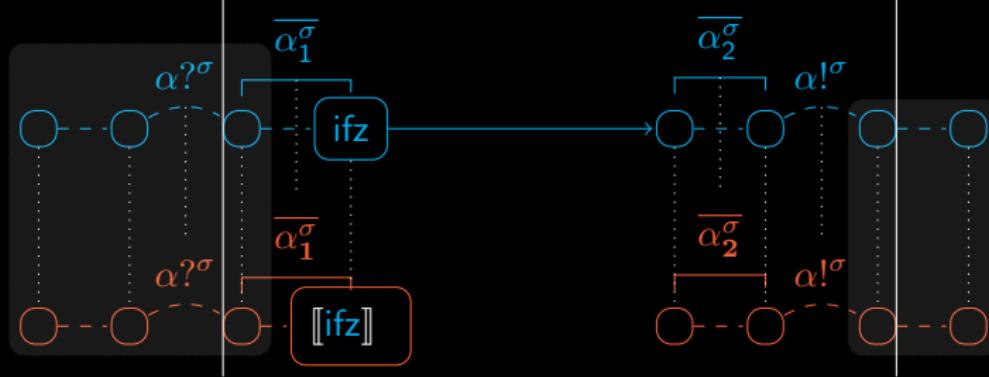


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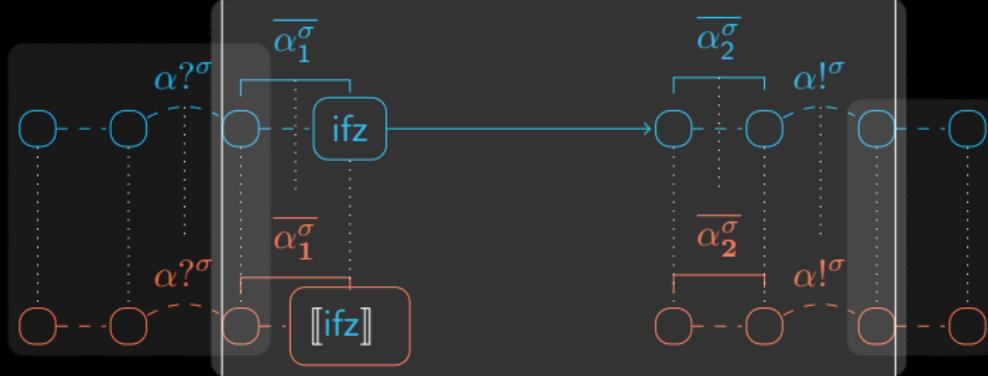


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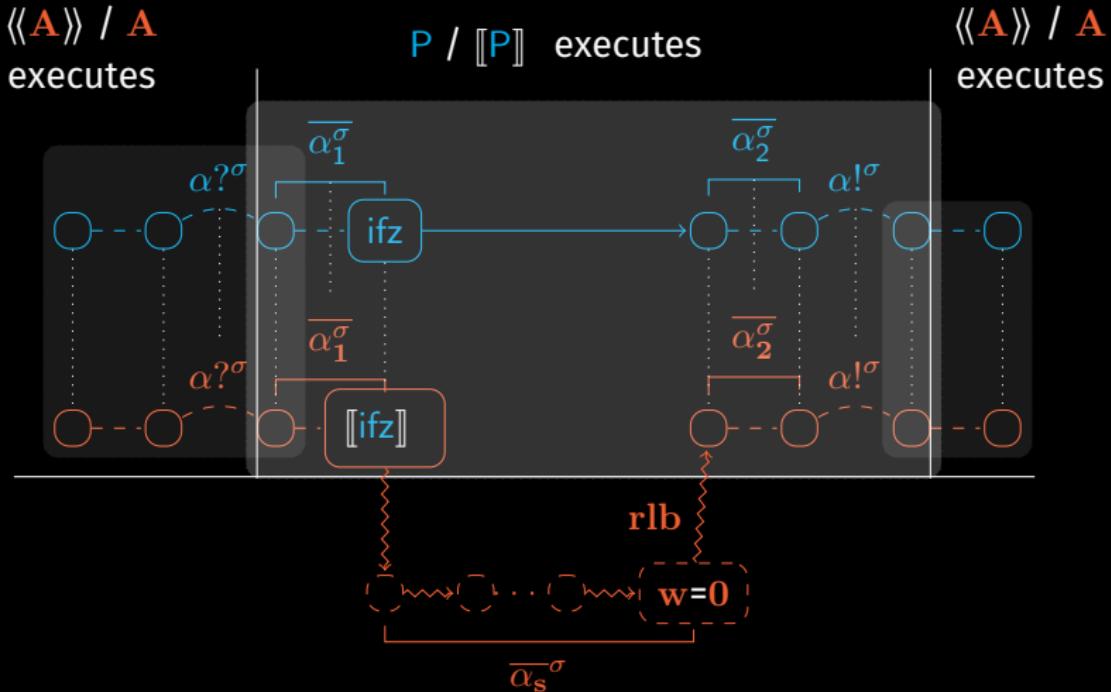
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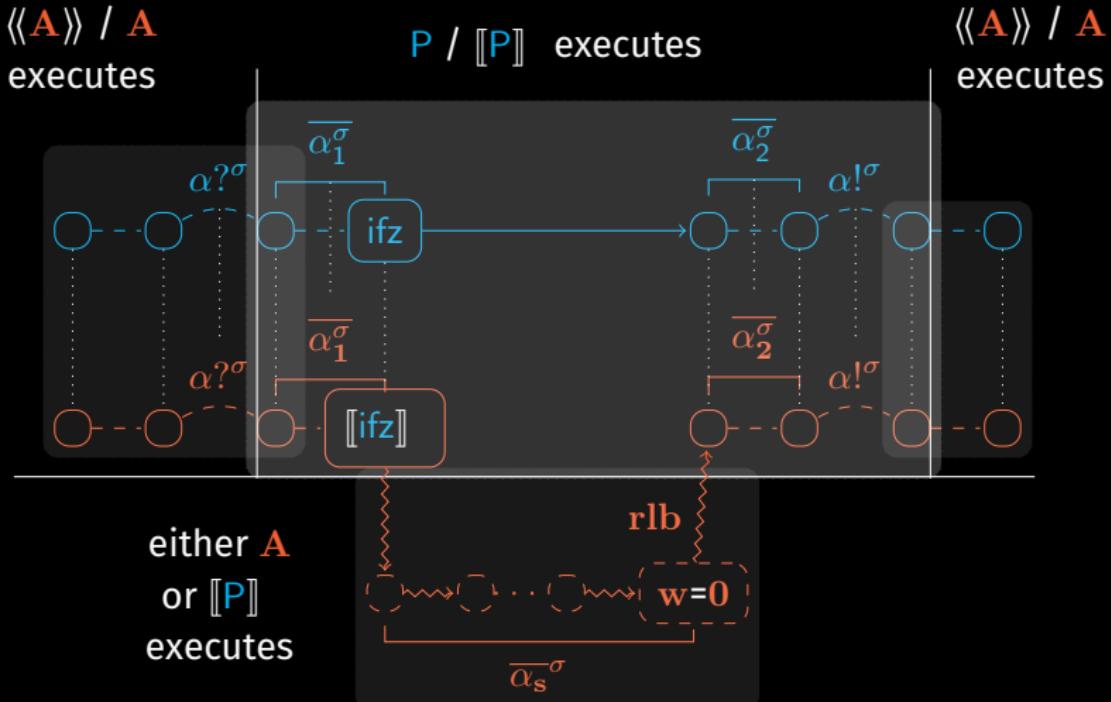
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# What Then?

CCS'22, wip

- SNIv1, SNIv2, SNIv4, SNIv5

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- Challenge: can we compose **lfence**(SNIv1) and **retpoline**(SNIv5)?

# **Security Architectures**

(e.g., Cheri/ARM Morello, Sancus/Intel SGX, ...) Toplas'15, CSF'21, ...

---

# Mechanise Cryptographic Proofs

CSF'24 + wip

---

# Robust Hyperproperty Preservation

$\llbracket \cdot \rrbracket \vdash \text{RHP} \stackrel{\text{def}}{=} \forall P, A. \exists A. \forall t.$

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This talk: generic flavour, geared towards the newer theories

- protocols  $\Pi$  (using concrete crypto)

*commitment for  $b \in \{0, 1\}$  with SID sid:*

compute  $G_{pk_b}(r)$  for random  $r \in \{0, 1\}^n$

set  $y = G_{pk_b}(r)$  for  $b = 0$ , or  $y = G_{pk_b}(r) \oplus \sigma$  for  $b = 1$

send  $(\text{Com}, sid, y)$  to the receiver

Upon receiving  $(\text{Com}, sid, y)$  from  $P_i, P_j$  outputs  $(\text{Receipt}, sid, cid, P_i, P_j)$

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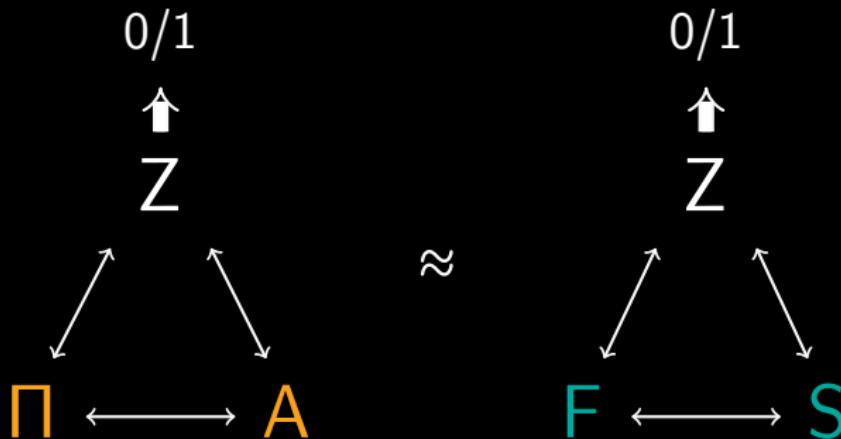
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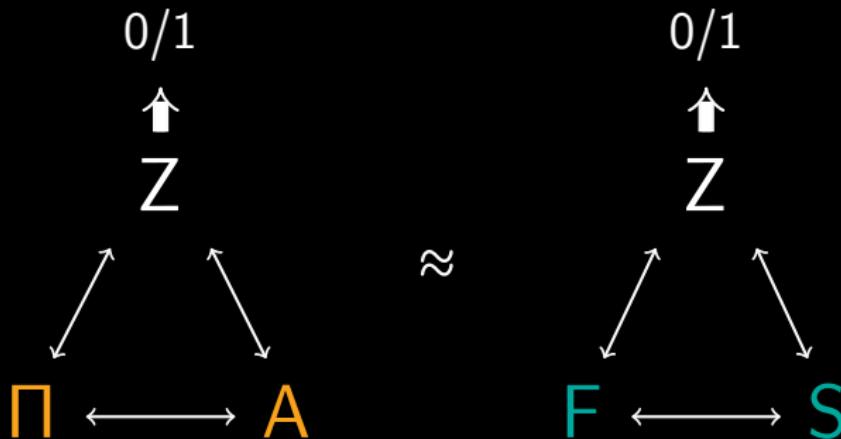
- **attackers A & S** (corrupting parties etc.)
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$$\Pi \vdash_{UC} F \stackrel{\text{def}}{=} \forall \text{poly } A, \exists S, \forall Z.$$

$$\text{Exec}[Z, A, \Pi] \approx \text{Exec}[Z, S, F]$$

# A Closer Look

$$\forall \text{poly } A, \exists S, \forall Z.$$

$\approx$

$$\begin{array}{c} 0/1 \\ \uparrow \\ Z \\ \swarrow \quad \searrow \\ \Pi \longleftrightarrow A \end{array} \qquad \qquad \begin{array}{c} 0/1 \\ \uparrow \\ Z \\ \swarrow \quad \searrow \\ F \longleftrightarrow S \end{array}$$

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$$\begin{array}{ccc} t & & t \\ \uparrow & & \uparrow \\ \llbracket P \rrbracket \bowtie A & \iff & P \bowtie A \end{array}$$

Isabelle'd both perfect and computational UC

# Analogy

<i>UC</i>		<i>SC</i>
protocol	$\Pi$	compiled program
concrete attacker	A	target context
ideal functionality	F	source program
simulator	S	source context
environment, output	Z, 0/1	trace, semantics
communication	$\leftrightarrow$	linking
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human translation	$\Pi \rightarrow F$	$\llbracket \cdot \rrbracket : P \rightarrow P$ compiler
general composition result		...

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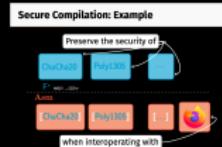
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- Mechanised  $UC$  for 1-Bit Commitment in Deepsec submission
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# Conclusion

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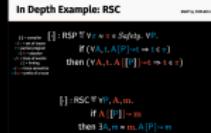
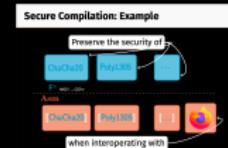
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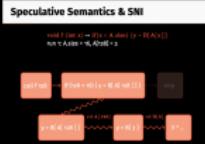
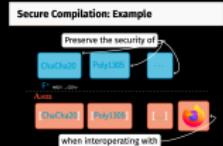
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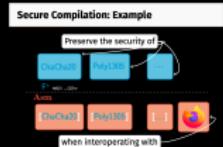
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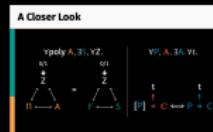
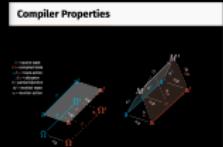
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In Depth Example: RSC

$\vdash \text{RSC} \nparallel \text{RHP} \wedge \forall v, m \in \text{Safety}, \text{RP}$   
 $\vdash (\forall A, \exists A[P] \rightarrow m \neq v)$   
then  $(\forall A, \exists A[P] \rightarrow m \neq v)$

$\vdash \text{RSC} \nparallel \text{RHP} \wedge \text{A}, m$   
 $\vdash A[P] \rightarrow m$   
then  $\exists A, m \in \text{A}[P] = m$



# Future

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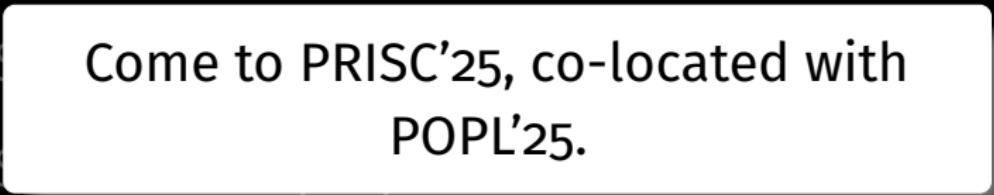
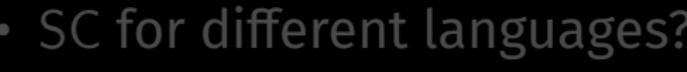
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# Questions?



