

# Fully Abstract Trace Semantics for Low-level Isolation Mechanisms

Marco Patrignani<sup>1</sup>   Dave Clarke<sup>1,2</sup>

<sup>1</sup>iMinds-DistriNet, Dept. Computer Science, KU Leuven, Belgium

<sup>2</sup>now at Dept. of Information Technology, Uppsala, Sweden

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# Outline

- 1 Low-level Isolation Mechanisms: PMA
- 2 Reasoning about PMA
- 3 A Fully Abstract Trace Semantics

# Why is PMA interesting?

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- can be exploited to enforce a control-flow safe execution of C code [APJ14]

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- industrial prototype too: Intel SGX [MAB<sup>+</sup>13]

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Let's see an example of PMA in action

# PMA in action

```
0x0001    call 0xb53
0x0002    movs r0 0x0b55
:
:
0x0b52    movs r0 0x0b55
0x0b53    call 0x0002
0x0b54    movs r0 0x0001
0x0b55    ...

:
0xab00    jmp 0xb53
0xab01    ...
```

- memory space

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r/w

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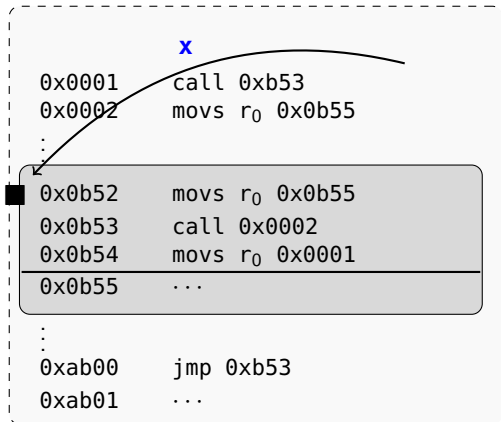
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0x0b52    movi r0 1
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```
0x0b53    movi r1 0x0b56
```

```
0x0b54    jl r1
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```
0x0b55    call 0xab01
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0x0b56    ret
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- contexts are complex to reason about (but very precise)

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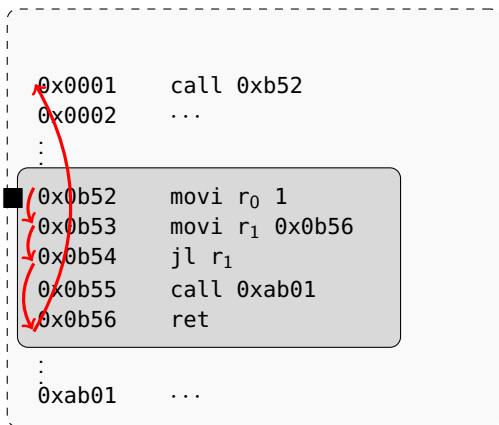
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- they describe what can be observed from the outside of protected PMA code

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  - 1 PMA code can write in unprotected memory
  - 2 flags convey information across function calls
  - 3 registers besides  $r_0$  in ret as well

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To ensure maximal precision, prove the trace semantics to be fully abstract

i.e. there are no other things that we missed

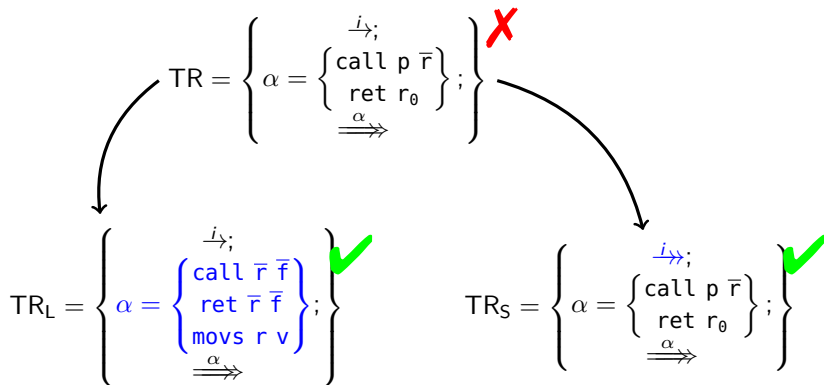
# The spectrum of full abstraction [Cur07]

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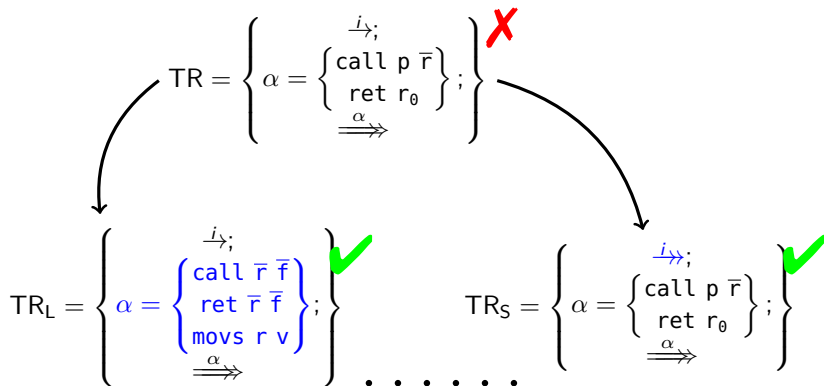
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 \downarrow \\
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 \end{array}$$

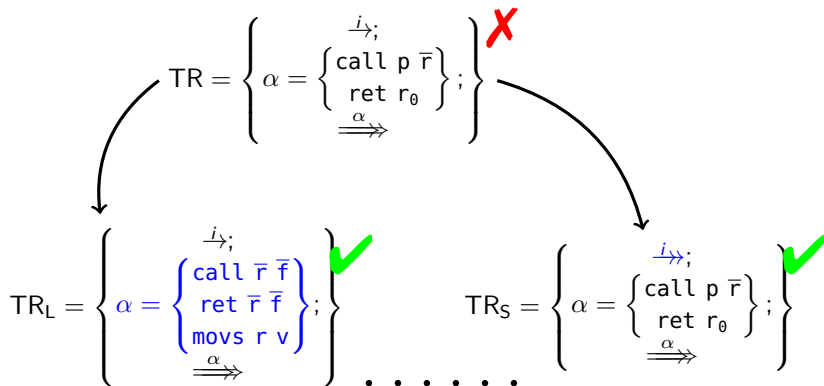
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$$\text{TR}_X(P_1) = \text{TR}_X(P_2) \iff P_1 \simeq P_2$$

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- trace semantics for PMA enables easier reasoning about PMA code
- if the trace semantics is fully abstract, trace equivalence can replace contextual equivalence altogether
- the paper provides two, examples of fully abstract trace semantics for PMA

# Questions

Thank you!

Qs ?

# Bibliographical References I



Pieter Agten, Frank Piessens, and Bart Jacobs, *Sound modular verification in an unverified context*, 2014, to appear.



Pierre-Louis Curien, *Definability and full abstraction*, Electron. Notes Theor. Comput. Sci. **172** (2007), 301–310.



Karim Eldefrawy, Aurélien Francillon, Daniele Perito, and Gene Tsudik, *SMART: Secure and Minimal Architecture for (Establishing a Dynamic) Root of Trust*, NDSS 2012, 19th Annual Network and Distributed System Security Symposium (San Diego, United States), 2012.



Alan Jeffrey and Julian Rathke, *Java Jr.: fully abstract trace semantics for a core Java language*, ESOP'05, LNCS, vol. 3444, Springer, 2005, pp. 423–438.



Frank McKeen, Ilya Alexandrovich, Alex Berenzon, Carlos V. Rozas, Hisham Shafi, Vedvyas Shanbhogue, and Uday R. Savagaonkar, *Innovative instructions and software model for isolated execution*, HASP '13, ACM, 2013, pp. 10:1–10:1.



Jonathan M. McCune, Yanlin Li, Ning Qu, Zongwei Zhou, Anupam Datta, Virgil Gligor, and Adrian Perrig, *Trustvisor: Efficient TCB reduction and attestation*, SP '10 (Washington, DC, USA), IEEE, 2010, pp. 143–158.



Jonathan M. McCune, Bryan J. Parno, Adrian Perrig, Michael K. Reiter, and Hiroshi Isozaki, *Flicker: an execution infrastructure for TCB minimization*, SIGOPS Oper. Syst. Rev. **42** (2008), no. 4, 315–328.

## Bibliographical References II



Job Noorman, Pieter Agten, Wilfried Daniels, Raoul Strackx, Anthony Van Herrewege, Christophe Huygens, Bart Preneel, Ingrid Verbauwhede, and Frank Piessens, *Sancus: Low-cost trustworthy extensible networked devices with a zero-software Trusted Computing Base*, Proceedings of the 22nd USENIX conference on Security symposium, USENIX Association, 2013.



Marco Patrignani, Dave Clarke, and Frank Piessens, *Secure Compilation of Object-Oriented Components to Protected Module Architectures*, Proceedings of the 11th Asian Symposium on Programming Languages and Systems (APLAS'13), LNCS, vol. 8301, 2013, pp. 176–191.



Raoul Strackx and Frank Piessens, *Fides: selectively hardening software application components against kernel-level or process-level malware*, Proceedings of the 2012 ACM conference on Computer and communications security (New York, NY, USA), CCS '12, ACM, 2012, pp. 2–13.