

# Multi-Module Fully-Abstract Compilation

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13 July 2015

# Outline

- 1 Goals of this Talk
  - Background
- 2 Failures of Full Abstraction for Compiler Security
- 3 Solutions
  - PMA

# Goal

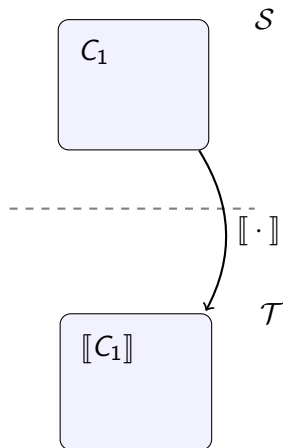
- see secure compilation failures due to linking

# Goal

- see secure compilation failures due to linking
- see solutions to them (for PMA & Capability machines)

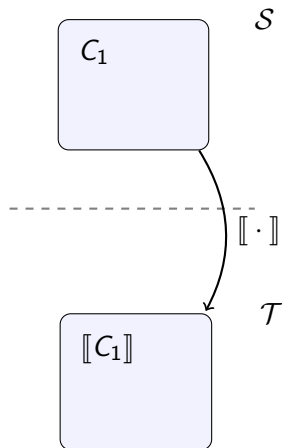
# What is a Secure Compiler?

- a compiler is a function from source to target **components**



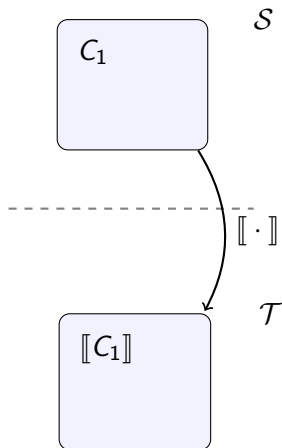
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- a compiler is secure if it preserves source-level security properties in the components it generates *no more, no less*



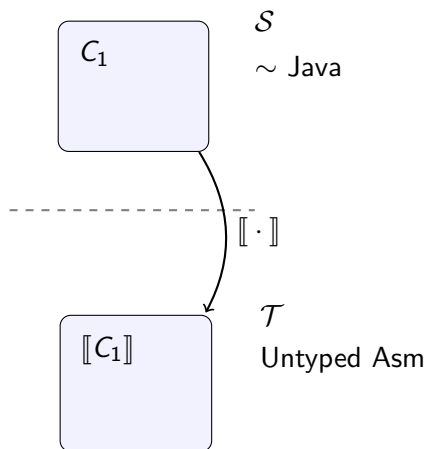
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Fully abstract compilers preserve (and reflect) source-level behaviour in compiled components

- preserve security properties
- protect against code injection attacks
- enable source-level reasoning

# Fully Abstract Compilation Formally

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- $\mathbb{C}$  models an attacker
- $\simeq$  implies security properties e.g., confidentiality, integrity, etc

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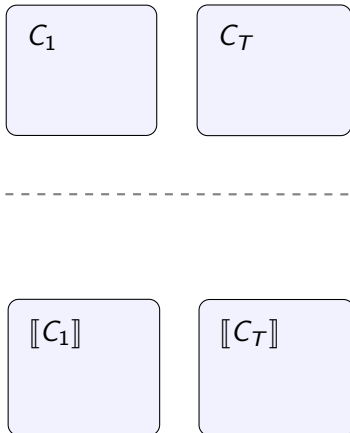
# Compiler INsecurity

- compiler full abstraction is often studied in simple settings

 $C_1$  $\llbracket C_1 \rrbracket$

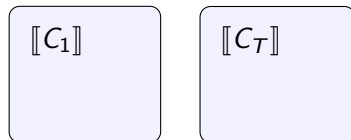
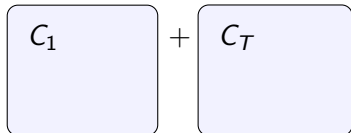
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- existing fully abstractly compiled programs are not linkable



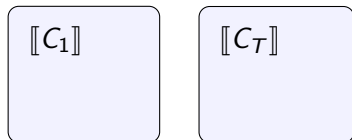
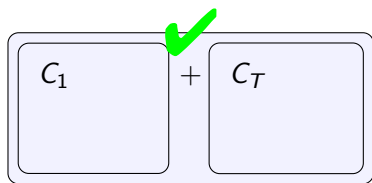
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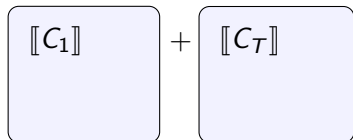
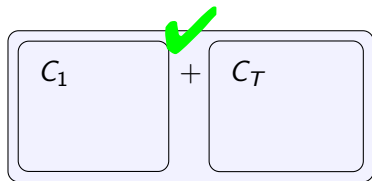
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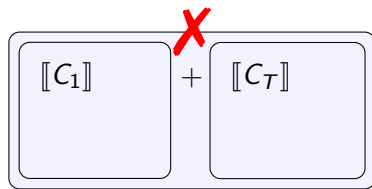
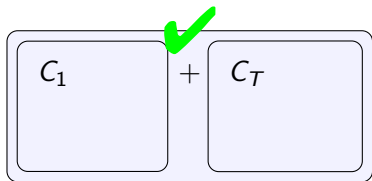
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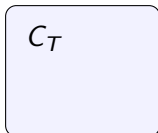
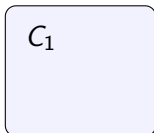
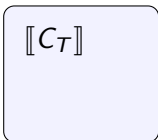
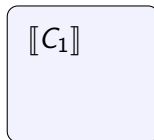


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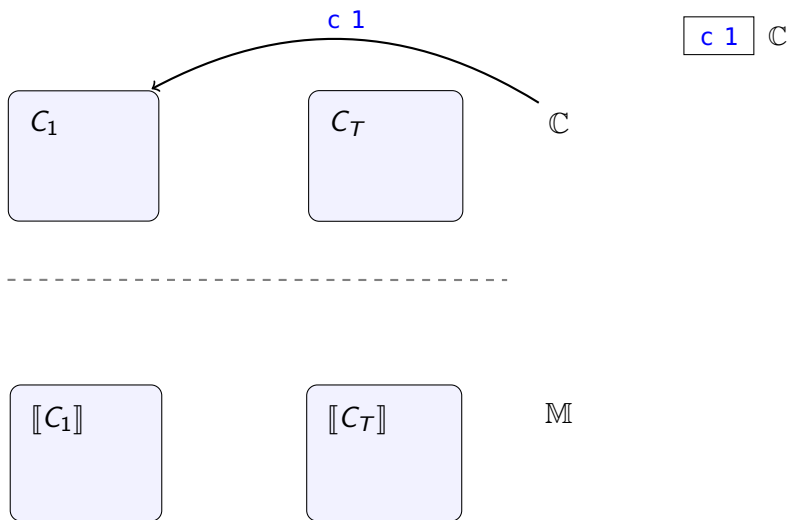
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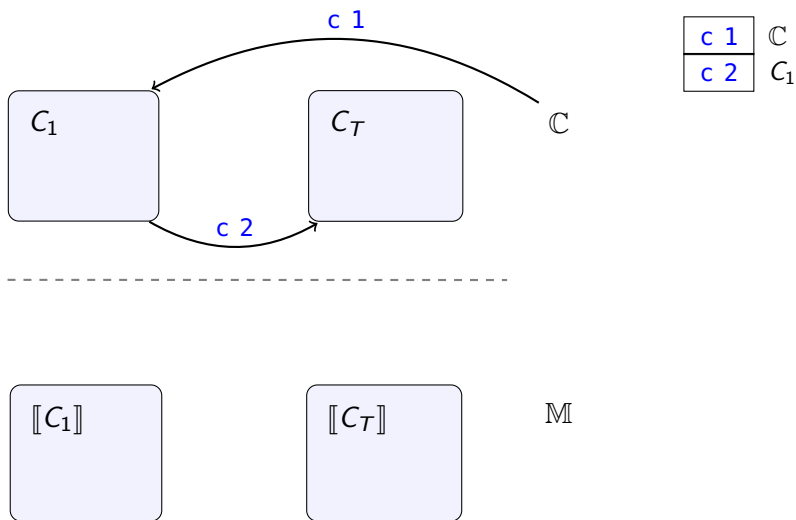
# Call Stack Shortcutting

 $\mathbb{C}$  $\mathbb{M}$

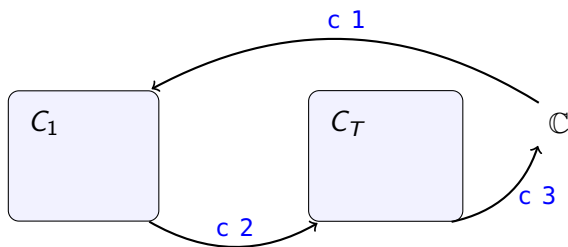
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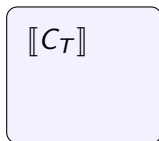
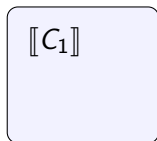
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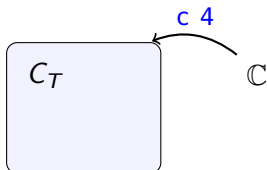
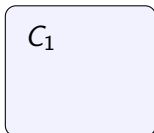


$c_1$	$C$
$c_2$	$C_1$
$c_3$	$C_T$

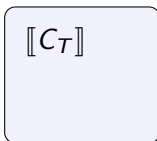
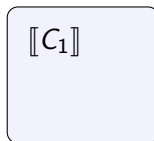


$M$

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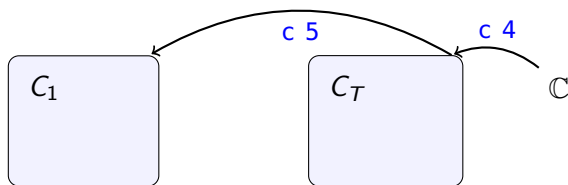


c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$



$M$

# Call Stack Shortcutting

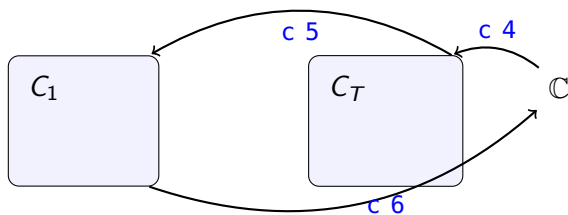


c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$

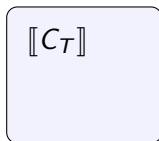
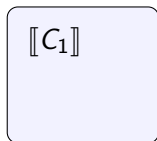




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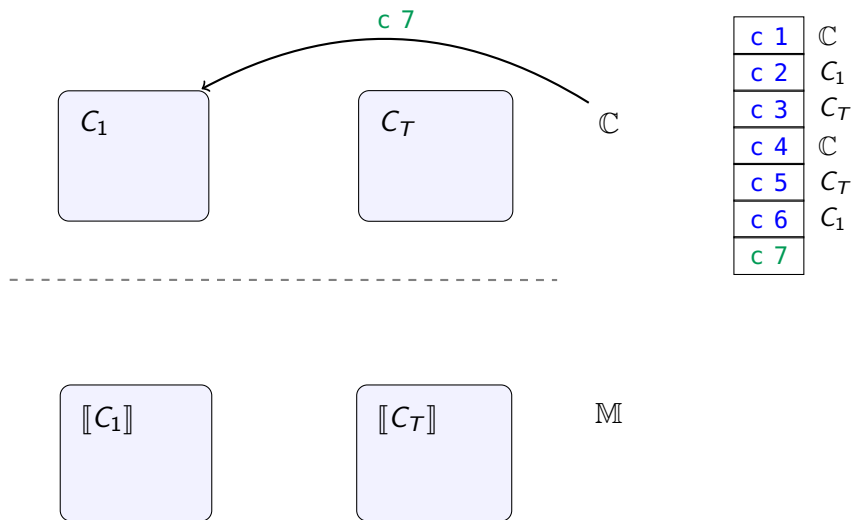


c 1	$C$
c 2	$C_1$
c 3	$C_T$
c 4	$C$
c 5	$C_T$
c 6	$C_1$

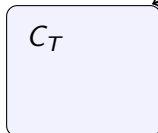
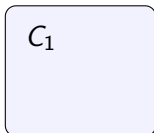


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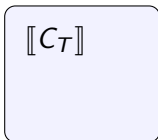
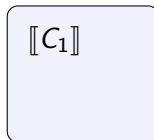
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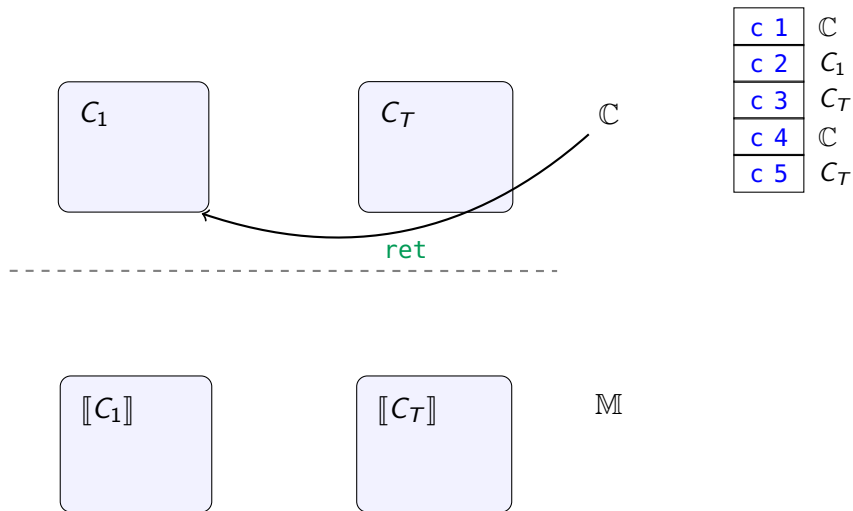


c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$
c 7	

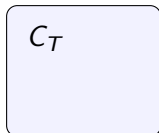
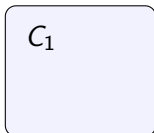


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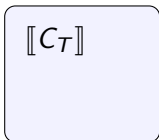
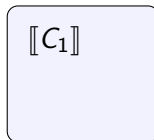
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 $\mathbb{C}$ 

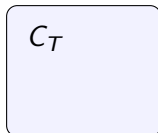
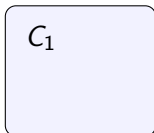
c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$


 $M$ 

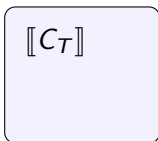
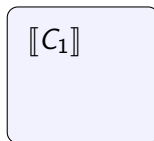
c 1

c 1	$M$
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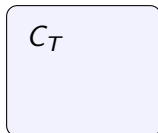
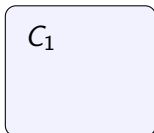

 $\mathbb{M}$ 

c 1	$\mathbb{M}$
c 2	$\llbracket C_1 \rrbracket$

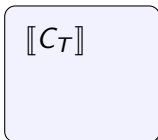
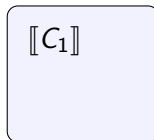
c 1

c 2

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c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$


 $\mathbb{M}$ 

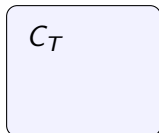
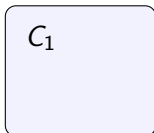
c 1	$\mathbb{M}$
c 2	$\llbracket C_1 \rrbracket$
c 3	$\llbracket C_T \rrbracket$

c 1

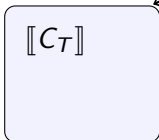
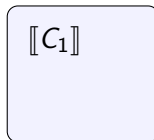
c 2

c 3

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c 5	$C_T$



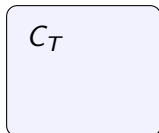
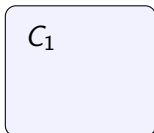
c 4

 $\mathbb{M}$ 

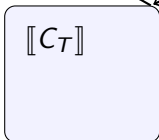
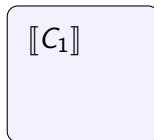
c 1	$\mathbb{M}$
c 2	$\llbracket C_1 \rrbracket$
c 3	$\llbracket C_T \rrbracket$
c 4	$\mathbb{M}$



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c 2	$C_1$
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c 5	$C_T$

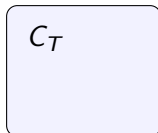
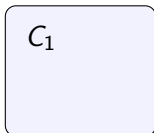

 $\mathbb{M}$ 

c 1	$\mathbb{M}$
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c 3	$\llbracket C_T \rrbracket$
c 4	$\mathbb{M}$
c 5	$\llbracket C_T \rrbracket$

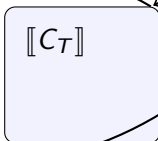
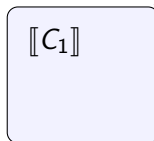
c 5

c 4

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 $\mathbb{C}$ 

c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
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c 5	$C_T$


 $\mathbb{M}$ 

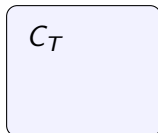
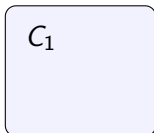
c 1	$\mathbb{M}$
c 2	$\llbracket C_1 \rrbracket$
c 3	$\llbracket C_T \rrbracket$
c 4	$\mathbb{M}$
c 5	$\llbracket C_T \rrbracket$
c 6	$\llbracket C_1 \rrbracket$

c 5

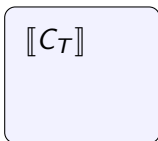
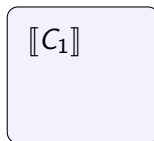
c 4

c 6

# Call Stack Shortcutting

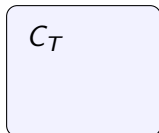
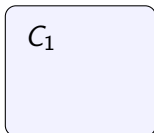

 $\mathbb{C}$ 

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c 2	$C_1$
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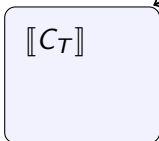
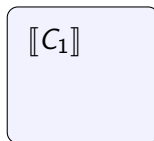

 $M$ 
 $c\ 7$ 

c 1	$M$
c 2	$\llbracket C_1 \rrbracket$
c 3	$\llbracket C_T \rrbracket$
c 4	$M$
c 5	$\llbracket C_T \rrbracket$
c 6	$\llbracket C_1 \rrbracket$
c 7	

# Call Stack Shortcutting

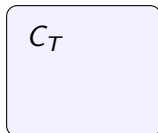
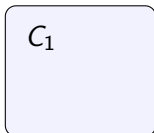

 $\mathbb{C}$ 

c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$

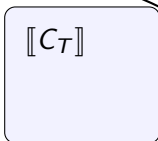
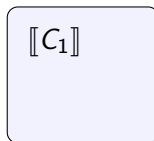

 $M$ 

c 1	$M$
c 2	$\llbracket C_1 \rrbracket$
c 3	$\llbracket C_T \rrbracket$
c 4	$M$
c 5	$\llbracket C_T \rrbracket$
c 6	$\llbracket C_1 \rrbracket$
c 7	

# Call Stack Shortcutting


 $\mathbb{C}$ 

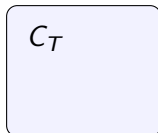
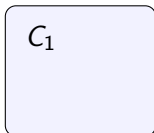
c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$


 $M$ 

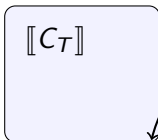
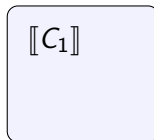
ret

c 1	$M$
c 2	$\llbracket C_1 \rrbracket$
c 3	$\llbracket C_T \rrbracket$
c 4	$M$
c 5	$\llbracket C_T \rrbracket$
c 6	$\llbracket C_1 \rrbracket$

# Call Stack Shortcutting


 $\mathbb{C}$ 

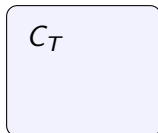
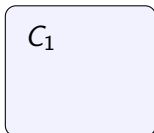
c 1	$\mathbb{C}$
c 2	$C_1$
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c 4	$\mathbb{C}$
c 5	$C_T$


 $M$ 

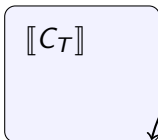
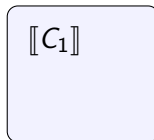
ret

c 1	$M$
c 2	$\llbracket C_1 \rrbracket$
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c 5	$\llbracket C_T \rrbracket$
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ret	

# Call Stack Shortcutting

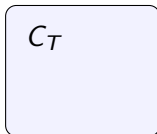
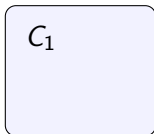

 $\mathbb{C}$ 

c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$

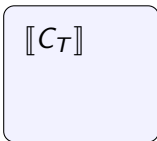
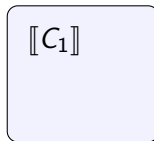

 $M$ 
 $ret$ 

c 1	$M$
c 2	$\llbracket C_1 \rrbracket$
<del>c 3</del>	<del><math>\llbracket C_T \rrbracket</math></del>
<del>c 4</del>	<del><math>M</math></del>
<del>c 5</del>	<del><math>\llbracket C_T \rrbracket</math></del>
<del>c 6</del>	<del><math>\llbracket C_1 \rrbracket</math></del>
<del>ret</del>	

# Object Guessing

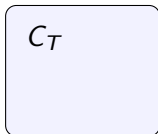
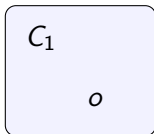
 $\mathbb{C}$ 

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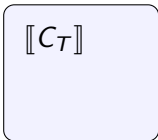
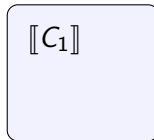
 $\mathbb{M}$



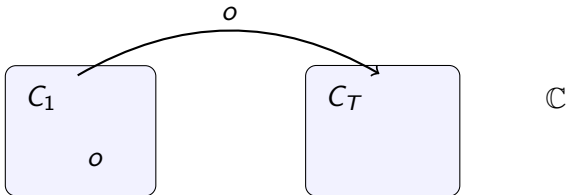
# Object Guessing


 $\mathbb{C}$ 

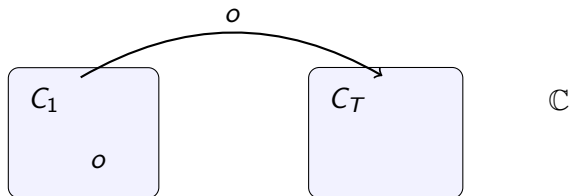
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 $\mathbb{M}$

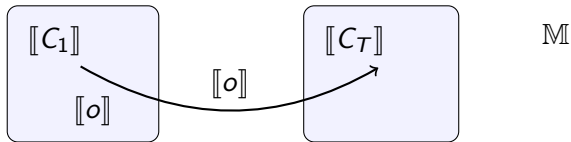
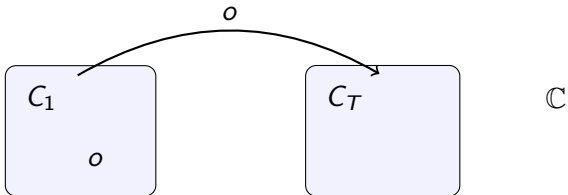
# Object Guessing



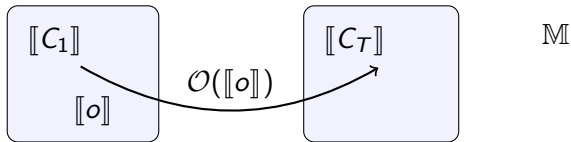
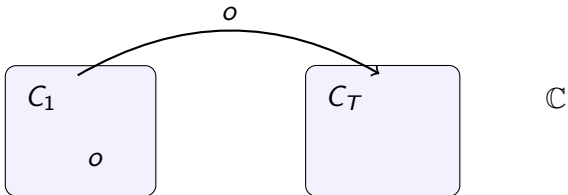
# Object Guessing



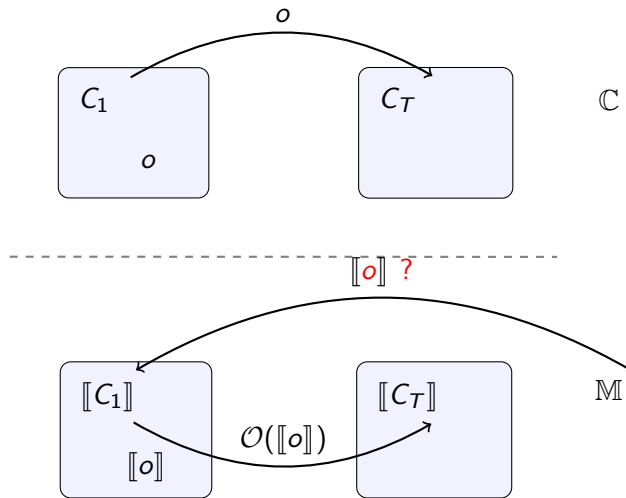
# Object Guessing



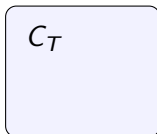
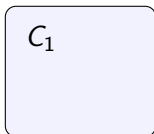
# Object Guessing



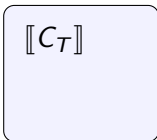
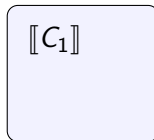
# Object Guessing



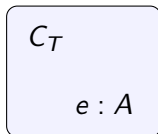
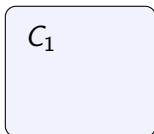
# Object Faking

 $\mathbb{C}$ 

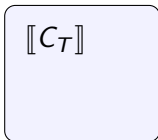
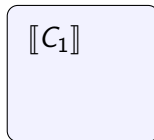
-----

 $\mathbb{M}$

# Object Faking

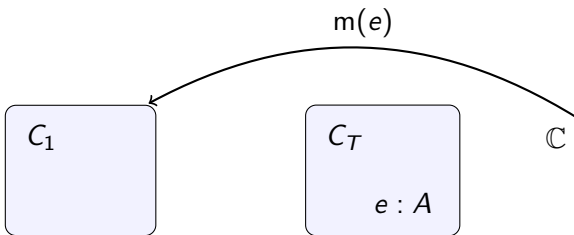
 $\mathbb{C}$ 

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 $\mathbb{M}$



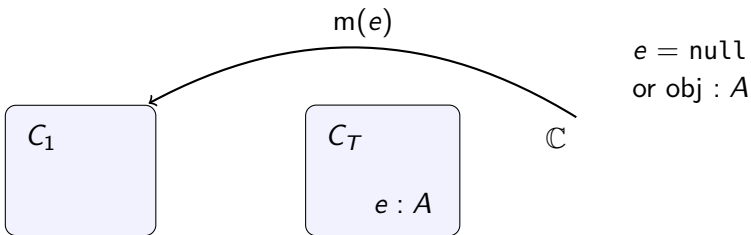
# Object Faking



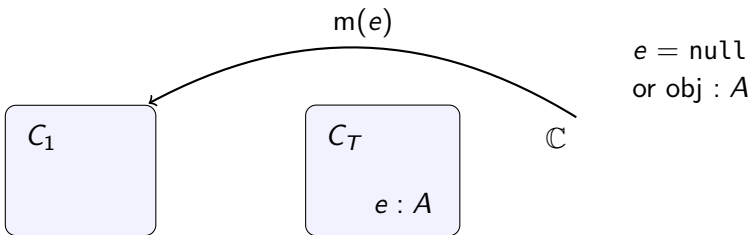
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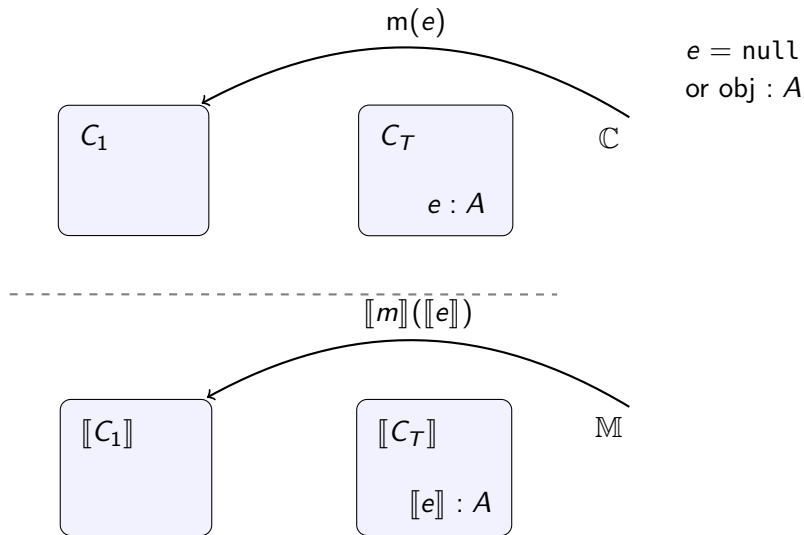
# Object Faking



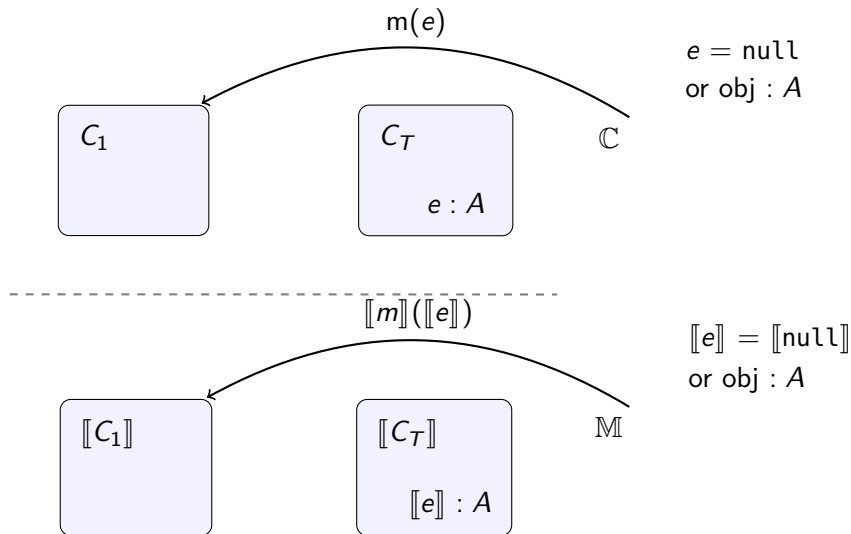
# Object Faking



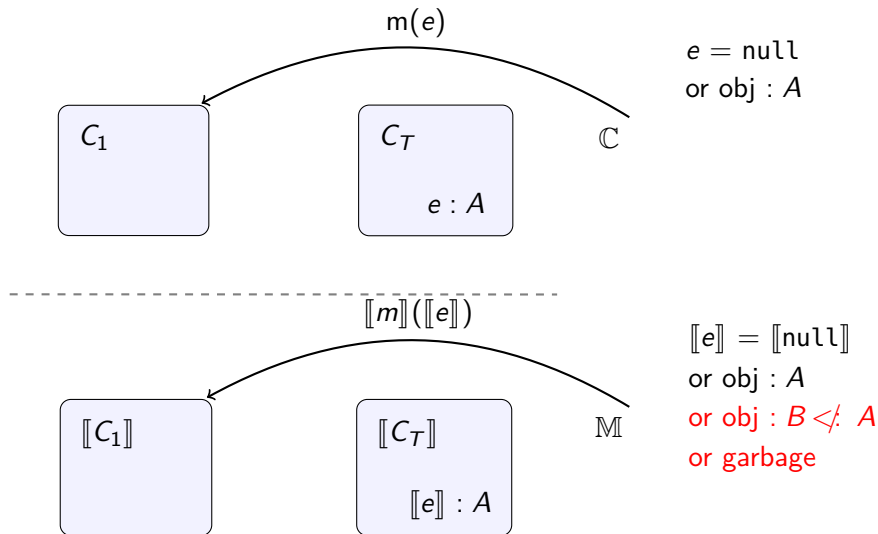
# Object Faking



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# Object Faking



# General Point

- problems always arising when two components are considered

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- problems always arising when two components are considered
- no existing secure compiler deals with linking between compiled components



# Outline

- 1 Goals of this Talk
  - Background
- 2 Failures of Full Abstraction for Compiler Security
- 3 Solutions
  - PMA

# Solutions

- Protected modules architectures

# Solutions

- Protected modules architectures [read, Intel SGX]

# Solutions

- Protected modules architectures [read, Intel SGX] (isolation at assembly)

# Solutions

- Protected modules architectures [read, Intel SGX] (isolation at assembly)
- Capability machines

# Solutions

- Protected modules architectures [read, Intel SGX] (isolation at assembly)
- Capability machines (subject/objects/operations at assembly)

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- Protected modules architectures
- Capability machines (subject/objects/operations at assembly)

# What is PMA?

- deep encapsulation at the lowest level of abstraction



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- deep encapsulation at the lowest level of abstraction
- the basis of several security-related works

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- deep encapsulation at the lowest level of abstraction
- the basis of several security-related works
- Intel is porting it into future processors (SGX)

# Untyped Assembly + PMA

```
0x0001    call 0xb53
0x0002    movs r0 0xb55
...
0x0b52    movs r0 0xb55
0x0b53    call 0x0002
0x0b54    movs r0 0xeb54
0x0b55    ...
...
0xab00    jmp 0xb53
...
0xeb52    movs r0 0xeb54
0xeb53    call 0xab02
0xeb54    ...
...
```

# Untyped Assembly + PMA

```
0x0001    call 0xb53  
0x0002    movs r0 0xb55
```

```
...
```

```
0x0b52    movs r0 0xb55  
0x0b53    call 0x0002  
0x0b54    movs r0 0xeb54  
0x0b55    ...
```

**ID 1**

```
...
```

```
0xab00    jmp 0xb53
```

```
...
```

```
0xeb52    movs r0 0xeb54  
0xeb53    call 0xab02  
0xeb54    ...
```

**ID 2**

```
...
```

# Untyped Assembly + PMA

```
0x0001    call 0xb53
0x0002    movs r0 0xb55
```

```
...
```

```
0x0b52    movs r0 0xb55
0x0b53    call 0x0002
0x0b54    movs r0 0xeb54
0x0b55    ...
```

**ID 1**

```
...
```

```
0xab00    jmp 0xb53
```

```
...
```

```
0xeb52    movs r0 0xeb54
0xeb53    call 0xab02
0xeb54    ...
```

**ID 2**

```
...
```

# Untyped Assembly + PMA

```
0x0001    call 0xb53  
0x0002    movs r0 0xb55
```

```
...
```

```
0x0b52    movs r0 0xb55  
0x0b53    call 0x0002  
0x0b54    movs r0 0xeb54  
0x0b55    ...
```

**ID 1**

r/w

```
...
```

```
0xab00    jmp 0xb53
```

```
...
```

```
0xeb52    movs r0 0xeb54  
0xeb53    call 0xab02  
0xeb54    ...
```

**ID 2**

r/w

```
...
```

# Untyped Assembly + PMA

```
0x0001    call 0xb53  
0x0002    movs r0 0xb55
```

```
...
```

```
0xb52     movs r0 0xb55  
0xb53     call 0x0002  
0xb54     movs r0 0xeb54  
0xb55     ...
```

*r/x*

**ID 1**

```
...
```

```
0xab00    jmp 0xb53
```

```
...
```

```
0xeb52    movs r0 0xeb54  
0xeb53    call 0xab02  
0xeb54    ...
```

**ID 2**

```
...
```

# Untyped Assembly + PMA

```
0x0001    call 0xb53  
0x0002    movs r0 0xb55  
...
```

r/w/x

```
0x0b52    movs r0 0xb55  
0x0b53    call 0x0002  
0x0b54    movs r0 0xeb54  
0x0b55    ...
```

ID 1

```
...
```

```
0xab00    jmp 0xb53  
...
```

r/w/x

```
0xeb52    movs r0 0xeb54  
0xeb53    call 0xab02  
0xeb54    ...
```

ID 2



# Untyped Assembly + PMA

```
0x0001    call 0xb53  
0x0002    movs r0 0xb55  
...
```

```
0x0b52    movs r0 0xb55  
0x0b53    call 0x0002  
0x0b54    movs r0 0xeb54  
0x0b55    ...
```

**ID 1***r/w/x*

```
0xab00    jmp 0xb53  
...
```

```
0xeb52    movs r0 0xeb54  
0xeb53    call 0xab02  
0xeb54    ...
```

**ID 2**

## Untyped Assembly + PMA

```

0x0001    call 0xb53
0x0002    movs r0 0xb55

```

```

...

```

```

0x0b52    movs r0 0xb55
0x0b53    call 0x0002
0x0b54    movs r0 0xeb54
0x0b55    ...

```

```

...

```

```

0xab00    jmp 0xb53

```

```

...

```

```

0xeb52    movs r0 0xeb54
0xeb53    call 0xab02
0xeb54    ...

```

r/w/x

ID 1

r/w/x

r/w/x

r/w/x

ID 2

# Untyped Assembly + PMA

```
0x0001    call 0xb53
0x0002    movs r0 0xb55
```

```
...
```

```
0x0b52    movs r0 0xb55
0x0b53    call 0x0002
0x0b54    movs r0 0xeb54
0x0b55    ...
```

**ID 1**

```
...
```

```
0xab00    jmp 0xb53
```

```
...
```

```
0xeb52    movs r0 0xeb54
0xeb53    call 0xab02
0xeb54    ...
```

**ID 2**

```
...
```

## Untyped Assembly + PMA

0x0001 ~~call 0xb53~~  
 0x0002 ~~movs r0 0x0b55~~  
 ...

0x0b52 movs r0 0x0b55  
 0x0b53 call 0x0002  
 0x0b54 movs r0 0xeb54  
 0x0b55 ...

ID 1

...  
 0xab00 ~~jmp 0xb53~~  
 ...

0xeb52 movs r0 0xeb54  
 0xeb53 call 0xab02  
 0xeb54 ...

ID 2

# Untyped Assembly + PMA

```
0x0001    call 0xb53
0x0002    movs r0 0x0b55
```

...

```
0x0b52    movs r0 0x0b55
0x0b53    call 0x0002
0x0b54    movs r0 0xeb54
0x0b55    ...
```

**ID 1**

...

```
0xab00    jmp 0xb53
```

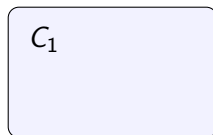
...

```
0xeb52    movs r0 0xeb54
0xeb53    call 0xab02
0xeb54    ...
```

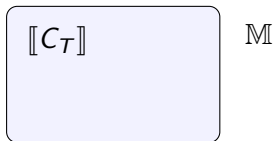
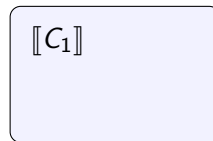
**ID 2**

...

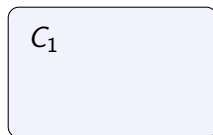
# Addressing Call Stack Shortcutting



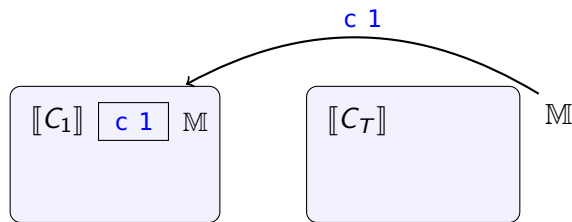
c 1	$\mathbb{C}$
c 2	$G_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$G_1$



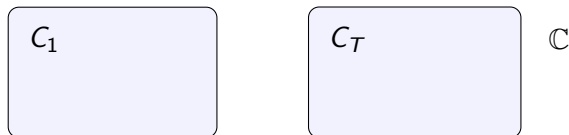
# Addressing Call Stack Shortcutting



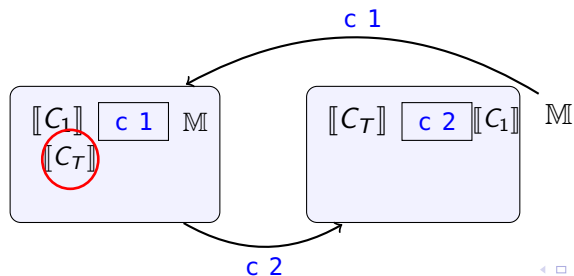
c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$



# Addressing Call Stack Shortcutting

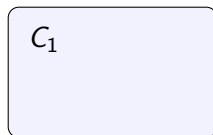


c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$

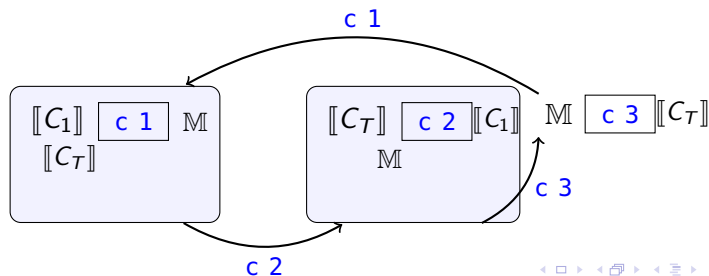




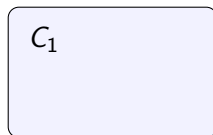
# Addressing Call Stack Shortcutting



c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$

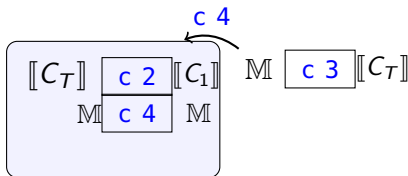
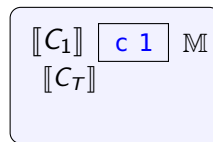


# Addressing Call Stack Shortcutting

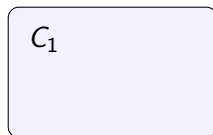


$\mathbb{C}$

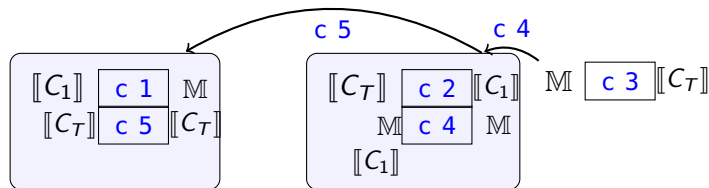
c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$



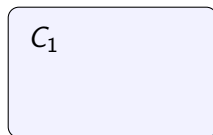
# Addressing Call Stack Shortcutting



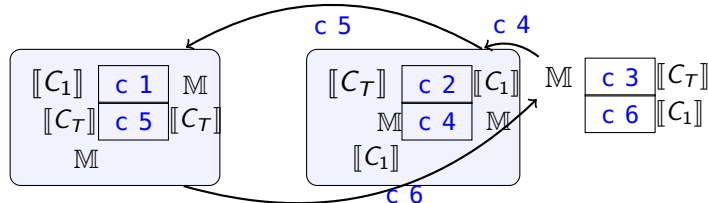
c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$



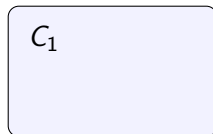
# Addressing Call Stack Shortcutting



c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$

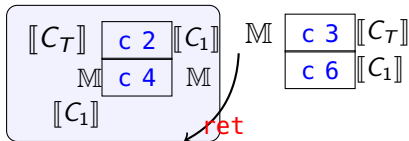
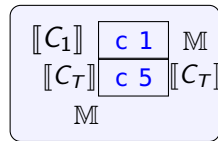


# Addressing Call Stack Shortcutting

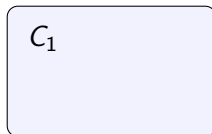


$\mathbb{C}$

c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$

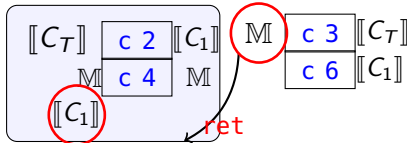
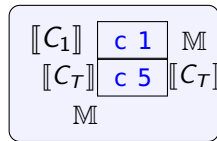


# Addressing Call Stack Shortcutting

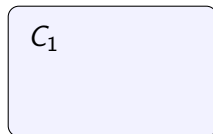


$\mathbb{C}$

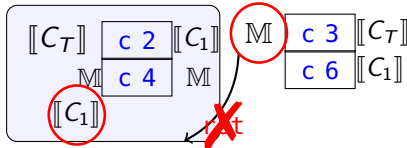
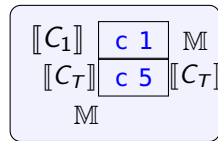
c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$



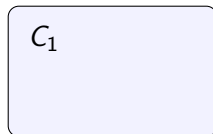
# Addressing Call Stack Shortcutting



c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$

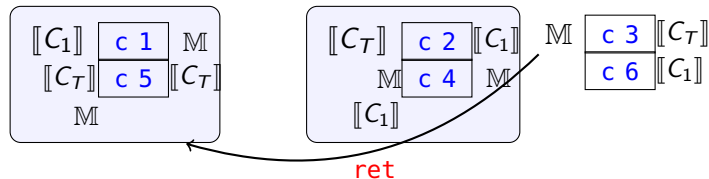


# Addressing Call Stack Shortcutting



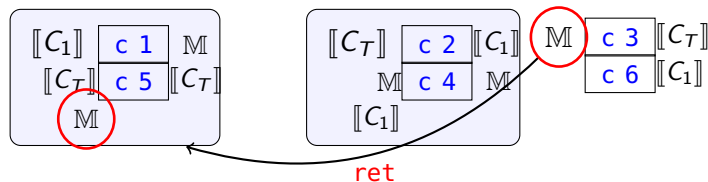
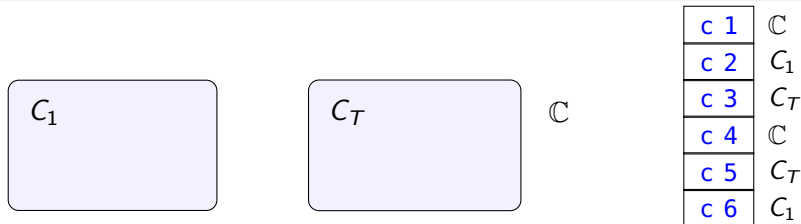
$\mathbb{C}$

c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$

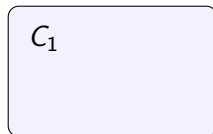




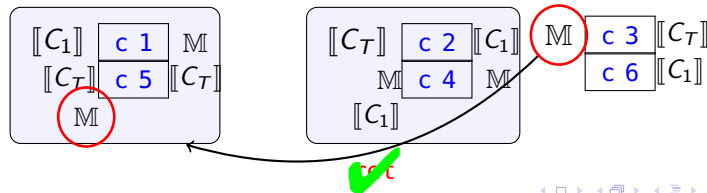
# Addressing Call Stack Shortcutting



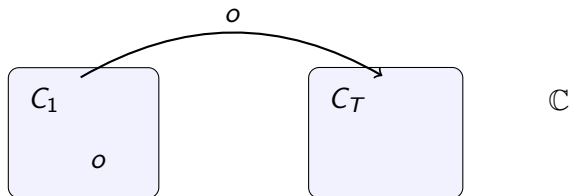
# Addressing Call Stack Shortcutting



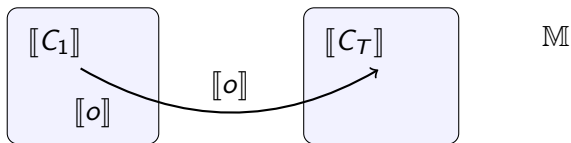
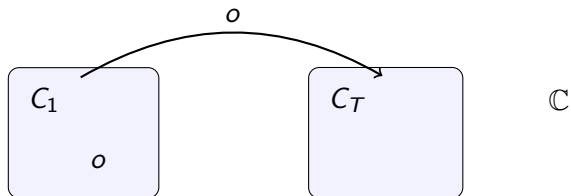
c 1	$\mathbb{C}$
c 2	$C_1$
c 3	$C_T$
c 4	$\mathbb{C}$
c 5	$C_T$
c 6	$C_1$



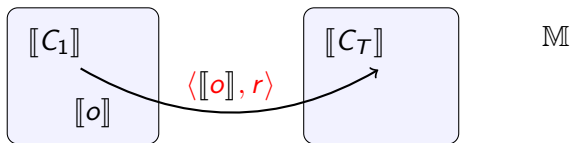
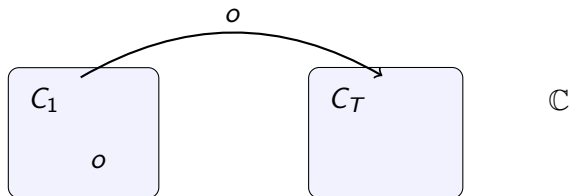
# Addressing Object Guessing



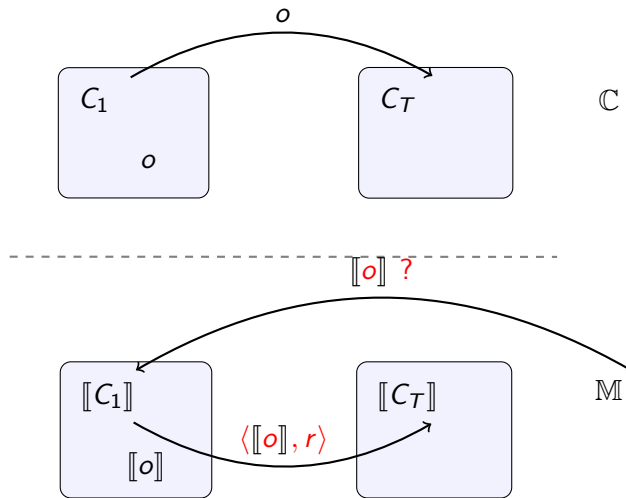
# Addressing Object Guessing



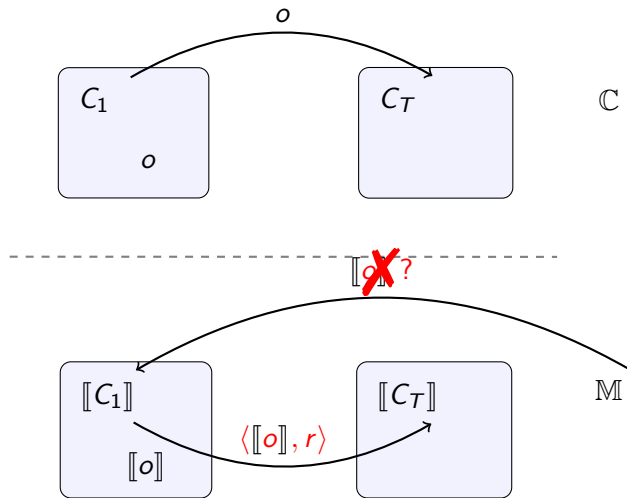
# Addressing Object Guessing



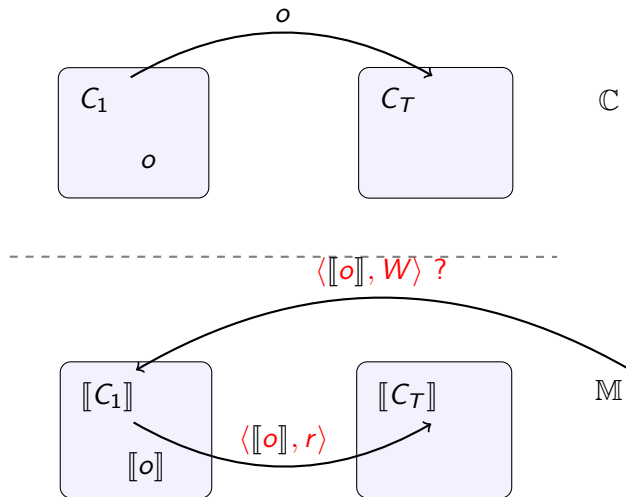
# Addressing Object Guessing



# Addressing Object Guessing

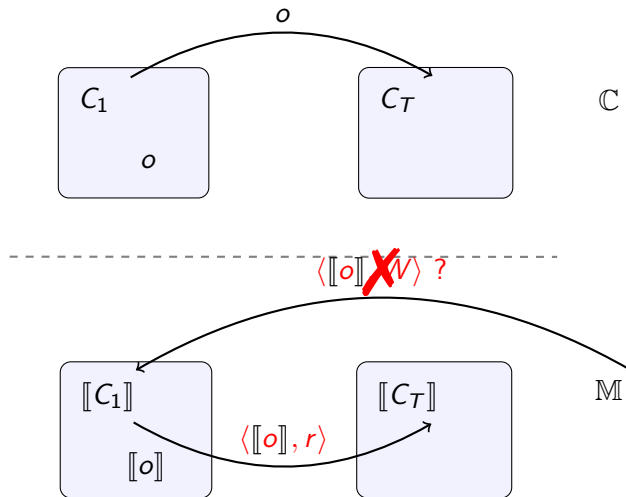


# Addressing Object Guessing

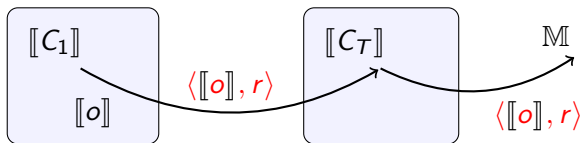
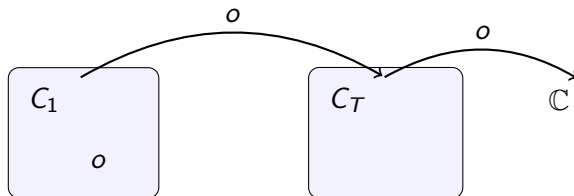




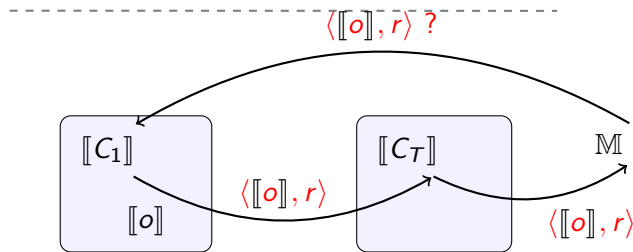
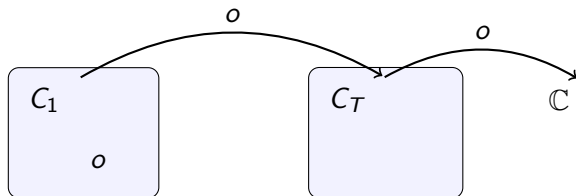
# Addressing Object Guessing



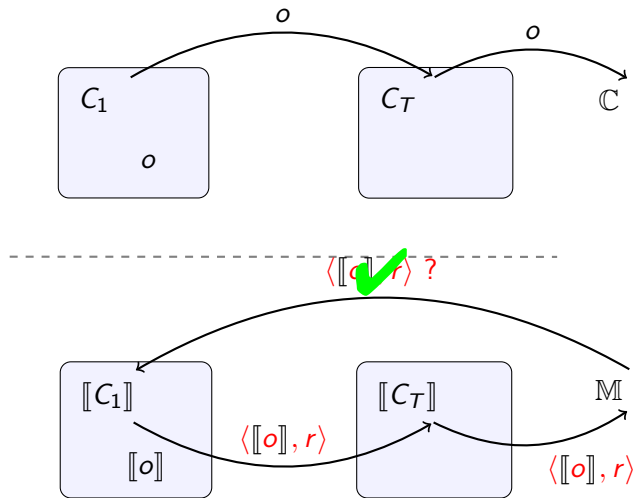
# Addressing Object Guessing



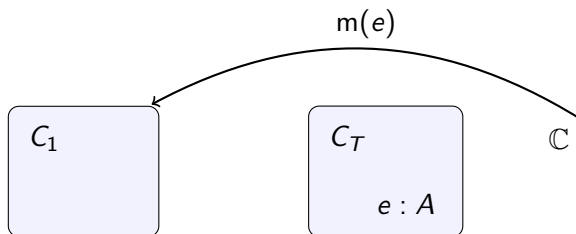
# Addressing Object Guessing



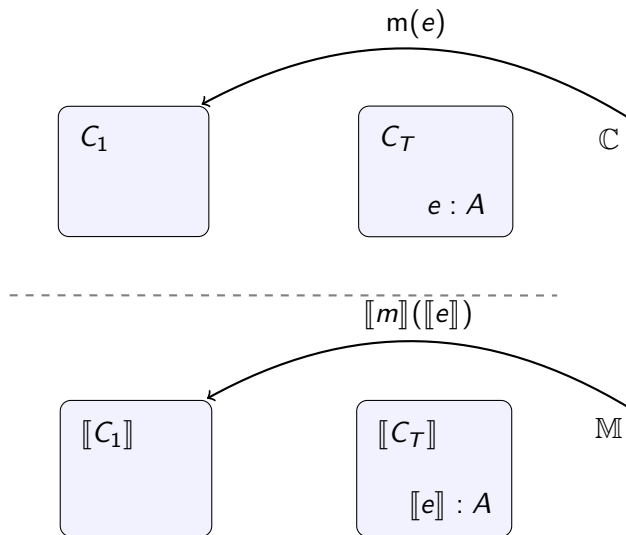
# Addressing Object Guessing



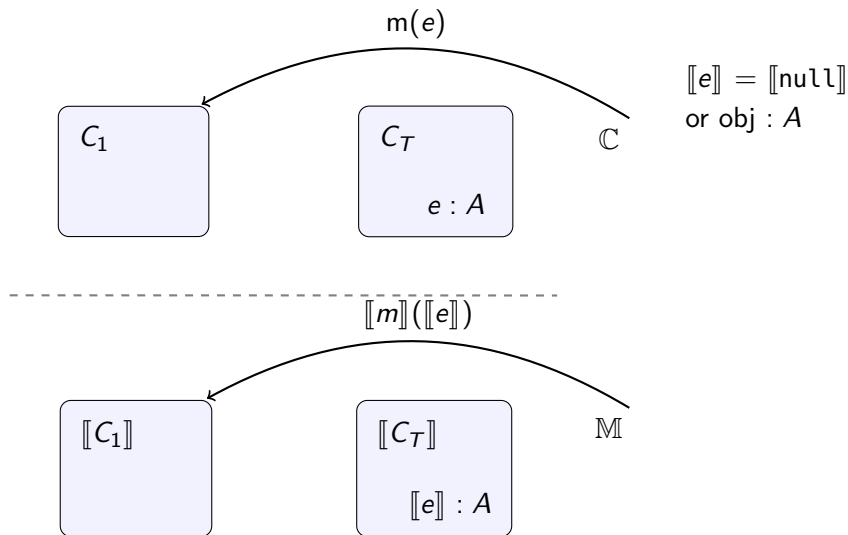
# Addressing Object Faking



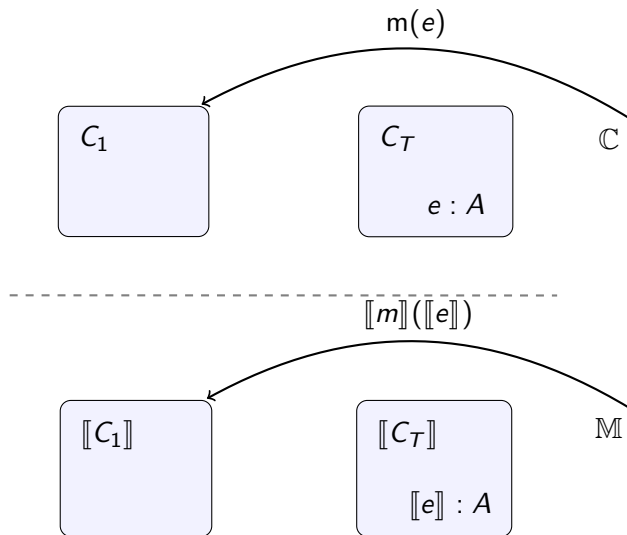
# Addressing Object Faking



# Addressing Object Faking



# Addressing Object Faking



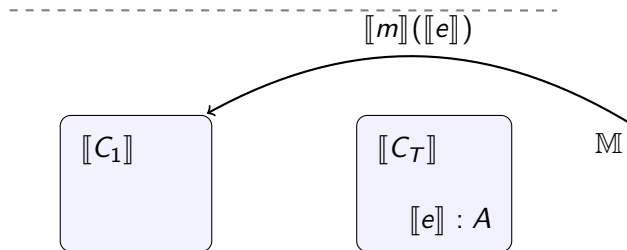
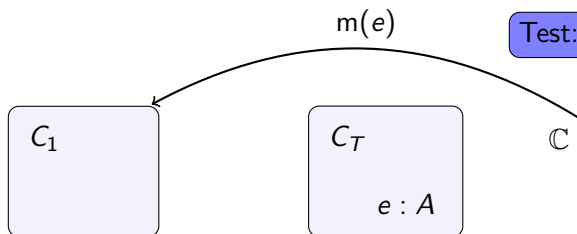
$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
 or  $\text{obj} : A$   
 or  $\text{obj} : B \not\leq A$   
 or garbage



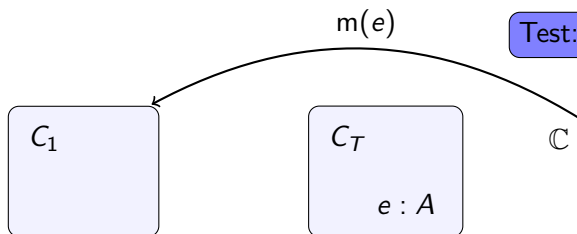
# Addressing Object Faking

Test: Type & Existence

$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
or  $\text{obj} : A$   
or  $\text{obj} : B \not\leq A$   
or garbage

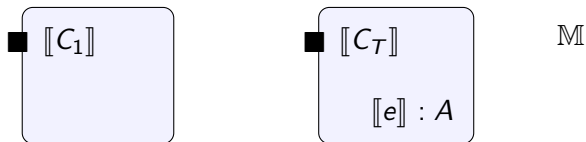


# Addressing Object Faking

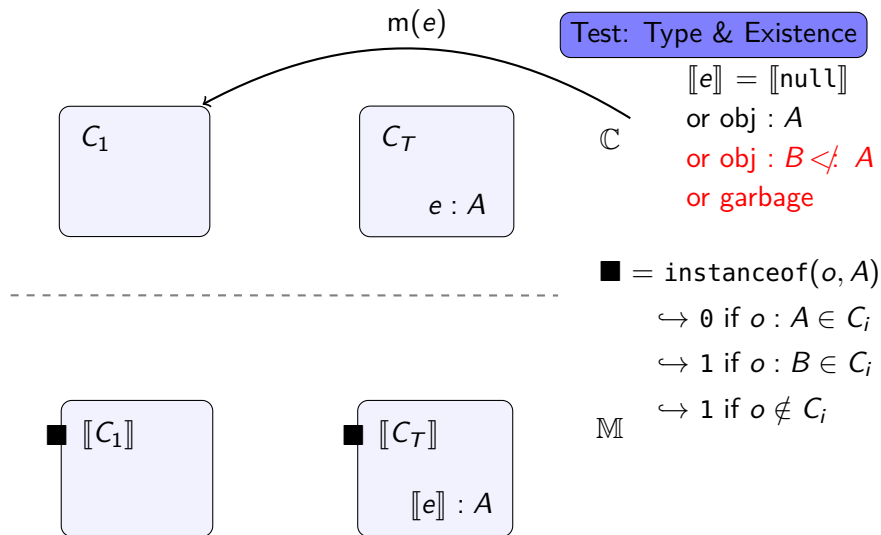


Test: Type & Existence

$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
 or  $\text{obj} : A$   
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 or garbage



# Addressing Object Faking



# Addressing Object Faking

Test: Type & Existence

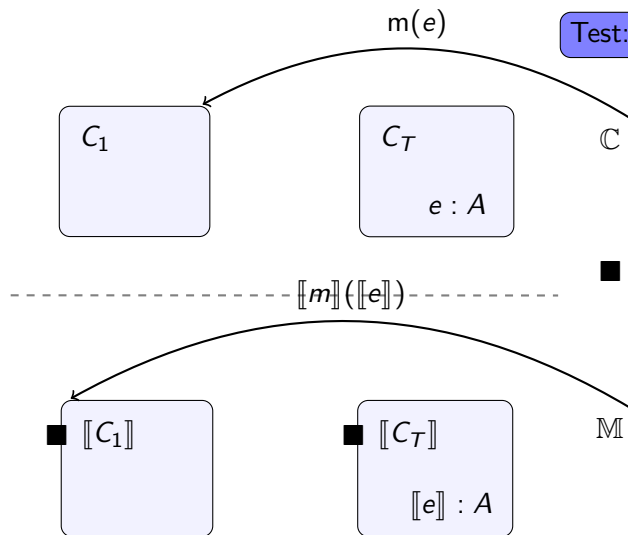
$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
or  $\text{obj} : A$   
or  $\text{obj} : B \not\leq A$   
or garbage

$\blacksquare = \text{instanceof}(o, A)$

$\hookrightarrow 0$  if  $o : A \in C_i$

$\hookrightarrow 1$  if  $o : B \in C_i$

$\hookrightarrow 1$  if  $o \notin C_i$



# Addressing Object Faking

Test: Type & Existence

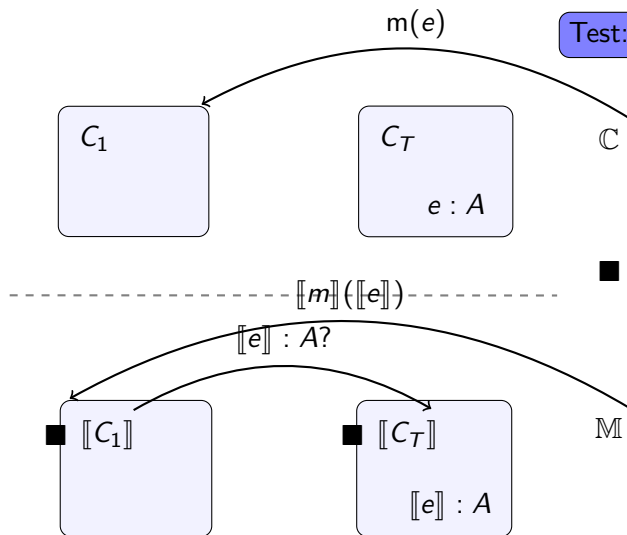
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Test: Type & Existence

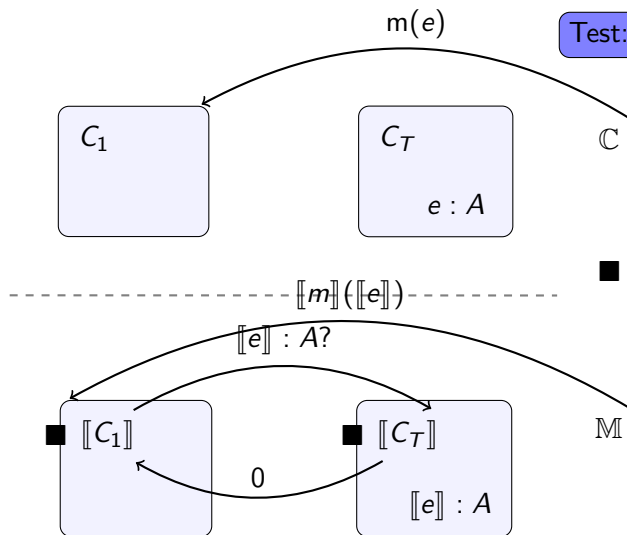
$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
or  $\text{obj} : A$   
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Test: Type & Existence

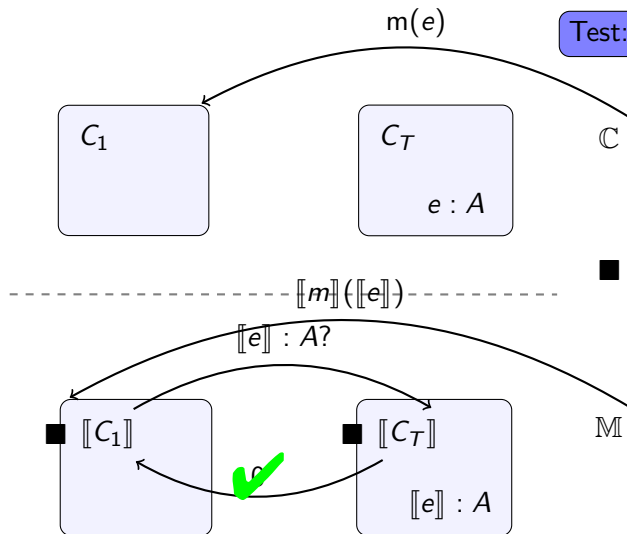
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Test: Type & Existence

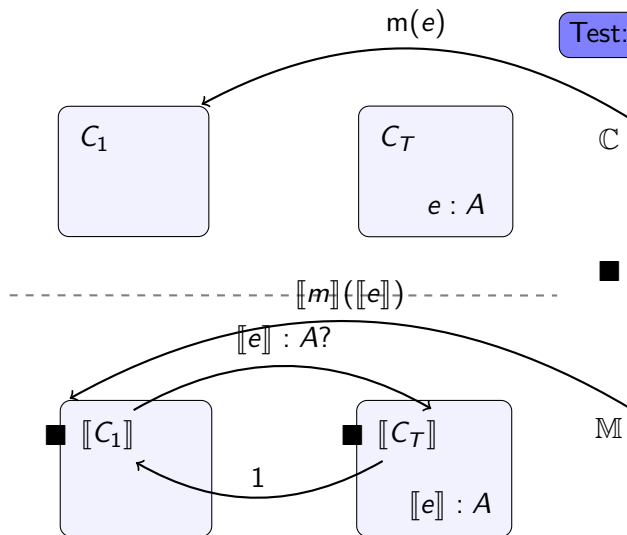
$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
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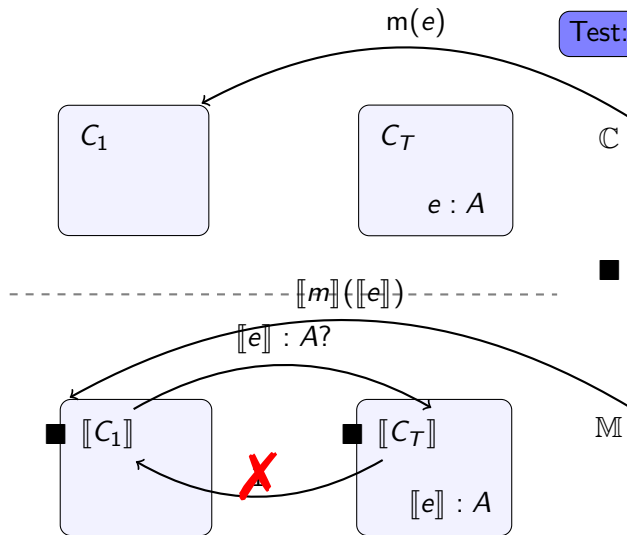




# Addressing Object Faking

Test: Type & Existence

$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
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Test: Type & Existence

$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
or  $\text{obj} : A$   
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or garbage

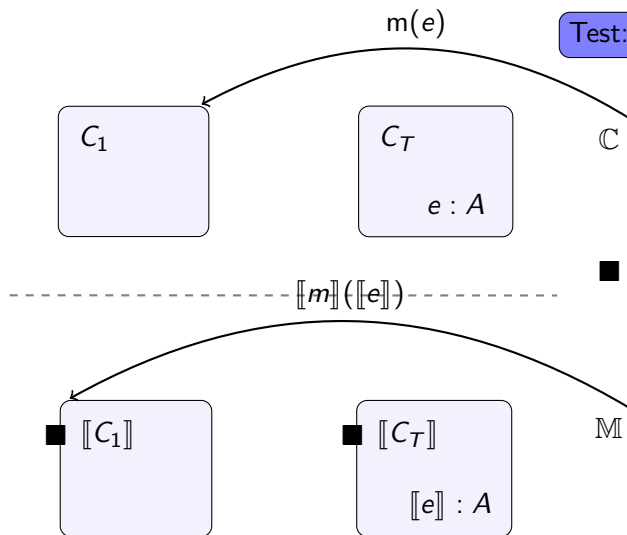
$\blacksquare = \text{instanceof}(o, A)$

$\hookrightarrow 0$  if  $o : A \in C_i$

$\hookrightarrow 1$  if  $o : B \in C_i$

$\hookrightarrow 1$  if  $o \notin C_i$

$\blacksquare \in \mathcal{S}$



# Addressing Object Faking

Test: Type & Existence

$\llbracket e \rrbracket = \llbracket \text{null} \rrbracket$   
or  $\text{obj} : A$   
or  $\text{obj} : B \not\leq A$   
or garbage

$\blacksquare = \text{instanceof}(o, A)$

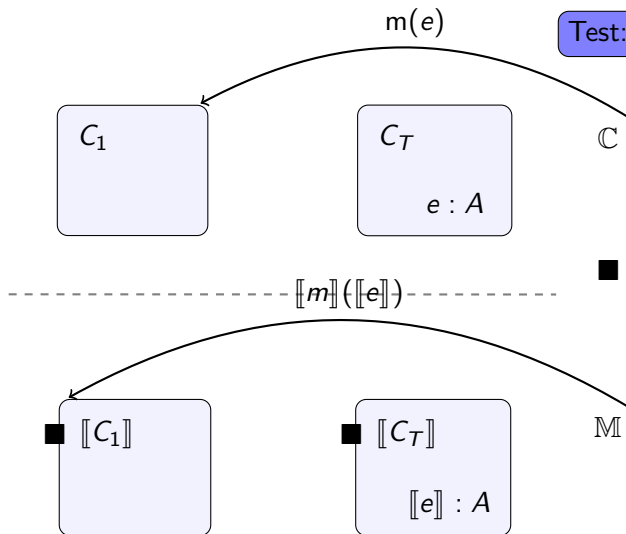
$\hookrightarrow 0$  if  $o : A \in C_i$

$\hookrightarrow 1$  if  $o : B \in C_i$

$\hookrightarrow 1$  if  $o \notin C_i$

$\blacksquare \in \mathcal{S}$

$\blacksquare \notin C_i$



# Questions

Thank you!

Qs ?