Secure Compilation to Protected Module Architectures

Marco Patrignani ¹ Dave Clarke ^{1,2} Frank Piessens ¹

¹iMinds-DistriNet, Dept. Computer Science, KU Leuven, Belgium first.last@cs.kuleuven.be
²Dept. Information Technology, Uppsala University, Sweden first.last@it.uu.se

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Goal of the Talk

• introduce my research on secure compilation

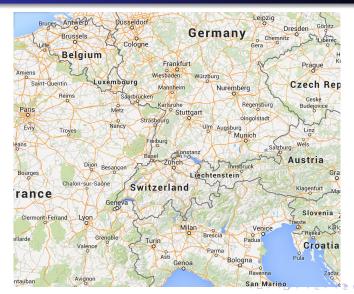
Goal of the Talk

- introduce my research on secure compilation
- define secure compilation and related notions

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- introduce my research on secure compilation
- define secure compilation and related notions
- point out open challenges

Me



Outline

- Background (What are Secure Compilation and PMA?)
 - Secure Compilation
 - PMA and Isolation
 - Fully Abstract Trace Semantics for PMA
- Secure Compilation of J+E
 - Source Language J+E
 - Secure Compilation, Informally
 - Proof Strategy
- Open Challenges

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What is a Secure Program?

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- a program is secure if it enjoys at least a security property
- a security property is one expressible via program equivalence (e.g. confidentiality, integrity, etc.)

What is a Secure Compiler?

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- a fully abstract compiler is a secure compiler

PMA and Isolation
Fully Abstract Trace Semantics for PMA

Benefits of Fully abstract Compilation

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protect against code injection attacks

Benefits of Fully abstract Compilation

Fully abstract compilation preserves source-level abstractions in target-level languages

- protect against code injection attacks
- enables source-level reasoning

What is a Protected Modules Architecture?

• deep encapsulation at the lowest level of abstraction

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- deep encapsulation at the lowest level of abstraction
- the basis of several security-related works
- Intel wants to port it to future processors (SGX)

```
0 \times 0001
            call 0xb53
0 \times 0002
            movs r_0 0x0b55
0 \times 0 h52
            movs r_0 0x0b55
0x0b53
            call 0x0002
0x0b54
            movs r_0 0x0001
0x0b55
             . . .
0xab00
             jmp 0xb53
0xab01
```

memory space

```
0 \times 0001
            call 0xb53
0 \times 0002
            movs r_0 0x0b55
0x0b52
            movs r_0 0x0b55
0x0b53
            call 0x0002
0x0b54
            movs r_0 0x0001
0x0b55
            . . .
0xab00
            jmp 0xb53
0xab01
```

- memory space
- protected module = protected memory

```
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            call 0x0002
0x0b54
            movs r_0 0x0001
0x0b55
            . . .
0xab00
            jmp 0xb53
0xab01
```

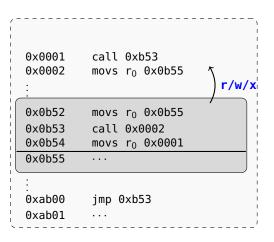
- memory space
- protected module = protected memory
- split in code and data

```
0 \times 0001
            call 0xb53
0 \times 0002
            movs r_0 0x0b55
0x0b52
            movs r_0 0x0b55
0x0b53
            call 0x0002
                                     r/w
0x0b54
            movs r_0 0x0001
0x0b55
            . . .
0xab00
            jmp 0xb53
0xab01
```

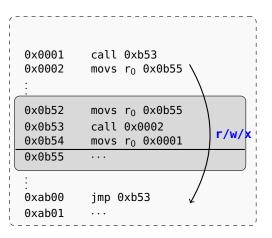
- memory space
- protected module = protected memory
- split in code and data
- protected code is unrestricted

```
0 \times 0001
            call 0xb53
0 \times 0002
            movs r_0 0x0b55
0x0b52
            movs r_0 0x0b55
                                    r/x
            call 0x0002
0x0b53
0x0b54
            movs r_0 0x0001
0x0b55
            . . .
0xab00
            jmp 0xb53
0xab01
```

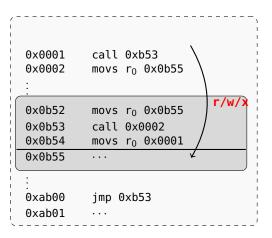
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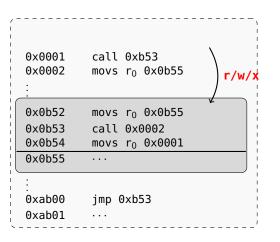
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- memory space
- protected module = protected memory
- split in code and data
- protected code is unrestricted
- unprotected code is restricted



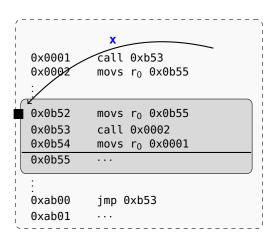
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- split in code and data
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- entry points for communication (■)



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- protected module = protected memory
- split in code and data
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- unprotected code is restricted
- entry points for communication (■)

```
0×0001
           call 0xb52
0x0002
0x0b52
           movi r_0 1
0x0b53
           movi r_1 0x0b56
0x0b54
           jl r_1
0x0b55
           call 0xab01
0x0b56
           ret
0xab01
```

behaviour in this case is:

```
call 0xb52
0x0001
0x0002
0x0b52
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           call 0xab01
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           ret
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```

behaviour in this case is:call in

```
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             call 0xb52
₹0×0002
 0x0b52
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 0x0b53
             movi r_1 0x0b56
 0x0b54
             jl r<sub>1</sub>
 0k0b55
             call 0xab01
 0x0b56
             ret
 0xab01
```

behaviour in this case is: call in, ret 1

```
0x0001
           call 0xb52
0x0002
0x0b52
           movi r_0 1
0x0b53
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0x0b54
           jl r_1
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           ret
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```

 behaviour in this case is: call in, ret 1 or call in,

```
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           call 0xb52
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           movi r_1 0x0b56
0x0b54
           jl r_1
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Trace Semantics for PMA

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0x0b55
           call 0xab01
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```

- behaviour in this case is: call in, ret 1 or call in, call out
- traces rely only on the PMA code

Trace Semantics for PMA

```
0x0001
              call 0xb52
0x0002
0x0b52
             movi r_0 1
0x0b53
             movi r_1 0x0b56
0 \times 0 \text{ b} 54
             jl r<sub>1</sub>
0x0b55
              call 0xab01
0x0b56
              ret
0xab01
```

- behaviour in this case is: call in, ret 1 or call in, call out
- traces rely only on the PMA code
- they describe what can be observed from the outside of protected PMA code

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 - private fields
 - programming to an interface
 - exceptions

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- component-based
- private fields
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- exceptions

```
package PI;
    interface Account {
     public createAccount() : Foo;
    extern extAccount : Account;
  package PE;
    class AccountClass
      implements PI.Account {
     AccountClass() { counter = 0; }
10
      public createAccount() : Account {
11
       return new PE.AccountClass();
12
13
14
15
     private counter : Int;
16
    object extAccount : AccountClass;
17
```

```
 source language: +/- Java jr

    component-based

     private fields
     programming to an
                                 10
       interface
                                 11
     exceptions
                                 12
                                 13
Q: How to securely compile
                                 14
                                 15
         this code?
                                 16
                                 17
```

```
package PI;
 interface Account {
   public createAccount() : Foo;
 extern extAccount : Account:
package PE;
 class AccountClass
   implements PI.Account {
   AccountClass() { counter = 0; }
   public createAccount() : Account {
    return new PE.AccountClass();
   private counter : Int;
 object extAccount : AccountClass;
```

```
package PI;
    interface Account {
     public createAccount() : Foo;
    extern extAccount : Account:
  package PE;
    class AccountClass
      implements PI.Account {
     AccountClass() { counter = 0; }
10
      public createAccount() : Account {
11
12
       return new PE.AccountClass();
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15
     private counter : Int;
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    object extAccount : AccountClass;
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```

Dynamic dispatch

v-tables

Secure stack

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■ proxy to createAccount

Dynamic dispatch

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    object extAccount : AccountClass;
17
```

```
proxy to createAccount
createAccount body
constructor
Dynamic dispatch
v-tables
Secure stack
extAccount
 counter
```

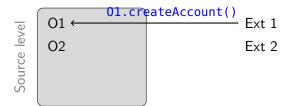
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Source level

O1 O2

Ext 1

Ext 2

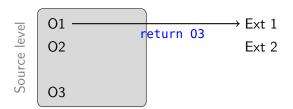


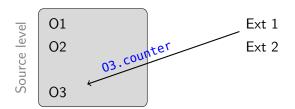
Source level

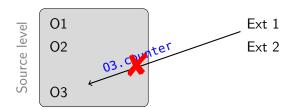
O1 O2 O3

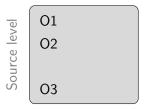
Ext 1

Ext 2



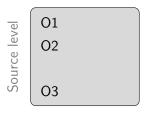






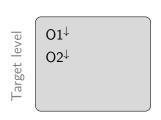
Ext 1

Ext 2



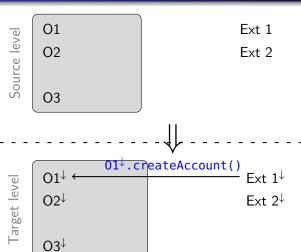
Ext 1

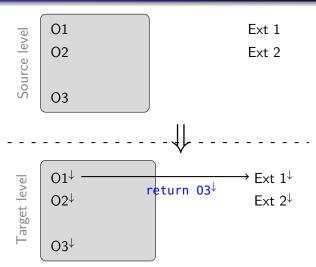
Ext 2

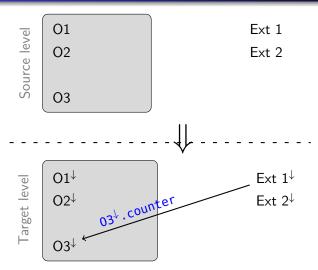


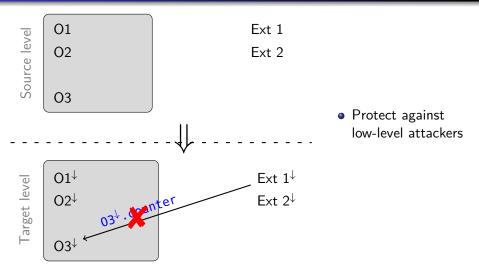
Ext 1[↓]

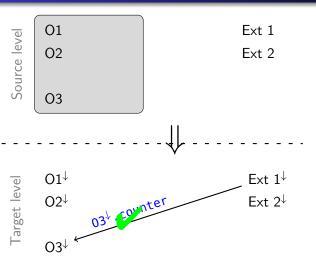
Ext 2^{\downarrow}











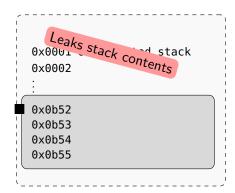
- Protect against low-level attackers
- Target code is vulnerable without PMA

```
0x0001 Unprotected stack
0x0002
:

0x0b52
0x0b53
0x0b54
0x0b55
```

Q: : Is that all?

protected stack

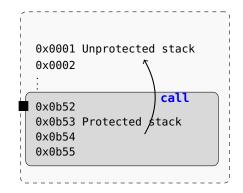


Q: : Is that all?

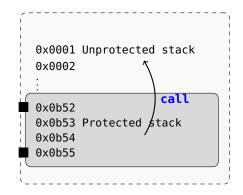
protected stack



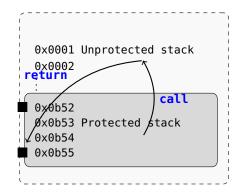
- protected stack
- returnback entry point



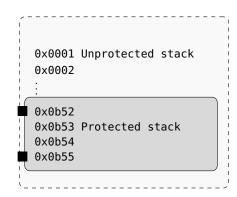
- protected stack
- returnback entry point



- protected stack
- returnback entry point



- protected stack
- returnback entry point
- reset flags and registers



- protected stack
- returnback entry point
- reset flags and registers



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- returnback entry point
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- returnback entry point
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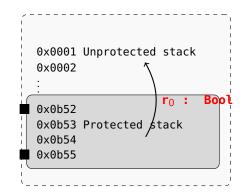
- protected stack
- returnback entry point
- reset flags and registers
- ground-typed values check



Secure Compilation of Outcalls

Q: : Is that all?

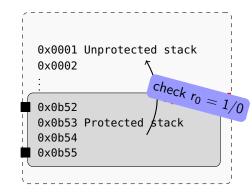
- protected stack
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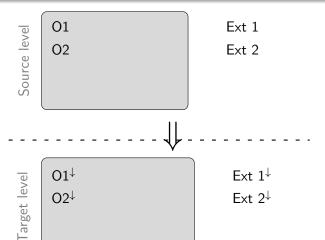


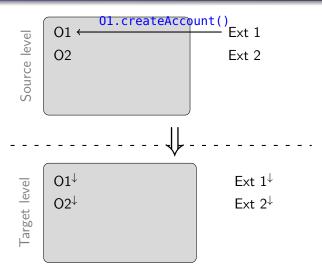
Secure Compilation of Outcalls

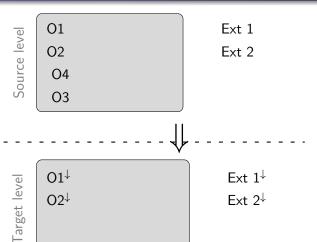
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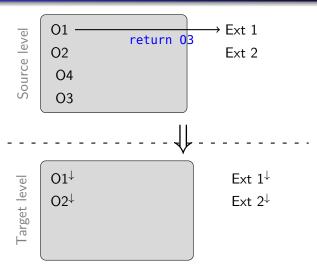
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- returnback entry point
- reset flags and registers
- ground-typed values check

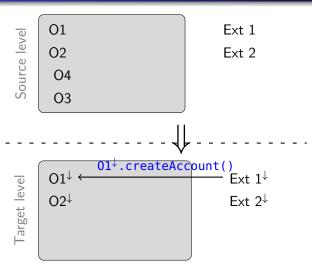


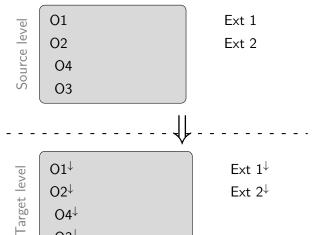




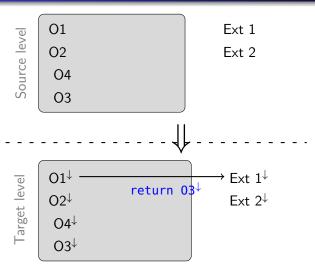


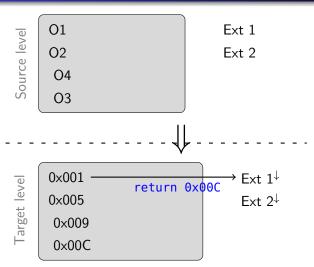


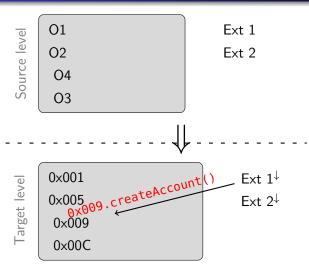


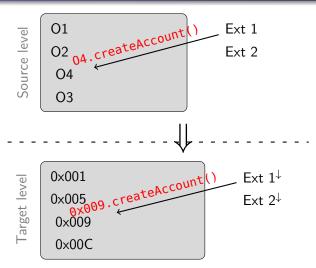


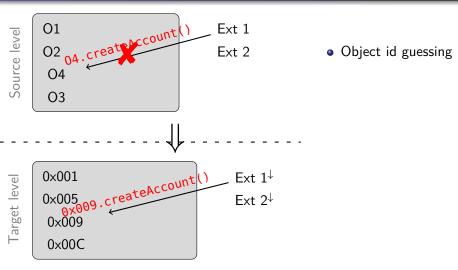
 $O4^{\downarrow}$ O3[↓]

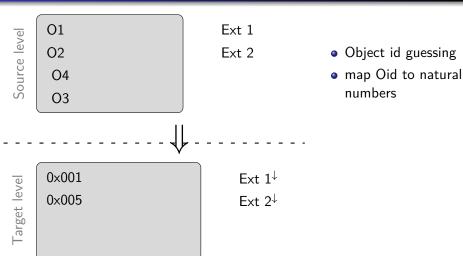


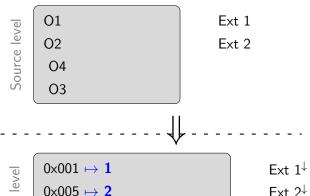




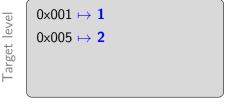


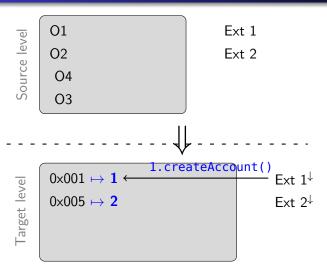




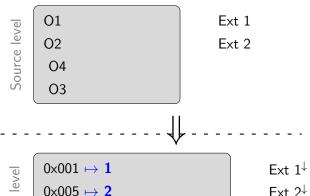


- Object id guessing
- map Oid to natural numbers

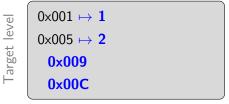


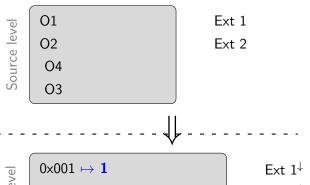


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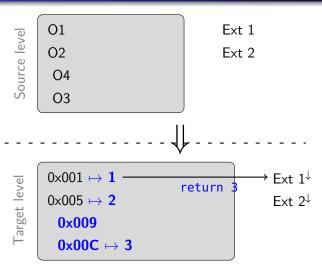




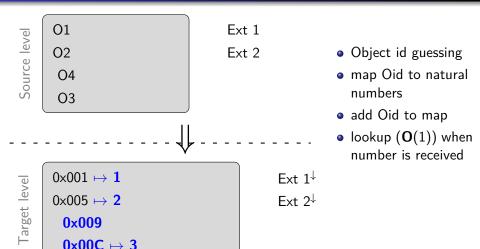
- Object id guessing
- map Oid to natural numbers
 - add Oid to map

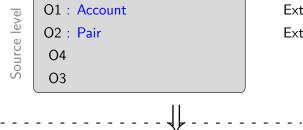


Ext 2↓



- Object id guessing
- map Oid to natural numbers
- add Oid to map





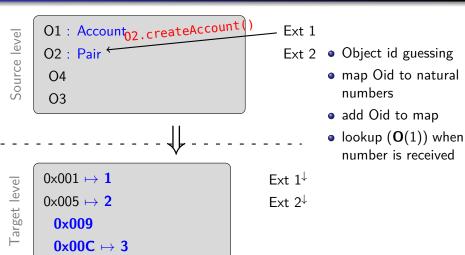
Ext 1

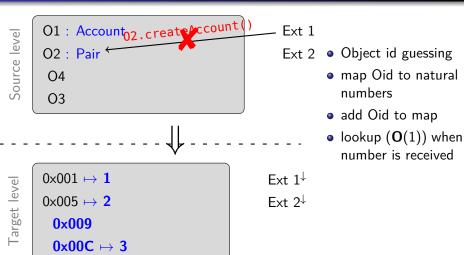
- Ext 2 Object id guessing
 - map Oid to natural numbers
 - add Oid to map
 - lookup (O(1)) when number is received

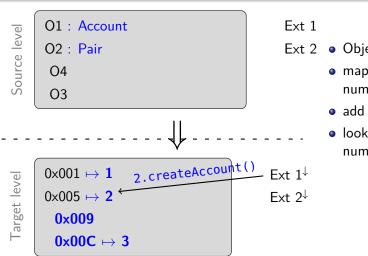


Ext 1[↓]

Ext 2↓

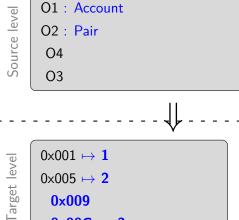






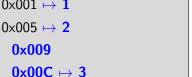
xt 2 • Object id guessing

- 2 Object to guessing
 - map Oid to natural numbers
 - add Oid to map
 - lookup (O(1)) when number is received



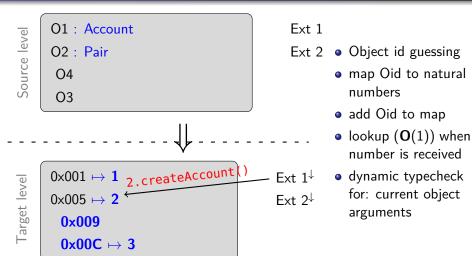
Ext 1

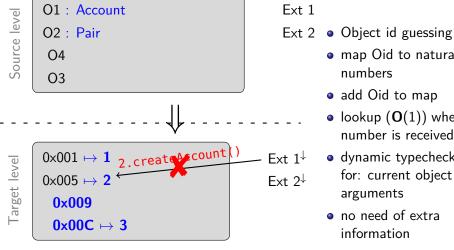
- Object id guessing
 - map Oid to natural numbers
 - add Oid to map
 - lookup (**O**(1)) when number is received
 - dynamic typecheck for: current object



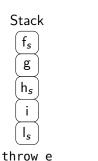
Ext 1[↓]

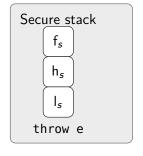
Ext 2↓

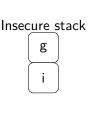


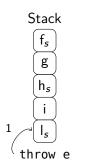


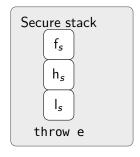
- map Oid to natural numbers
- add Oid to map
- lookup (**O**(1)) when number is received
- dynamic typecheck for: current object arguments
- no need of extra information

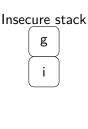


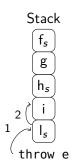


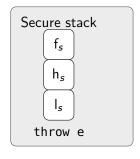


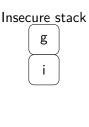


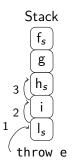


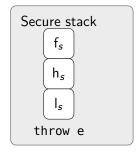




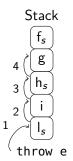


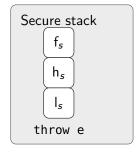




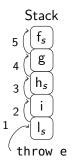


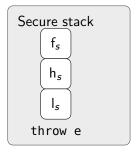


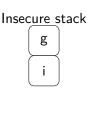


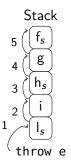


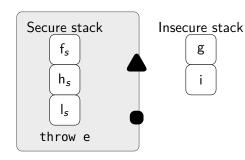


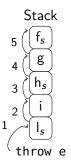


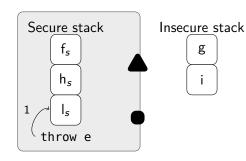


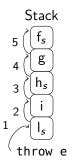


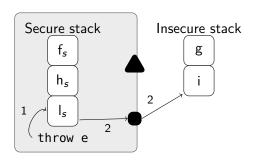






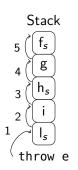


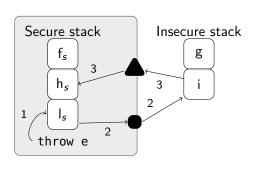




Record passed exceptions

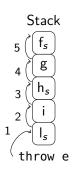
Exceptions

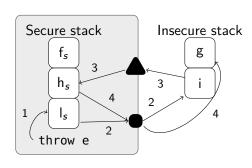




Record passed exceptions
Check that exception could be thrown

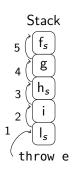
Exceptions

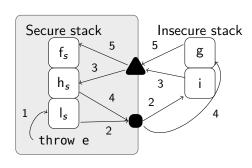




Record passed exceptions Check that exception could be thrown

Exceptions





Record passed exceptions Check that exception could be thrown

Additional Features

- cross-package inheritance
- inner classes
- ML-like modules & functors (joint work with A. Larmuseau from UU)

• We have a strategy to securely compile J+E code

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Q: What is missing?

- We have a strategy to securely compile J+E code
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- We have an idea of the security properties of our secure compilation scheme



Secure Compilation, Formally

$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

Secure Compilation, Formally

$$C_1 \stackrel{\downarrow}{\rightleftharpoons} C_2 \iff C_1^{\downarrow} \stackrel{\wedge}{\rightleftharpoons} C_2^{\downarrow}$$

Secure Compilation, Formally

$$C_1 \stackrel{\downarrow}{ } C_2 \iff C_1^{\downarrow} \stackrel{\bigwedge}{ } C_2^{\downarrow}$$

$$C_1 \simeq^{\mathcal{S}} C_2 \triangleq \forall \mathbb{C}. \ \mathbb{C}[C_1] \Uparrow \iff \mathbb{C}[C_2] \Uparrow$$

$$C_1 \simeq^{\mathcal{S}} C_2 \triangleq \bigvee \mathbb{C}[C_1] \uparrow \iff \mathbb{C}[C_2] \uparrow \uparrow$$

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All contexts

$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$(\forall \mathbb{C}. \ \mathbb{C}[C_1] \Uparrow \iff \mathbb{C}[C_2] \Uparrow) \iff (\forall \mathbb{M}. \ \mathbb{M}[C_1^{\downarrow}] \Uparrow \iff \mathbb{M}[C_2^{\downarrow}] \Uparrow)$$

$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

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$$C_1 \simeq^{\mathsf{J} + \mathsf{E}} C_2 \;\; \leftarrow \;\; C_1^{\downarrow} \simeq^{\mathsf{A} + \mathsf{I}} C_2^{\downarrow}$$

$$C_1 \simeq^{\mathsf{J+E}} C_2 \quad C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$C_1 \simeq^{\mathsf{J} + \mathsf{E}} C_2 \ \Rightarrow \ C_1^{\downarrow} \simeq^{\mathsf{A} + \mathsf{I}} C_2^{\downarrow}$$

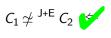
$$C_1 \simeq^{\mathsf{J+E}} C_2 \quad \Rightarrow \qquad \qquad C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow} \\ \updownarrow \\ \mathsf{Traces}(C_1^{\downarrow}) = \mathsf{Traces}(C_2^{\downarrow})$$

Fully Abstract Trace Semantics

$$C_1 \not\simeq {}^{\mathsf{J+E}} C_2 \ \Leftarrow \ \mathsf{Traces}(C_1^\downarrow) \not= \mathsf{Traces}(C_2^\downarrow)$$

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$$C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$\updownarrow$$

$$\mathsf{Traces}(C_1^{\downarrow}) = \mathsf{Traces}(C_2^{\downarrow})$$

Fully Abstract Trace Semantics

Outline

- Background (What are Secure Compilation and PMA?)
 - Secure Compilation
 - PMA and Isolation
 - Fully Abstract Trace Semantics for PMA
- Secure Compilation of J+E
 - Source Language J+E
 - Secure Compilation, Informally
 - Proof Strategy
- Open Challenges

Multi-principal Languages

• current model has a single secure entity

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Q: How can we improve on this?

Multi-principal Languages

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Q: How can we improve on this?

 secure compilation of languages with multiple security principals

Better Proof Technique

current proof technique is unpleasant

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Q: How can we improve on this?

 develop a more scalable proof technique (most likely based on logical relations)

Garbage Collection

current secure compilation scheme does not account for garbage collection

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find a suitable technique (distributed GC-based)

Garbage Collection

 current secure compilation scheme does not account for garbage collection

Q: How can we improve on this?

- find a suitable technique (distributed GC-based)
- prove that it does not introduce security leaks

Q: Are there language features that cannot be securely compiled through PMA?

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• how to formalise this statement?

- Q: Are there language features that cannot be securely compiled through PMA?
 - how to formalise this statement?
 - i think the answer is NO

Questions



Qs?

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$$\mathsf{TR} = \left\{ \alpha = \left\{ \begin{matrix} \overset{i}{\rightarrow}; \\ \mathsf{call} \ \mathsf{p} \ \overline{\mathsf{r}} \\ \mathsf{ret} \ \mathsf{r}_{\mathsf{0}} \end{matrix} \right\}; \right\}$$

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 - flags convey information across function calls
 - registers besides r_θ in ret as well

Fully Abstract Trace Semantics

To ensure maximal precision, prove the trace semantics to be fully abstract

Fully Abstract Trace Semantics

To ensure maximal precision, prove the trace semantics to be fully abstract

i.e. there are no other things that we missed

$$\mathsf{TR} = \left\{ \alpha = \left\{ \begin{matrix} \overset{i}{\hookrightarrow}; \\ \mathsf{call} \ \mathsf{p} \ \overline{\mathsf{r}} \\ \mathsf{ret} \ \mathsf{r}_{\mathsf{\theta}} \end{matrix} \right\}; \right\}$$

$$\mathsf{TR} = \left\{ \alpha = \left\{ \begin{matrix} \frac{i}{\gamma}; \\ \mathsf{call} \ \mathsf{p} \ \overline{\mathsf{r}} \\ \mathsf{ret} \ \mathsf{r}_{\mathsf{0}} \\ \end{matrix} \right\}; \right\}$$

$$\mathsf{TR}_{\mathsf{L}} = \left\{ \alpha = \left\{ \begin{matrix} \frac{i}{\gamma}; \\ \mathsf{call} \ \overline{\mathsf{r}} \ \overline{\mathsf{f}} \\ \mathsf{ret} \ \overline{\mathsf{r}} \ \overline{\mathsf{f}} \\ \mathsf{movs} \ \mathsf{r} \ \mathsf{v} \\ \overset{\alpha}{\Longrightarrow} \end{matrix} \right\}; \right\}$$

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$$\mathsf{TR}_{\mathsf{S}} = \left\{ \alpha = \left\{ \begin{matrix} \frac{i}{\gamma}; \\ \mathsf{call} \ \mathsf{p} \ \bar{\mathsf{r}} \\ \mathsf{ret} \ \mathsf{r}_{\theta} \\ \xrightarrow{\alpha} \right\}; \right\}$$

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$$TR = \begin{cases} \alpha = \begin{cases} \frac{i}{\gamma}; \\ \text{call p } \overline{r} \\ \text{ret } r_{\theta} \end{cases}; \end{cases}$$

$$TR_{L} = \begin{cases} \alpha = \begin{cases} \frac{i}{\gamma}; \\ \text{call } \overline{r} \\ \text{ret } \overline{r} \\ \text{movs r v} \\ \frac{\alpha}{\Rightarrow} \end{cases}; \end{cases}$$

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$$\mathsf{TR}_\mathsf{X}(\mathcal{C}_1) = \mathsf{TR}_\mathsf{X}(\mathcal{C}_2) \iff \mathcal{C}_1 \simeq \mathcal{C}_2$$