Secure Compilation to Protected Module Architectures

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Goal of the Talk

• present my research on secure compilation

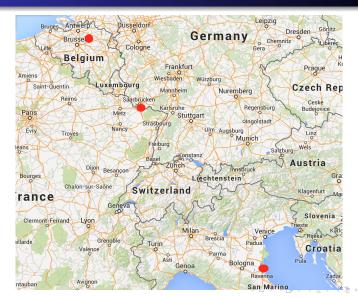
Goal of the Talk

- present my research on secure compilation
- define secure (fully-abstract) compilation

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- present my research on secure compilation
- define secure (fully-abstract) compilation
- discuss present and future work

Me



Outline

- Background (What are Secure Compilation and PMA?)
 - Secure Compilation
 - PMA and Isolation
 - Fully Abstract Trace Semantics for PMA
- Secure Compilation of J+E
 - Source Language J+E
 - Secure Compilation, Informally
 - Proof Strategy
- Recent Work

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- Background (What are Secure Compilation and PMA?)
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- a program is secure if it enjoys at least a security property
- a security property is one expressible via program equivalence (e.g. confidentiality, integrity, etc.)

What is a Secure Compiler?

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- a compiler is a function from source to target programs
- a compiler is secure if it preserves source-level security properties in the programs it generates no more, no less
- a fully abstract compiler is a secure compiler

Fully abstract compilation preserves source-level abstractions in target-level languages

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Fully abstract compilation preserves source-level abstractions in target-level languages

- protect against code injection attacks
- enables source-level reasoning

What is a Protected Modules Architecture?

deep encapsulation at the lowest level of abstraction

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- deep encapsulation at the lowest level of abstraction
- the basis of several security-related works
- Intel wants to port it to future processors (SGX)

```
0 \times 0001
            call 0xb53
0 \times 0002
            movs r_0 0x0b55
0 \times 0 h52
            movs r_0 0x0b55
0x0b53
            call 0x0002
0x0b54
            movs r_0 0x0001
0x0b55
             . . .
0xab00
             jmp 0xb53
0xab01
```

memory space

```
0 \times 0001
            call 0xb53
0 \times 0002
            movs r_0 0x0b55
0x0b52
            movs r_0 0x0b55
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            call 0x0002
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0x0b55
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            jmp 0xb53
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```

- memory space
- protected module = protected memory

```
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            movs r_0 0x0001
0x0b55
            . . .
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            jmp 0xb53
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```

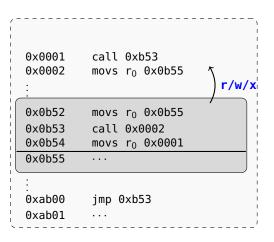
- memory space
- protected module = protected memory
- split in code and data

```
0 \times 0001
            call 0xb53
0 \times 0002
            movs r_0 0x0b55
0x0b52
            movs r_0 0x0b55
0x0b53
            call 0x0002
                                     r/w
0x0b54
            movs r_0 0x0001
0x0b55
            . . .
0xab00
            jmp 0xb53
0xab01
```

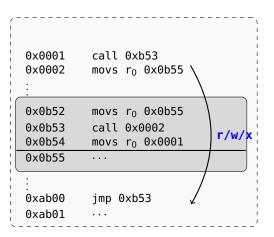
- memory space
- protected module = protected memory
- split in code and data
- protected code is unrestricted

```
0 \times 0001
            call 0xb53
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            movs r_0 0x0b55
0x0b52
            movs r_0 0x0b55
                                    r/x
            call 0x0002
0x0b53
0x0b54
            movs r_0 0x0001
0x0b55
            . . .
0xab00
            jmp 0xb53
0xab01
```

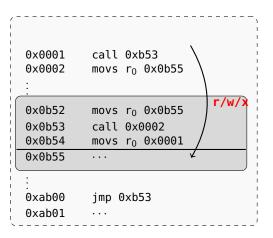
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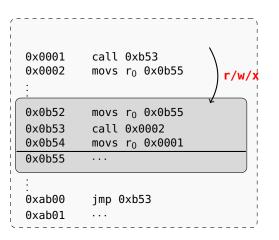
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- memory space
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- split in code and data
- protected code is unrestricted
- unprotected code is restricted



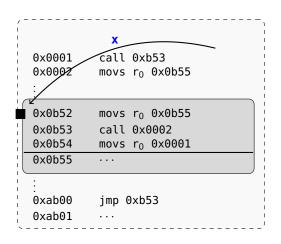
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```
0×0001
           call 0xb52
0x0002
0x0b52
           movi r_0 1
0x0b53
           movi r_1 0x0b56
0x0b54
           jl r_1
0x0b55
           call 0xab01
0x0b56
           ret
0xab01
```

behaviour in this case is:

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0x0001
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```

behaviour in this case is:call in

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₹0×0002
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 0k0b55
             call 0xab01
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             ret
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```

behaviour in this case is: call in, ret 1

```
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           call 0xb52
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           ret
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 behaviour in this case is: call in, ret 1 or call in,

```
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Trace Semantics for PMA

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- behaviour in this case is: call in, ret 1 or call in, call out
- traces rely only on the PMA code

Trace Semantics for PMA

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0 \times 0 \text{ b} 54
             jl r<sub>1</sub>
0x0b55
              call 0xab01
0x0b56
              ret
0xab01
```

- behaviour in this case is: call in, ret 1 or call in, call out
- traces rely only on the PMA code
- they describe what can be observed from the outside of protected PMA code

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 - private fields
 - programming to an interface
 - exceptions

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- component-based
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- exceptions

```
package PI;
    interface Account {
     public createAccount() : Foo;
    extern extAccount : Account;
  package PE;
    class AccountClass
      implements PI.Account {
     AccountClass() { counter = 0; }
10
      public createAccount() : Account {
11
       return new PE.AccountClass();
12
13
14
15
     private counter : Int;
16
    object extAccount : AccountClass;
17
```

```
 source language: +/- Java jr

    component-based

     private fields
     programming to an
       interface
     exceptions
Q: How to securely compile
         this code?
```

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     public createAccount() : Foo;
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Dynamic dispatch

v-tables

Secure stack

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■ proxy to createAccount

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    object extAccount : AccountClass;
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```

```
proxy to createAccount
createAccount body
constructor
Dynamic dispatch
v-tables
Secure stack
extAccount
 counter
```

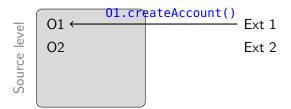
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Source level

O1 O2

Ext 1

Ext 2

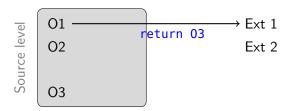


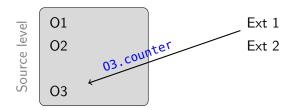
Source level

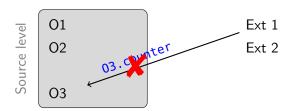
O1 O2 O3

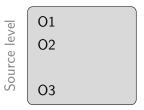
Ext 1

Ext 2





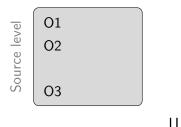




Ext 1

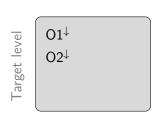
Ext 2





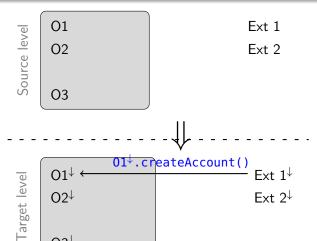
Ext 1

Ext 2

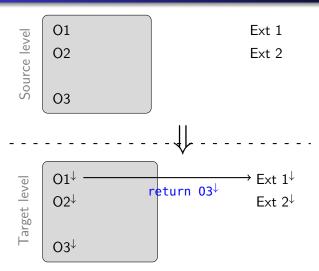


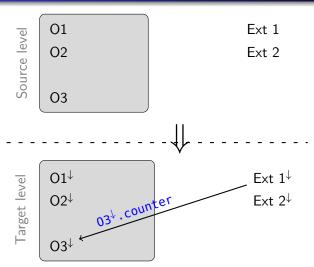
Ext 1[↓]

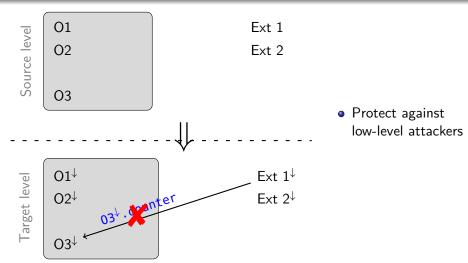
Ext 2^{\downarrow}

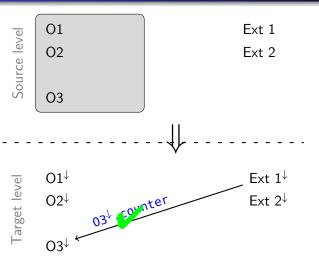


O3[↓]







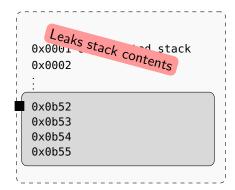


- Protect against low-level attackers
- Target code is vulnerable without PMA

```
0x0001 Unprotected stack
0x0002
:
0x0b52
0x0b53
0x0b54
0x0b55
```

Q: : Is that all?

protected stack

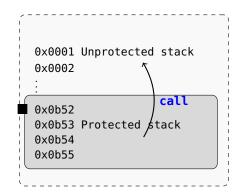


Q: : Is that all?

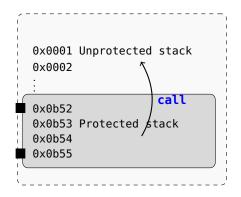
protected stack



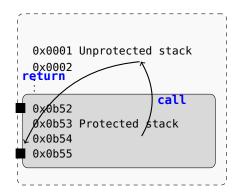
- protected stack
- returnback entry point



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- protected stack
- returnback entry point



- protected stack
- returnback entry point
- reset flags and registers



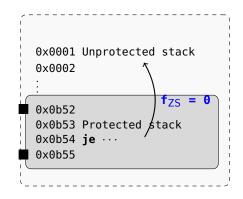
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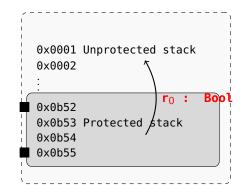
- protected stack
- returnback entry point
- reset flags and registers
- ground-typed values check



Secure Compilation of Outcalls

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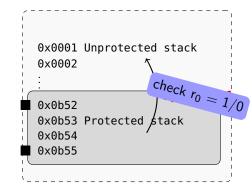
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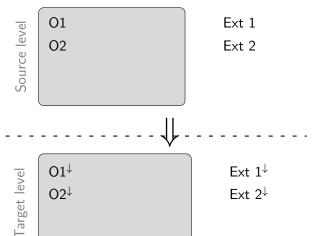


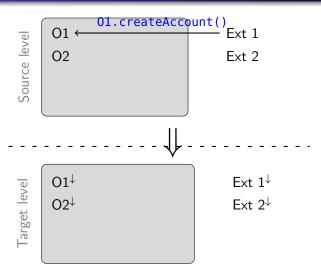
Secure Compilation of Outcalls

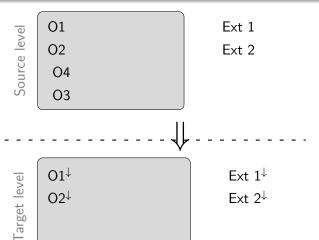
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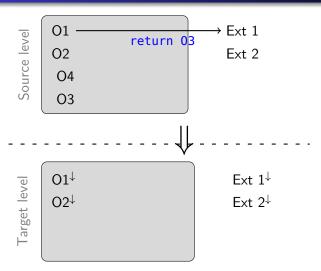
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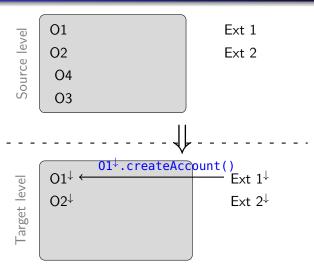


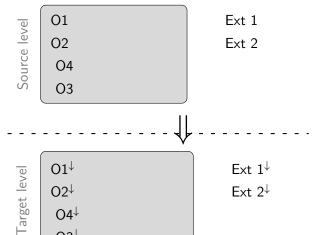




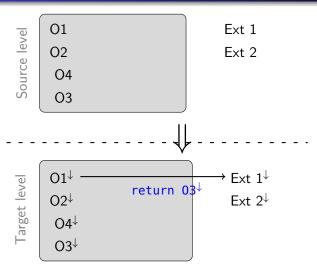


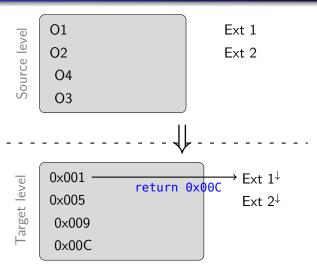


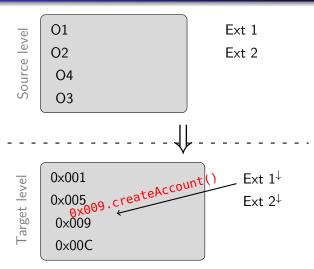


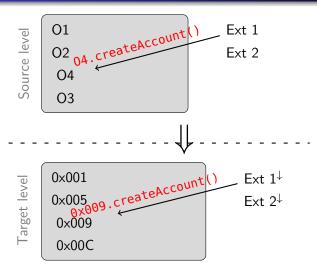


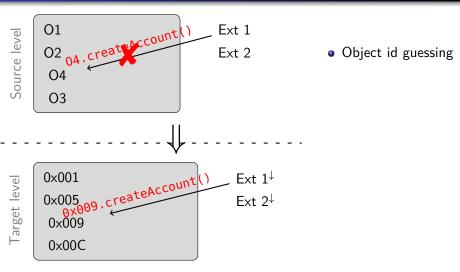
 $O4^{\downarrow}$ O3[↓]

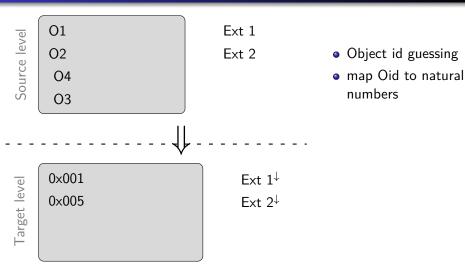


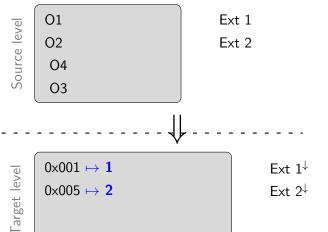




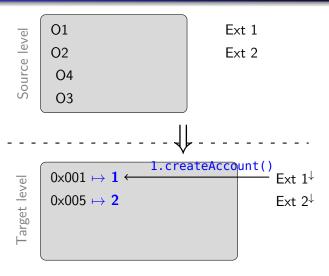




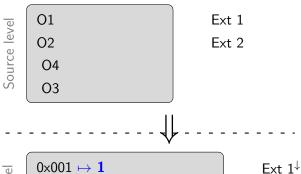




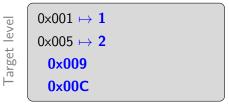
- Object id guessing
- map Oid to natural numbers



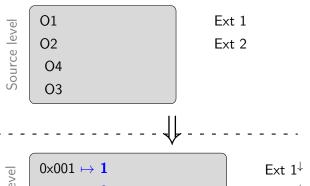
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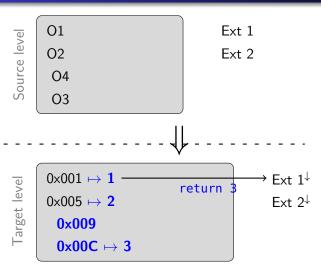
Ext 2↓



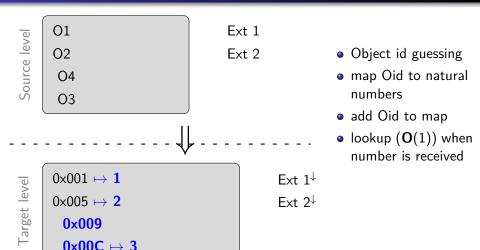
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Ext 2↓



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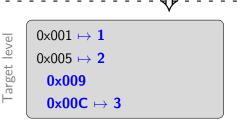




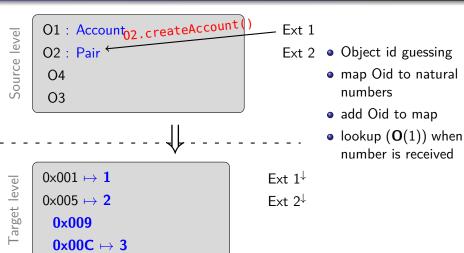
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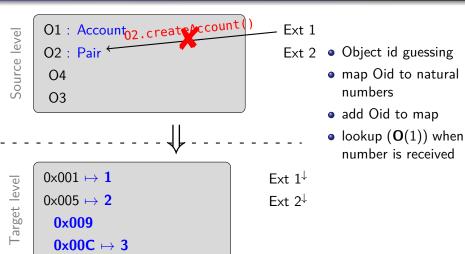
Ext 2 • Object id guessing

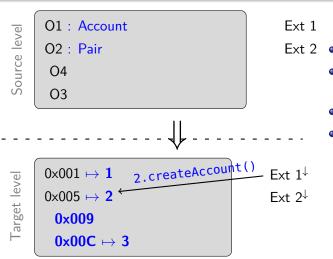
- map Oid to natural numbers
- add Oid to map
- lookup (O(1)) when number is received



Ext 1[↓] Ext 2↓

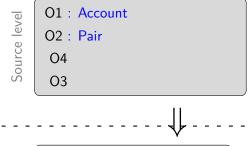






kt 1

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 - map Oid to natural numbers
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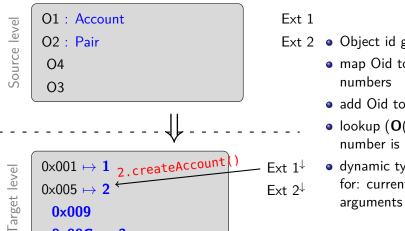
Ext 1

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 dynamic typecheck for: current object

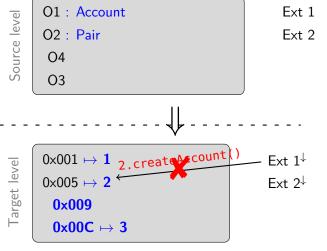


Ext 1[↓] Ext 2↓

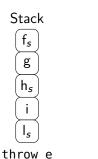


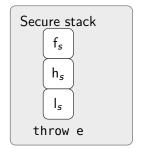
- Ext 2 Object id guessing
 - map Oid to natural
 - add Oid to map
 - lookup (**O**(1)) when number is received
 - dynamic typecheck for: current object

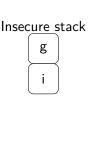
 $0x00C \mapsto 3$

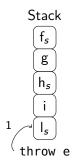


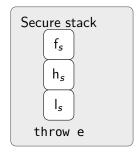
- Ext 2 Object id guessing
 - map Oid to natural numbers
 - add Oid to map
 - lookup (**O**(1)) when number is received
 - dynamic typecheck for: current object arguments
 - no need of extra information

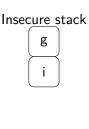


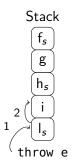


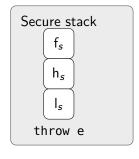


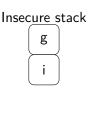


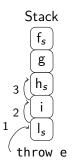


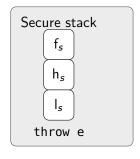


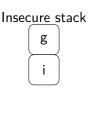


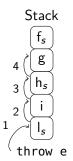


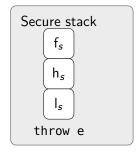




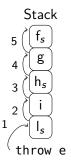


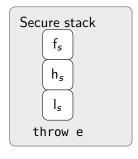


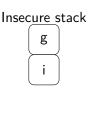


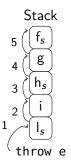


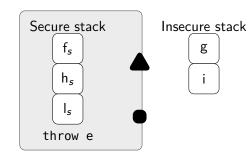


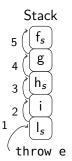


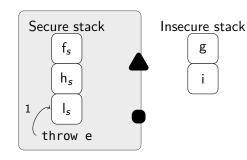


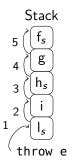


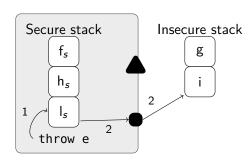






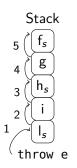


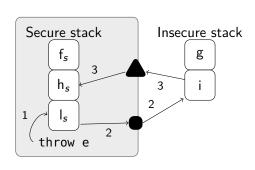




Record passed exceptions

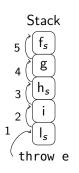
Exceptions

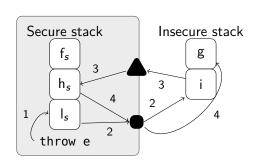




Record passed exceptions
Check that exception could be thrown

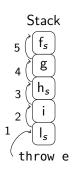
Exceptions

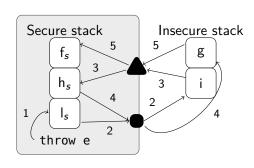




Record passed exceptions Check that exception could be thrown

Exceptions





Record passed exceptions Check that exception could be thrown

Source Language J+E
Secure Compilation, Informally
Proof Strategy

So now...

• We have a strategy to securely compile J+E code

- We have a strategy to securely compile J+E code
- We have the tools to implement it

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- We have an idea of the security properties of our secure compilation scheme

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Q: What is missing?

- We have a strategy to securely compile J+E code
- We have the tools to implement it
- We have an idea of the security properties of our secure compilation scheme



Secure Compilation, Formally

$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

Secure Compilation, Formally



Secure Compilation, Formally



$$C_1 \simeq^{\mathcal{S}} C_2 \triangleq \forall \mathbb{C}. \ \mathbb{C}[C_1] \Uparrow \iff \mathbb{C}[C_2] \Uparrow$$

$$C_1 \simeq^{\mathcal{S}} C_2 \triangleq \bigvee \mathbb{C} \mathbb{C}[C_1] \uparrow \longleftrightarrow \mathbb{C}[C_2] \uparrow \uparrow$$



$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$(\forall \mathbb{C}. \ \mathbb{C}[C_1] \Uparrow \iff \mathbb{C}[C_2] \Uparrow) \iff (\forall \mathbb{M}. \ \mathbb{M}[C_1^{\downarrow}] \Uparrow \iff \mathbb{M}[C_2^{\downarrow}] \Uparrow)$$

$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$(\forall \mathbb{C}. \ \mathbb{C}[C_1)) \overset{\longleftarrow}{\longleftarrow} (\forall \mathbb{M}. \ \mathbb{M}[C_1^{\downarrow}] \uparrow) \iff \mathbb{M}[C_2^{\downarrow}] \uparrow))$$

$$C_1 \simeq^{\mathsf{J+E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$C_1 \simeq^{\mathsf{J} + \mathsf{E}} C_2 \iff C_1^{\downarrow} \simeq^{\mathsf{A} + \mathsf{I}} C_2^{\downarrow}$$

$$C_1 \simeq^{\mathsf{J+E}} C_2 \quad \leftarrow \quad C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow}$$

$$C_1 \simeq^{\mathsf{J} + \mathsf{E}} C_2 \ \Rightarrow \ C_1^{\downarrow} \simeq^{\mathsf{A} + \mathsf{I}} C_2^{\downarrow}$$

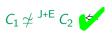
$$C_1 \simeq^{\mathsf{J+E}} C_2 \Rightarrow \begin{bmatrix} C_1^{\downarrow} \simeq^{\mathsf{A+I}} C_2^{\downarrow} \\ \updownarrow \\ \mathsf{Traces}(C_1^{\downarrow}) = \mathsf{Traces}(C_2^{\downarrow}) \end{bmatrix}$$

Fully Abstract Trace Semantics

$$C_1 \not\simeq {}^{\mathsf{J+E}} C_2 \Leftarrow \mathsf{Traces}(C_1^{\downarrow}) \not= \mathsf{Traces}(C_2^{\downarrow})$$

$$C_1 \not\simeq {}^{\mathsf{J+E}} C_2$$

$$C_1 \not\simeq {}^{\mathsf{J+E}} C_2 \qquad \mathsf{Traces}(C_1^\downarrow) \not= \mathsf{Traces}(C_2^\downarrow)$$



$$C_{1}^{\downarrow} \simeq^{\mathsf{A}+\mathsf{I}} C_{2}^{\downarrow}$$

$$\updownarrow$$

$$\mathsf{Traces}(C_{1}^{\downarrow}) = \mathsf{Traces}(C_{2}^{\downarrow})$$

Fully Abstract Trace Semantics

Outline

- Background (What are Secure Compilation and PMA?)
 - Secure Compilation
 - PMA and Isolation
 - Fully Abstract Trace Semantics for PMA
- Secure Compilation of J+E
 - Source Language J+E
 - Secure Compilation, Informally
 - Proof Strategy
- Recent Work

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- can we link securely-compiled modules securely?

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- assembly-level linking of securely-compiled modules is not investigated
- can we link securely-compiled modules securely?
- what attacks arise in the presence of linking?

Logical-relations Based Proof Technique

Devirese et al.@ POPL'16

Logical-relations Based Proof Technique

- Devirese et al.@ POPL'16
- proof technique for fully-abstract compilation based on logical relations

Logical-relations Based Proof Technique

- Devirese et al.@ POPL'16
- proof technique for fully-abstract compilation based on logical relations
- check out the video on the popl website!

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- can it express all security properties?
- is there a better/more precise notion of secure compilation?
- can we relate it to hyperproperties (i.e., properties over sets of traces)?

Questions



Qs?