SRAVANIKELLA

✉ sravanikella24062001@gmail.com | 📱 6305165226 | **in**
linkedin.com/in/sravani-kella-a6565b284

EDUCATION

Lendi Institute of Engineering and Technology | *B.Tech*   **2020-2024**
Electrical and Electronics | GPA: 8.38   Vizianagaram, AP

Narayana Junior College   **2018-2020**
Intermediate | GPA: 9.3   Visakhapatnam, AP

Narayana High School   **2017-2018**
SSC | GPA: 9.7   Visakhapatnam, AP

CERTIFICATION & INTERNSHIP

Tech Mahindra Company   **Jun-Sep (2024)**
UI/UX Developer   Visakhapatnam, AP
- Successfully completed an intensive UI/UX Developer training program at Tech Mahindra, earning a certification that highlights my expertise in user interface and user experience design.

Cybersecurity Virtual Internship Certified by AICTE Eduskills   **2022**
- I learned how to implement strong security measures to protect against potential risks.

Salesforce Administrator Internship Certified by SmartInternz   **Jun 2023**
- Salesforce is a popular cloud-based customer relationship management (CRM) platform that helps businesses manage their sales, marketing, and customer service activities.

AI-ML Virtual Internship Certified by AWS Academy   **2023**
- I participated in hands-on projects and competitions that provided valuable practical experience.

SKILLS

Technical : Python, C, SQL | UI Development - HTML, CSS, JavaScript, React.js, Figma

Languages : English | Proficient, Telugu | Native, Hindi | Beginner

Soft Skills : Problem Solving | Time Management

Interests : Painting | Drawing

PROJECTS

tourist website | *https://sravanikella24062001.github.io/tours/*   **AUG 2024**
- Technologies Used, HTML,CSS,Boostrap,javascript,React Js
- The tourist website project showcases popular travel destinations, tour packages, and contact information, allowing users to explore and book their trips.

Designing a space vector Pulse Width Modulation for 9 and 11 Cascaded H-Bridge Multilevel Inverters
- This involves creating a control strategy to efficiently manage the switching of the cascaded H-bridge inverters to generate the desired output voltage levels.