MPU Resources

▶️ The ARM University Program, ARM Architecture Fundamentals

Book 1 : [The Definitive Guide to ARM Cortex-M3 and Cortex-M4 Processors](#)
Book 2 : [Sarah Harris, David Harris - Digital Design and Computer Architecture. ARM Edition-Morgan Kaufmann (2015)](#)

Per slide:
1. Exclusively slide topics
2. 1.5, 3.1, 4.1, 4.2.1, 4.2.2, 4.4 from book 1, with extra info in slides
3. 4.1 repeated, 4.2.3, 3.2.8 from book 1,
4. 3.1.4, 13.2.1, 3.2.4, 3.2.6, Table 3.2 (3.1.4), 3.2.10 along with
   a. [Cortex-M4 Technical Reference Manual r0p0 (arm.com)](#)
   b. [Cortex-M4 Technical Reference Manual r0p0 (arm.com)](#)
   c. [Cortex-M4 Technical Reference Manual r0p0 (arm.com)](#)
5. 5.1, 5.3, 5.4, 5.5, 5.6 from book 1
   a. [Addressing Modes (sonoma.edu)](#)
   b. [ARM Compiler armasm User Guide Version 5.06](#)
   c. [ARM Compiler toolchain Using the Assembler Version 4.1](#)
   d. [Cortex-M1 Technical Reference Manual r0p1 (arm.com)](#)
6. Two parts
   a. Addressing : [Addressing Modes](#)
   b. Binary encoding : 6.4 from book 2
   c. [32-Bit Instruction - an overview | ScienceDirect Topics](#)
7. 6.4 from book 2, 5.6.8, 5.6.9, 5.6.7 from book 1
8. 4.5, 7.1, 7.2, 7.3, 7.4, 7.5, 7.6, 8.3.3 from book 1
9. 7.6, 8.2, 8.1, 7.7, 7.8, 7.9, 7.10

Chapter 7 and 8 probably full as well