

# SALVADOR RAZO

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## EDUCATION

Dec. 2014 **University of Florida, Gainesville, FL,** GPA: 3.39/4.00  
**Bachelors of Science in Electrical Engineering and Bachelors of Science in Mathematics**

## SUMMARY OF COURSEWORK

**Digital:** Digital Logic, Elements of Machine Intelligence, Machine Intelligence and Synthesis, Digital Design(VHDL), Reconfigurable Computing, Microprocessor Applications 1.

**Analog:** Circuits 1&2, Electronics 1&2, Signals and Systems, Fourier Analysis.

## SKILLS

**Languages:** Spanish and English.

**Computer Languages:** Perl, Lisp, Prolog, VHDL, Assembly, C/C++, Labview (CLAD).

**Computer Software:** Altera Quartus, Modelsim, Xilinx ISE, TI Code Composer Studio, LTSpice, Matlab, Altium Designer, Solidworks (CSWA Certification).

## EXPERIENCE

May-Aug 2014 Texas Instruments, Tucson, Arizona – **OP AMP Test Engineer**

- Create diagnostic and calibration tests for an Eagle ETS364 handler interface board that was created as a comprehensive solution for a wide range of op amps.
- Wrote a debug manual for the ETS364 board that would allow a technician to quickly fix any issues once the test was deployed without relying on the engineer that created the solution.
- Responsible for porting an ETS364 solution of a voltage reference to a new reference.
- Designed a BeagleBone Black Based quadcopter for an Intern Design Challenge.

May-Aug 2013 Intel Corporation, Austin, Texas – **Debug Tool Development**

- Developed a tool to interface with an in-house Spice simulator to collect data and extract transistor parameters from a family of debug circuits used for measuring wafer variance.
- Developed a tool to download and summarize help request tickets to track team progress.

June 2010 Diagnostics/Retrieval Systems (DRS Technologies), Melbourne, Florida – **Test Technician**

- Calibrated test benches for uncooled optical rifle scopes. Tested rifle scopes for quality control.
- Shadowed head electrical engineer and gained basic trouble shooting skills.

## INVOLVEMENT

2014-Current NSF Center for High-Performance Reconfigurable Computing, Gainesville, Florida (CHREC)  
**Benchmarking**

- Responsible for writing serial and parallel algorithms using assembly and various C libraries including ATLAS for benchmarking of a space-based processor.

2012-2014 UF Branch of The Institute of Electrical and Electronics Engineers, Gainesville, Florida (IEEE)

**Advertising Chair and E-Week coordinator**

- Responsible for designing and printing posters and coordinating with other chairs.
- Designed a 2.5 kg quad copter for E-week 2013.

2012-2013 UF Small Satellite Design Club (SSDC), Gainesville, Florida – **Outreach Director**

- Responsible for organizing an E-week presentation for high school students.
- Responsible for managing a team of volunteers to build a homecoming float.

2007-2013 For Inspiration and Recognition of Science and Technology (FIRST Robotics), Gainesville, Florida  
**FIRST FRC Mentor, FLL Mentor, FRC Participant, FTC Participant**

- Led students by coordinating brainstorming sessions and organizing builds.
- Learned real world problem solving skills working with industry standard equipment.

Nov. 2012 Florida University Satellite Competition (FunSAT), Gainesville, Florida – **Student Participant**

- Responsible for the electrical subsystem and chassis design of a proposal for a small satellite that utilized modulating retro-reflectors to achieve laser communications with a ground station.

Oct. 2012 ACM Symposium on User Interface Software and Technology, Cambridge, Massachusetts

**Student Competition Participant**

- Submitted a touchpad that would be placed behind a steering wheel in a vehicle.
- Allowed gesture and force based user control of vehicle functions without releasing the wheel.