DRACONIS: NETWORK-ACCELERATED SCHEDULING FOR MICROSECOND-SCALE WORKLOADS

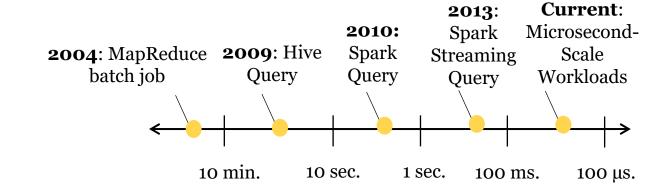
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Microsecond-Scale Workloads

- Recent datacenter advances enable microsecond-scale workloads
 - o Storage class memory, accelerators, and RDMA

- o Applications are getting shorter despite accessing large datasets [1]
 - o Financial analytics and algorithmic smart trading [2]
 - o Realtime IoT analytics [3]
 - o Rapid object detection [4]
 - o Low latency web services [5]



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[2] Xinhui Tian, Rui Han, Lei Wang, et al. Latency critical big data computing in finance. The Journal of Finance and Data Science. 2015

[3] S. Verma, Y. Kawamoto, Z. M. Fadlullah, et al. A Survey on Network Methodologies for Real-Time Analytics of Massive IoT Data and Open Research Issues. IEEE Communications Surveys & Tutorials. 2017

[4] Tan Zhang, Aakanksha Chowdhery, Paramvir Bahl, et al. The design and implementation of a wireless video surveillance system. MobiCom. 2015

[5] Jeffrey Dean and Luiz André Barroso. The Tail at Scale. Commun. ACM .2013



Scheduling Microsecond-scale Workloads

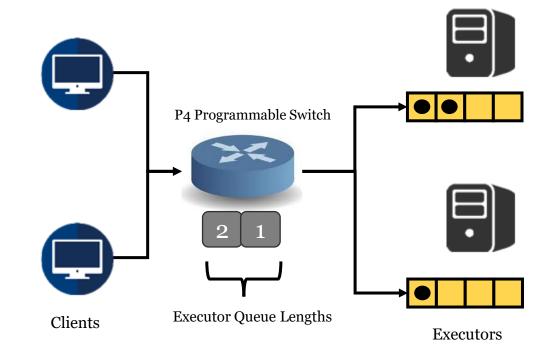
- Low Scheduling Tail Latency
 - Accurate scheduling decisions

- High Scheduling Throughput
 - o Millions of scheduling decisions per second [6]



Modern Network-Accelerated Scheduling

- Distributed queue design
- Disadvantages:
 - Suboptimal Node-level blocking
 - Inefficient implementations
 - o R2P2 [7] Recirculation
 - o RackSched [8]— Sampling





Programmable Switches

- Challenges
 - No loops / recursion
 - Limited pipeline stages and memory
 - Access a memory register only once per packet!
- "Switches cannot house dynamic data structures such as queues"









Draconis Overview

- A novel in-network scheduling paradigm
 - Centralized in-switch queue -> Eliminates node-level blocking
 - Supports complex scheduling policies

Evaluation Highlights:

- o **61%** lower tail latency over network-accelerated designs
- o **52**x throughput over server-based designs

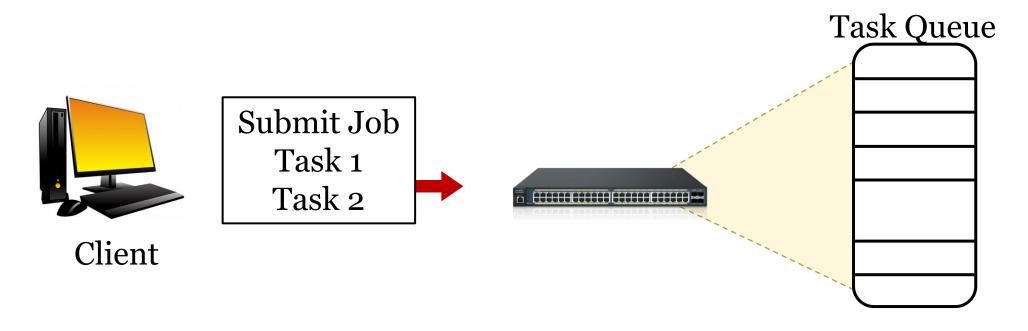


Outline

- Introduction
- Background
- FIFO Scheduling
- Complex Policies
- Evaluation
- Conclusion



Draconis - FIFO Scheduling







Executor 2



Executor 3



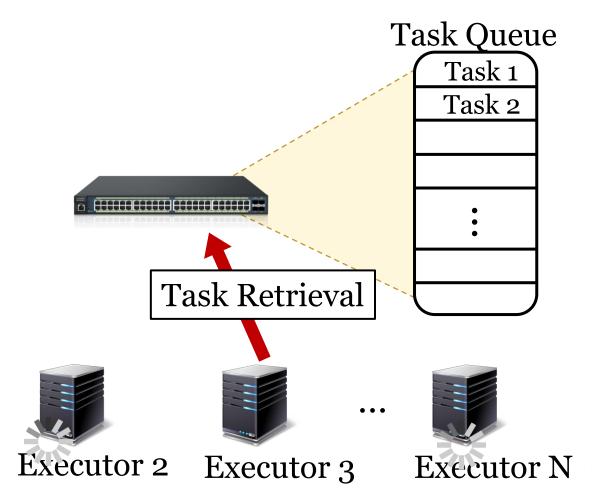
Executor N



Draconis - FIFO Scheduling

Executor 1

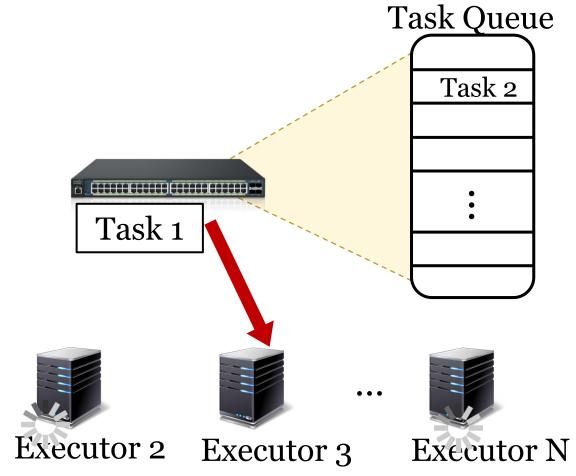




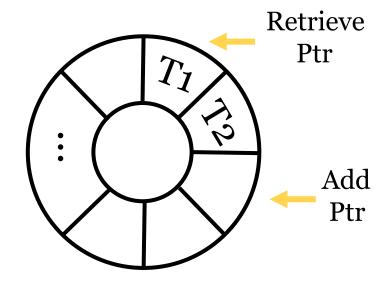
Draconis - FIFO Scheduling

Executor 1





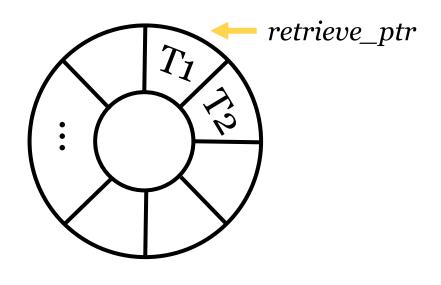
- Tasks are stored in a circular queue
- Simple yet tricky to implement on modern programmable switches



Draconis's Task Queue Design



```
on_retrieve {
  rtrv_ptr = read(rtrv_ptr)
  if( queue contains a task )
  {
    // Schedule the task
    rtrv_ptr ++
  } else {
    // Queue is empty
  }
}
```



Challenge: This accesses the pointer twice!

```
on_retrieve {
    rtrv_ptr = read(rtrv_ptr)
    if( queue contains a task )
    {
        // Schedule the task
        rtrv_ptr ++
    } else {
        // Queue is empty
    }
}
```

```
on_add {
    add_ptr = read(add_ptr)
    if( queue has space )
    {
        // Enqueue the task
        add_ptr++
    } else {
        // Queue is full
    }
}
```

Challenge: This accesses the pointer twice!

```
on_retrieve{
    rtrv_ptr = read_and_increment(rtrv_ptr)
    if(queue contains a task){
        // Schedule the task
    }
    else
        // rtrv_ptr needs fixing
}
```

```
on_add(task) {
    add_ptr = read_and_increment(add_ptr)
    if (queue is full){
        //add_ptr needs fixing
        fix(add_ptr)
    }
    if (rtrv_ptr needs fixing)
        fix(rtrv_ptr)

    // Enqueue the task
}
```

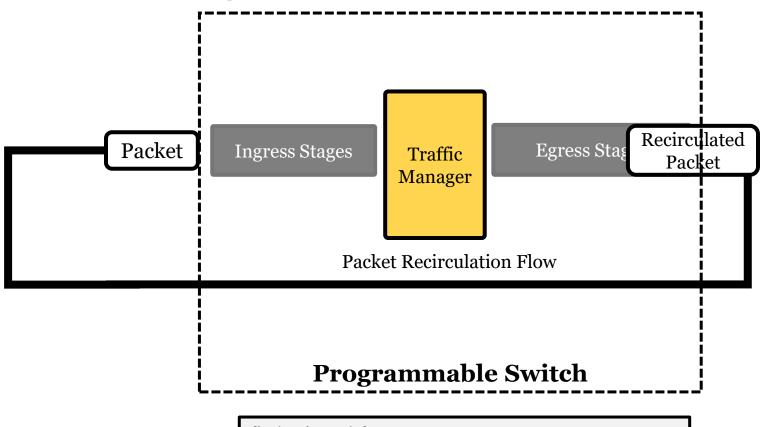
read_and_increment() - Optimistic atomic read and increment of pointers

fix() – Fixing pointer values as required



Design Components - Pointer Fixing

- Use packet recirculation
- Adjust pointer values as needed
 - Use Boolean flags to prevent race conditions



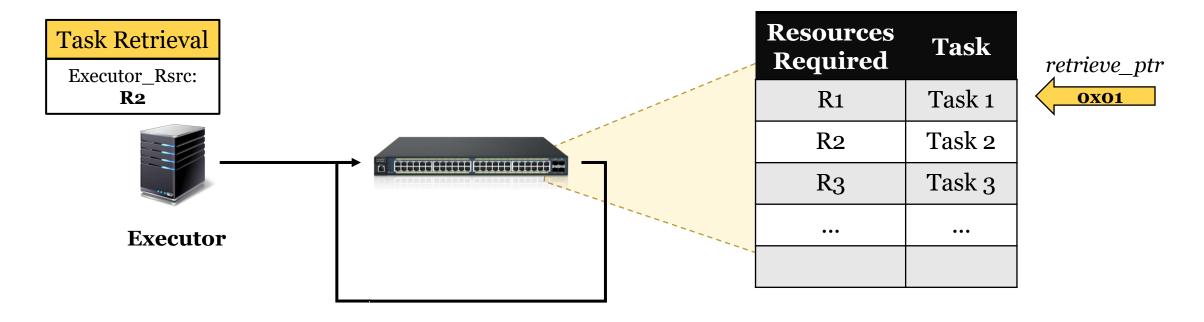
```
fix(pointer){
    send_fix_pkt ( pointer, new_val )
}
```



Complex Scheduling Policies

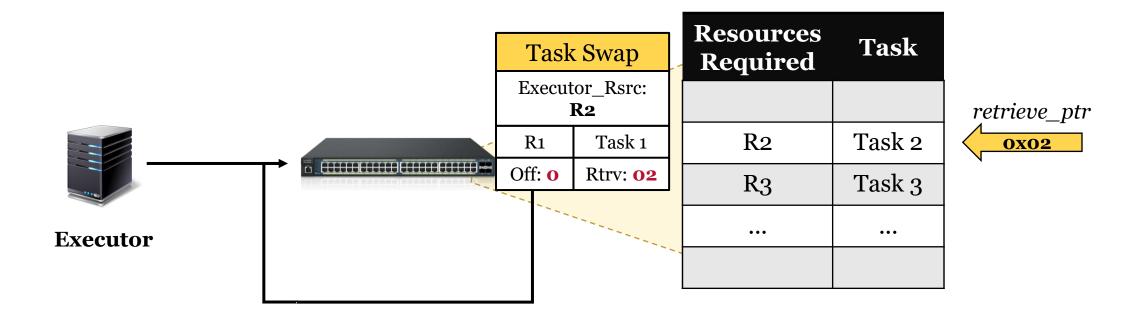
- Classes of service
 - Priority Scheduling

- Constraint based
 - Resource-Aware Scheduling
 - Locality Scheduling

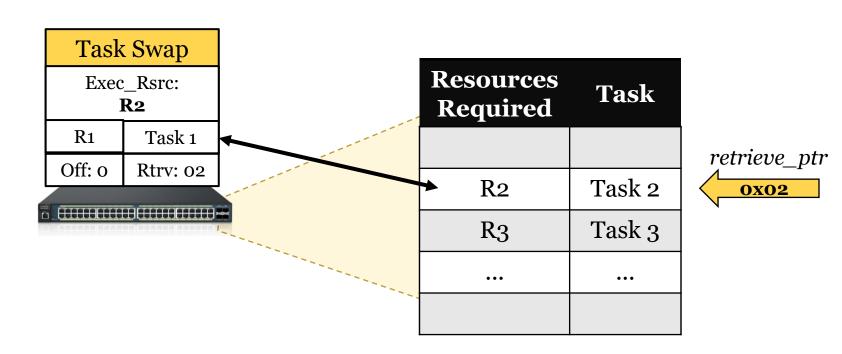




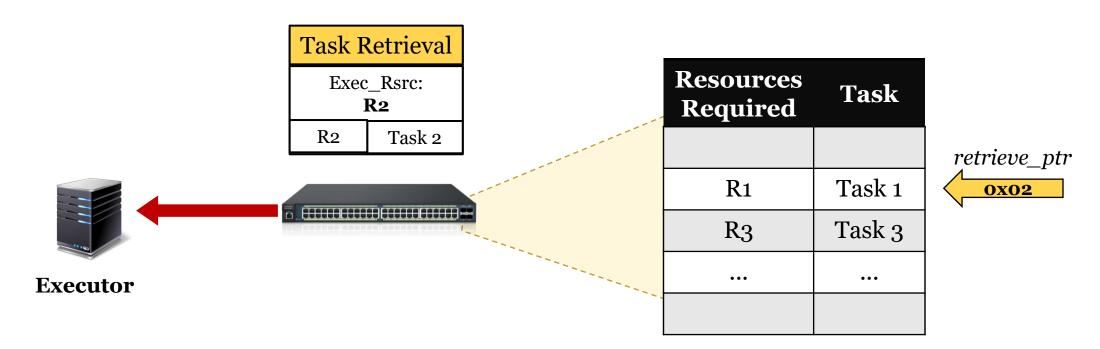












Evaluation Overview

Testbed

- o 13 node cluster
 - Intel Xeon 10 core CPU with HT
 - o 48GB RAM
 - o 100GBps Mellanox NIC
- EdgeCore Wedge 100BF-32x
 - o Intel Tofino ASIC

Workloads

- Synthetic Suite
 - Uniform Service Times (100, 250 & 500 μs)
 - o Bimodal and Trimodal
 - Exponential Mean 250 μs
- o Google Cluster Traces



Evaluation Overview

Alternatives

- o R2P2 [7]
- o RackSched [8]
- \circ Sparrow [9]
- Draconis-Socket-Server
- o Draconis-DPDK-Server

Metrics

- 99th Percentile Scheduling
 Latency
- Scheduling Throughput
- Effectiveness of complex scheduling policies

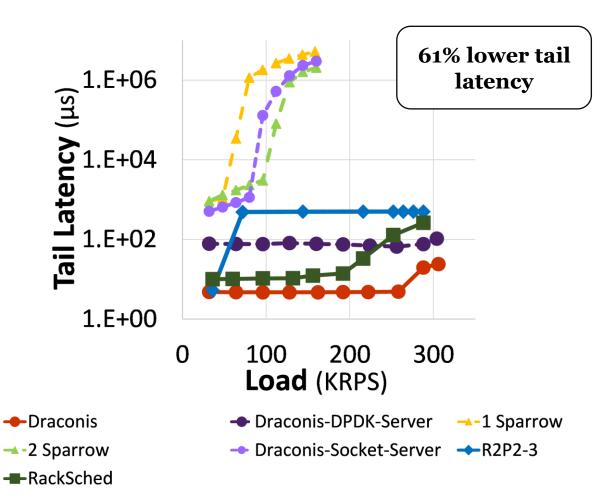


Scheduling Latency

Experimental Setup:

- Synthetic workload consisting of 500 μs tasks
- Tail Latency 99th %ile

Draconis outperforms all other alternatives by at least *61*%





Concluding Remarks

- Draconis overcomes the shortcomings of modern scheduling paradigms
 - o Novel in-network centralized scheduling approach
- Supports complex policies with generic design principles
 - Task swapping and Queue replication

Evaluation Highlights:

- o 61% lower latencies over network-accelerated scheduling
- o **52**x higher scheduling throughput over server-based scheduling

o Code: https://github.com/UWASL/Draconis

