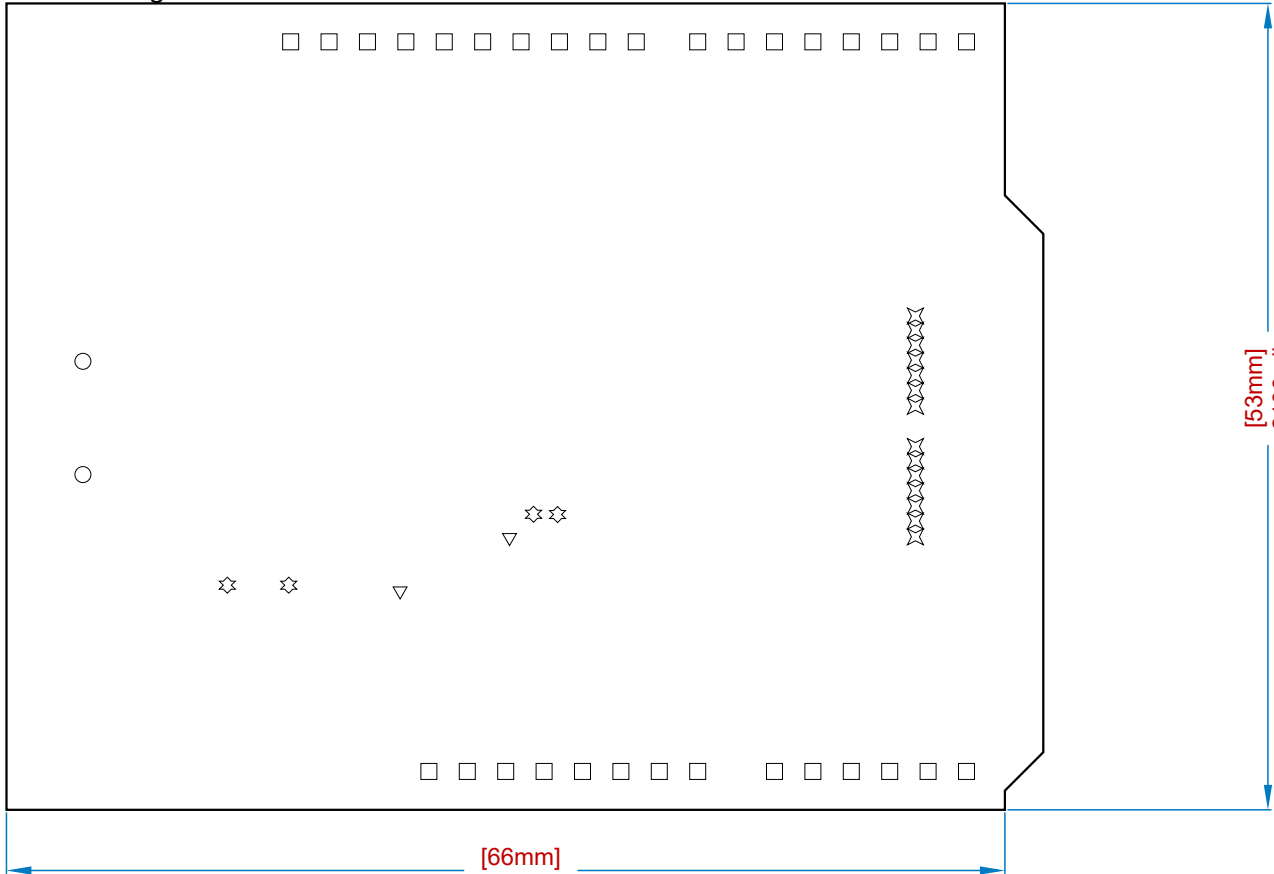


Drill Drawing View



NOTES: UNLESS OTHERWISE SPECIFIED.

1. FABRICATE PER IPC-6012A CLASS 2.
2. FOR BOARD THICKNESS AND IMPEDANCE DETAILS REFER STACKUP DOCUMENT.
3. PRINTED WIRING BOARD SHALL COMPLY WITH REQUIREMENTS OF ANSI/J-STD-003.
4. SURFACE FINISH: IMMERSION SILVER
5. SOLDERMASK ON BOTH SIDES OF THE BOARD SHALL BE LPI, COLOR XXXXXX.
6. SILK SCREEN LEGEND TO BE APPLIED PER LAYER STACKUP USING WHITE NON-CONDUCTIVE EPOXY INK.
7. THIS PRINTED WIRING BOARD IS DESIGNED WITH A MINIMUM CONDUCTOR WIDTH AND SPACING OF 4 MIL & 4 MILS.
8. ALL VIAS ARE TENTED ON BOTH SIDES UNLESS SOLDERMASK OPENED IN GERBER.
9. ALL VIAS ON PAD SHOULD BE FILLED WITH NON CONDUCTIVE EPOXY AND SURFACE SHOULD BE FLAT. FLATNESS TOLERANCE FOR VIA ON PADS: +0.000 /- 0.001 INCHES ON BOTH SIDES. THE MANUFACTURER IS REQUESTED TO SIZE PER THEIR SOLDERMASK TOLERANCE.
10. SOLDER MASK OPENING IS KEPT SAME SIZE AS PAD (1:1) FOR ALL COMPONENTS
11. VENDOR SHOULD FOLLOW ROHS COMPLIANT PROCESS AND Pb FREE FOR MANUFACTURING
12. MANUFACTURER'S IDENTIFICATION, DATECODE LETTER SHALL BE SILKSCREENED ON SOLDER SIDE OF THE BOARD.
13. TRACE WIDTH SHOULD BE ACCURATELY ETCHED. MAX TOLERANCE +/- 1 MIL
14. ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED.
15. FLATNESS REQUIREMENTS:
A. BOW AND TWIST OF ASSEMBLY SUB-PANEL OR SINGULATED PWB SHALL NOT EXCEED 0.7% OF LONGEST SIDE

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
	Surface Material	0.01mm	Solder Resist	Solder Mask	GTS
Copper	Top Layer	0.04mm		Signal	GTL
		1.52mm	FR-4	Dielectric	
Copper	Bottom Layer	0.04mm		Signal	GBL
Surface Material	Bottom Solder	0.01mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
Total thickness: 1.62mm					

Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
□	32	43.31	Plated	
✕	14	10.00	Non-Plated	+/-3.00
☆	4	20.00	Plated	+/-3.00
▽	2	10.00	Plated	+/-3.00
○	2	60.00	Plated	+/-3.00

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		UNLESS OTHERWISE SPECIFIED:		NAME	DATE			
		DIMENSIONS ARE IN INCHES	DRAWN		05-04-2025			
		TOLERANCES:	CHECKED			Sheild Board for Arduino UNO		
		FRACTIONAL±	ENG APPR.					
		ANGULAR: MACH± BEND ±	MFG APPR.					
		TWO PLACE DECIMAL ±	Q.A.					
		THREE PLACE DECIMAL ±	COMMENTS:			SIZE	DWG. NO.	
		INTERPRET GEOMETRIC TOLERANCING PER:						
		MATERIAL						
NEXT ASSY	USED ON	FINISH						
APPLICATION		DO NOT SCALE DRAWING				SCALE: 1:1	WEIGHT:	SHEET 1 OF 3

A

B

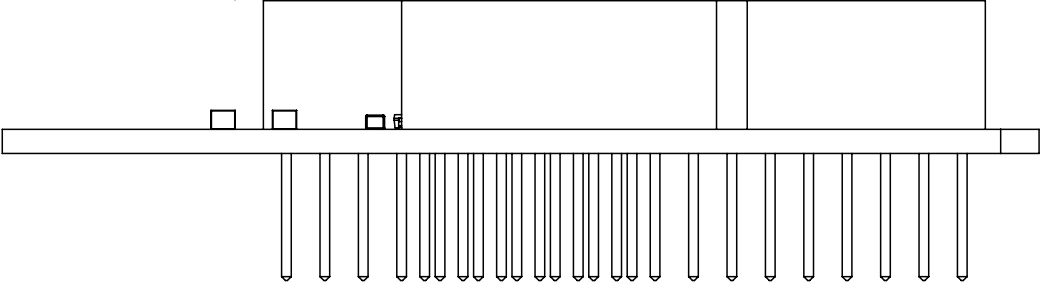
C

D

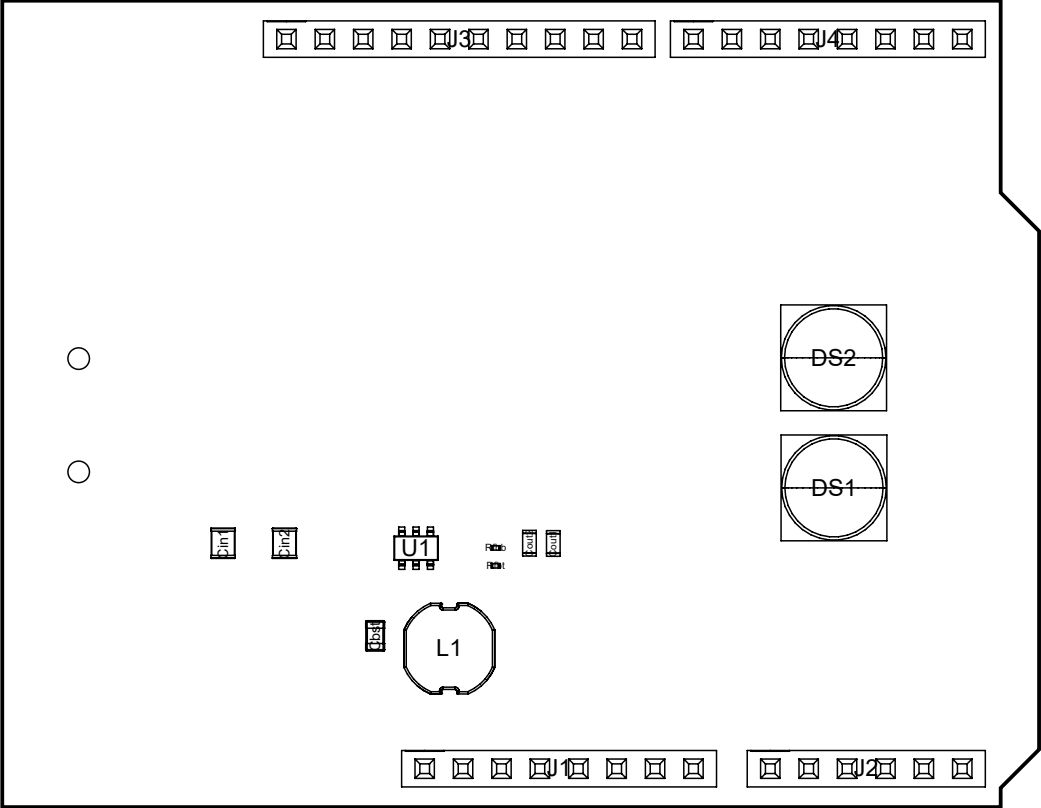
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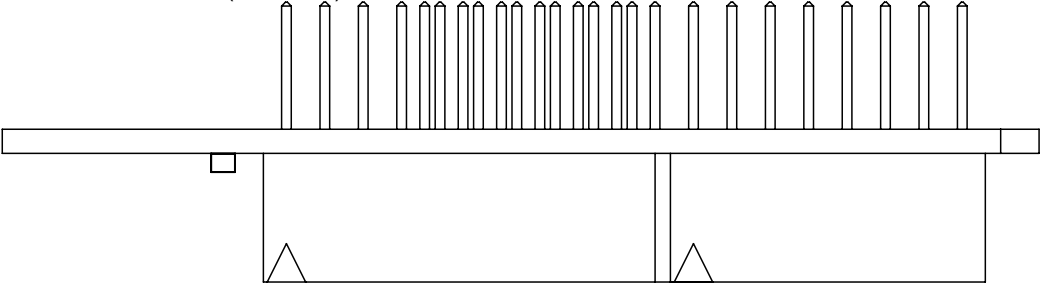
View from Front side (Scale 2:1)



View from Top side (Scale 2:1)



View from Back side (Scale 2:1)



2

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		UNLESS OTHERWISE SPECIFIED:		NAME	DATE	TITLE Sheild Board for Arduino UNO			
		DIMENSIONS ARE IN INCHES	DRAWN		05-04-2025				
		TOLERANCES:	CHECKED						
		FRACTIONAL±	ENG APPR.						
		ANGULAR: MACH± BEND ±	MFG APPR.						
		TWO PLACE DECIMAL ±	Q.A.			COMMENTS:			
		THREE PLACE DECIMAL ±							
		INTERPRET GEOMETRIC TOLERANCING PER:							
		MATERIAL				SIZE	DWG. NO.		
		FINISH							
NEXT ASSY	USED ON	DO NOT SCALE DRAWING				SCALE:	1:1	WEIGHT:	SHEET 2 OF 3
APPLICATION									

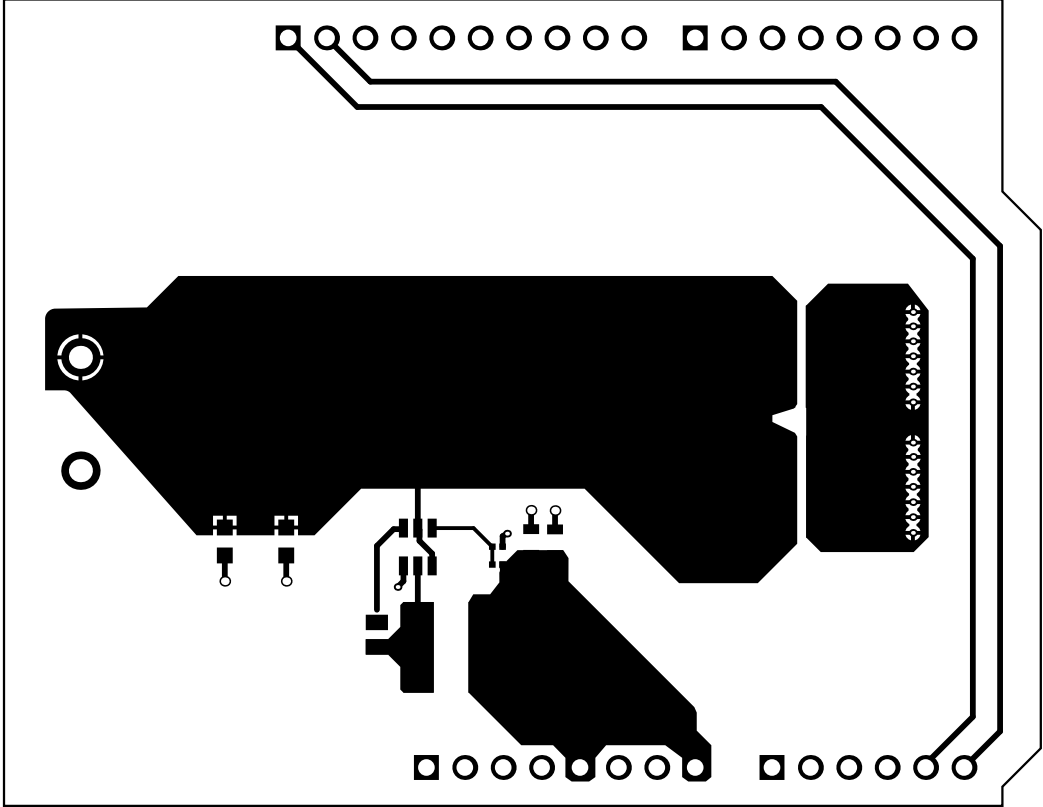
A

B

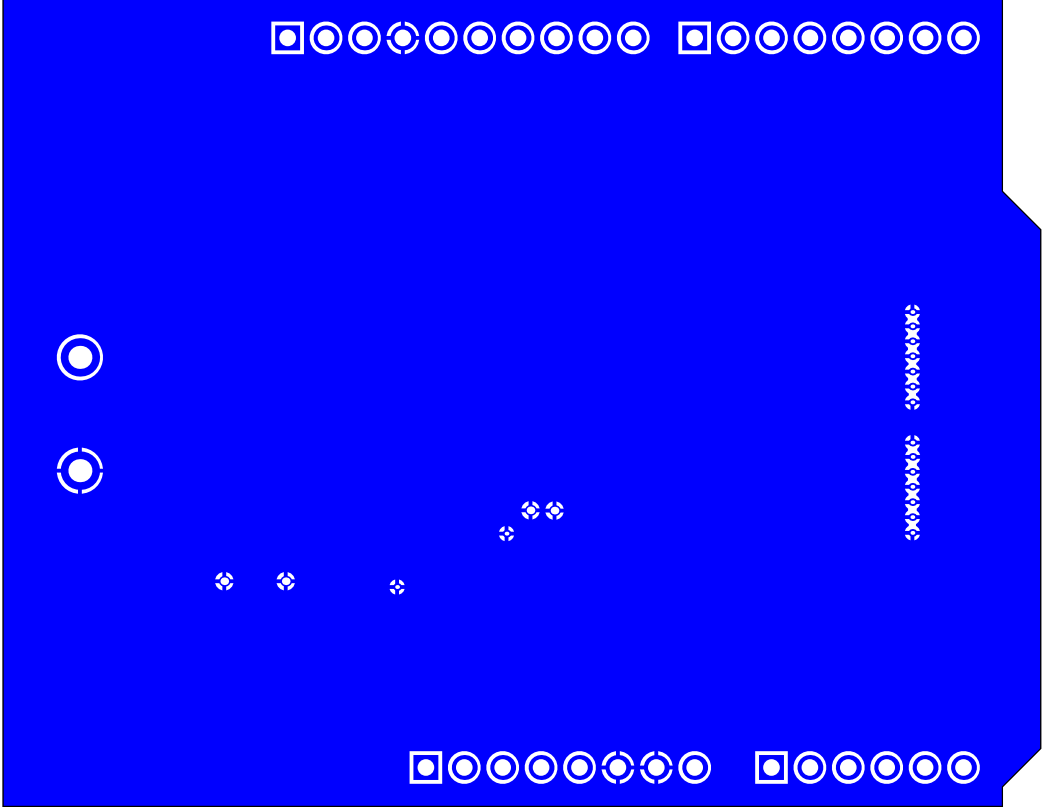
C

D

Top Layer (Scale: 2:1)



Bottom Layer (Scale: 2:1)



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		TOLERANCES:	CHECKED			TITLE Sheild Board for Arduino UNO			
		FRACTIONAL±	ENG APPR.						
		ANGULAR: MACH± BEND ±	MFG APPR.						
		TWO PLACE DECIMAL ±	Q.A.						
		THREE PLACE DECIMAL ±	COMMENTS:						
		INTERPRET GEOMETRIC							
		TOLERANCING PER:							
		MATERIAL							
NEXT ASSY	USED ON	FINISH				SIZE	DWG. NO.		
APPLICATION		DO NOT SCALE DRAWING				SCALE: 1:1	WEIGHT:	SHEET 3 OF 3	