

# Generalised Rijndael

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**Abstract.** <sup>3</sup> This is the abstract

**Keywords:** Cryptography, Symmetrics, Rijndael

## 1 Introduction

[1] [2] [3] [4]

## 2 Approach to the Rijndael Schema

### 2.1 Design

## 3 Generalising the schema

### 3.1 key expansion

### 3.2 Rounds

### 3.3 subBytes

How to build different SBoxes

### 3.4 shiftColumns

### 3.5 mixColumns

### 3.6 Operate in a polinomial ring, with coeficients in a polinomial field

$$\frac{\mathbb{F}_{2^n}[y]}{m(y)}$$

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<sup>3</sup> Partially founded by the Spanish project MTM20\_\_-\_\_\_\_-\_\_\_\_

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**Algorithm 1** KeyExpansion

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**INPUT:** byte  $k[nRows*nColumns]$ ,  $nRounds$ ,  $nRows$ ,  $nColumns$ ,  $wSize$ **OUTPUT:** word  $w[nRounds*(nRows+1)]$ 

```

1:  $i := 0$ 
2: while  $i < nColumns$  do
3:    $w[i] := \text{word}(k[nRows*(i+c) \text{ for } c \text{ in } \text{range}(nColumns)])$ 
4: end while
5:  $i := nColumns$ 
6: while  $i < nRounds*(nRows+1)$  do
7:    $\text{temp} := w[i-1]$ 
8:   if  $i \bmod nColumns == 0$  then
9:      $\text{temp} := \text{SubWord}(\text{RotWord}(\text{temp})) \oplus \text{Rcon}[i/nColumns]$ 
10:  else
11:     $\text{temp} := \text{SubWord}(\text{temp})$ 
12:  end if
13:   $w[i] := w[i-nColumns] \oplus \text{temp}$ 
14:   $i++$ 
15: end while

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where  $m(y)$  is a composed polynomial of degree  $r$  columns. This gives a polynomial ring. The coefficients of this polynomial ring are elements of a polynomial field

$$\mathbb{F}_{2^n} = \frac{\mathbb{F}_{2^2}[x]}{m(x)}$$

where  $m(x)$  is irreducible and gives a polynomial field. Standard rijndael (AES) uses a circular invertible matrix for this to simplify and speed up the operations in the ring.

**3.7 addRoundKey****4 Parameter combinations****5 New useful sizes for Rijndael**

[5]

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## Rijdael Schematic

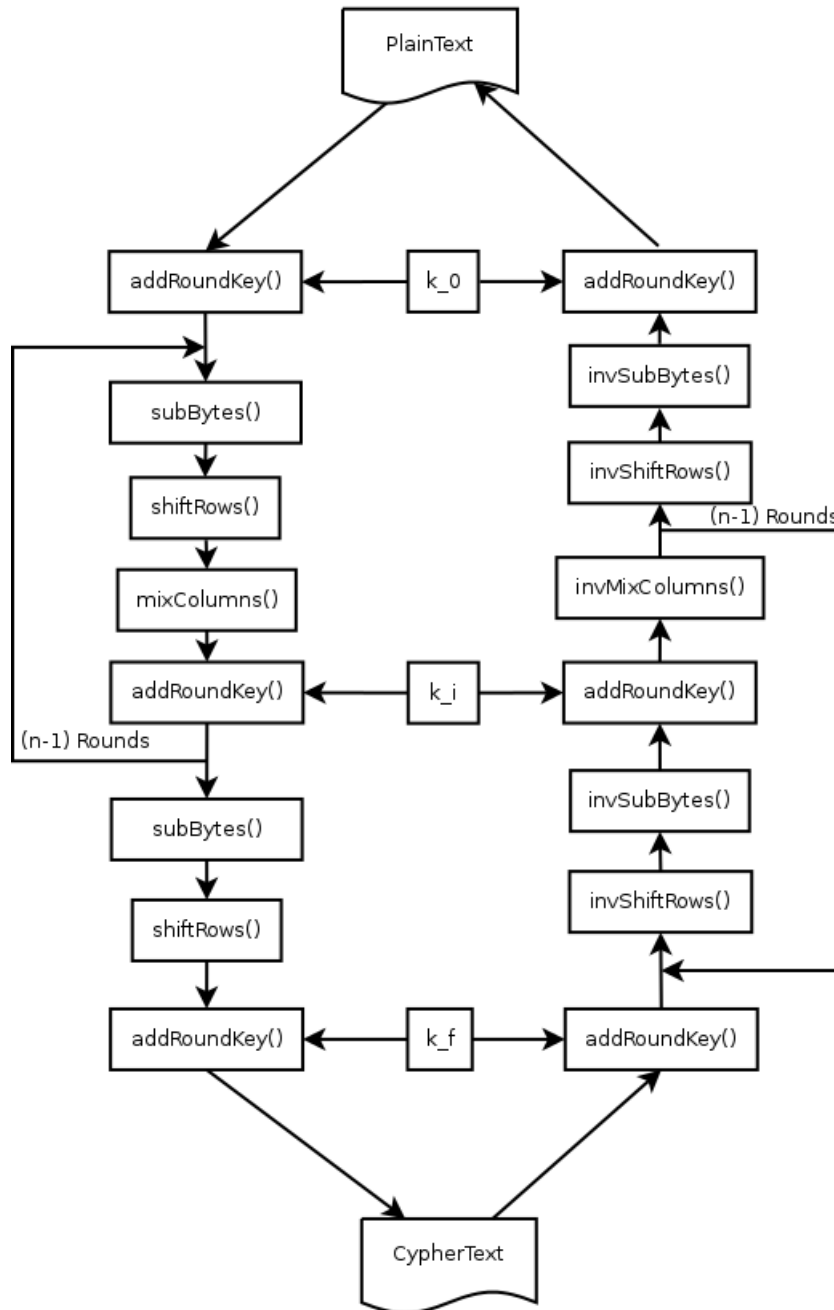
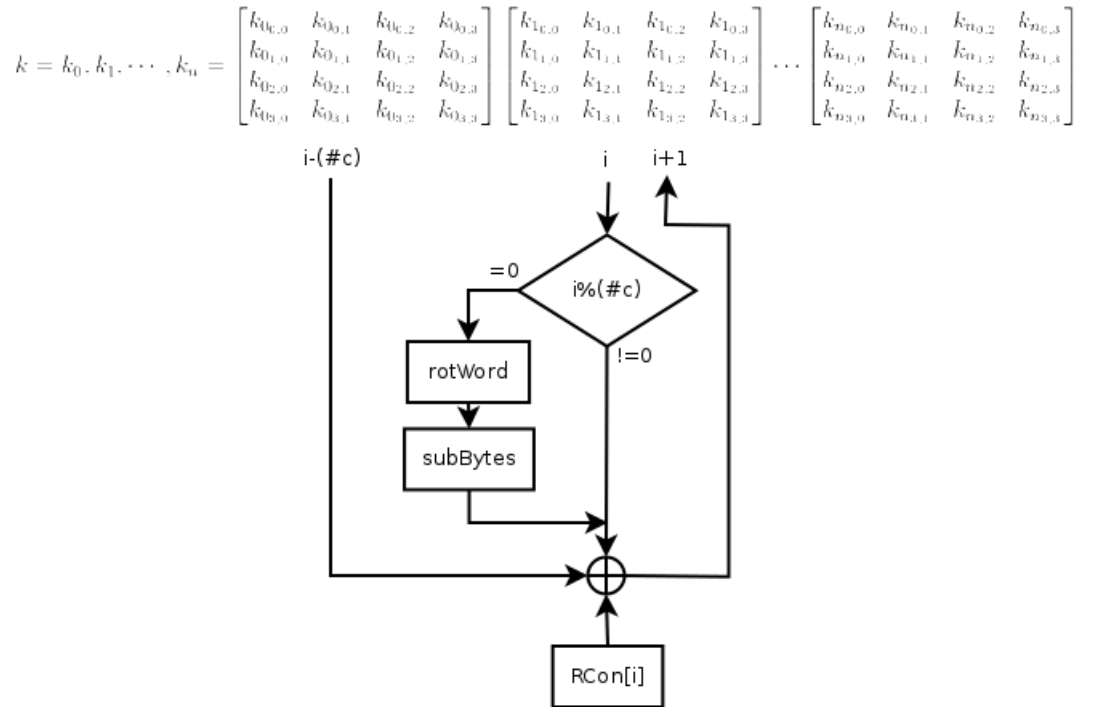


Fig. 1. rijndael diagram



**Fig. 2.** Block diagram of the construction of the rijndael key expansion