J- IT Uni

FET CIRCUITS

INTRODUCTION:

At low frequencies we analyse transistor using h-parameters. But at high frequencies to analyse the transistor the h-parameter model is not suitable for following two reasons.

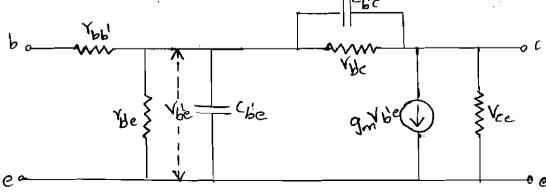
1. The values of h-parameters are not constant at high frequencies. Therefore, it is necessary to analyse transistor at each and every frequency, which is not possible practically.

2. At high frequencies the h-parameters become complex in nature

So, in order to analyse the high frequency circuits we will use hybrid-TT model.

HYBRID-TT COMMON EMITTER TRANSCONDUCTANCE MODEL:-

Common emitter circuit is most important practical configuration. So, we choose this circuit for the analysis of the transistor using hybrid-TI model. The following el circuit shows the hybrid-TI model configuration of a CE.



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All parameters in this model are assumed to be independent of frequency.

PARAMETERS IN THE HYBRID-IT MODEL:

Che and Chc:

Che: The forward biased PN junction exhibits a capacitive effect called as diffusion capacitance.

Ce is diffusion capacitance. This produces when the diode is in forward biased in the input.

Sic!— The reverse biased pN junction exhibits a capacitive effect called as transition capacitance.

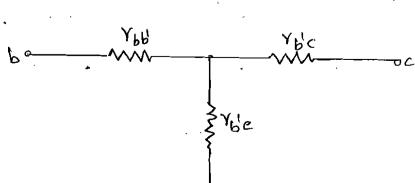
Ce is transistor capacitance exists due to reverse bias of the diode in the output.

(1 1 1 -

The base b' which is existing internally in the circuit is not practically possible to the use.

So, b' is spreaded to be with the help of existor do, $r_{bb'}$ is known as base spreading resistors

The: At is forward dynamic resistance existing in emitter diade.



rbc:-

Due to early effect, the varying voltages across the collector to emitter junction nexults in base width modulation. As V_{CE} is changing, the input current is also changing with the help of a feedback nesistance V_{DC} .

Trans conductance (GM):-

gt is the ratio of change in the imput collector current to that of change in input voltage Vbe when Vce is kept constant

Yce:-

It is the output resistance It is also the result of the early effect.

HYBRID - TT PARAMETER VALUES !-

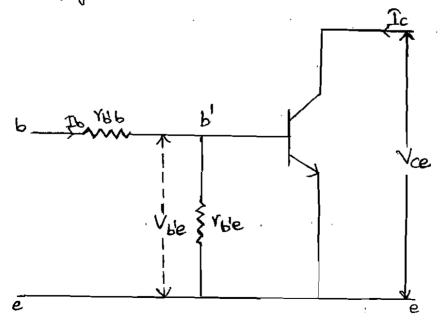
Parameter	Discription	Value
9m	Mutual trans conductance of the transistor	50 mA V
LPP,	Base spreading resistance	1001
rbe (or) re	Resistance between base and emitter (or) Dynamic resistance of emitter diode.	ιkΩ

Parameter	Discription	Value.
۳ _۵ ۱ و (۵۲) ۲ _۲	Feed back resistance due to early effect	4MD
rce.	Output resistance	80kN
Cble (or) Ce	Diffusion Capacitance	100Pf
Cbc (or) Cc	Trans Capacitance	3 P.F.

DETERMINATION OF HYBRID-TT CONDUCTANCES:-

TRANSISTOR TRANSCONDUCTANCE (GM)!-

Let us consider a P-n-p transistor in the CE configuration with Vcc bias in the collector Circuit



Now,

from the definition of transconductance we know that

The expression for the collector current in an n-p-n transistor is given as

$$T_c = T_{c_0} + \alpha T_{\epsilon}$$

Partial differentiate the above expression.

$$\partial I_c = \alpha \partial I_E$$
 [... $I_{c_0} = Constant$]

Substitute in gm.

The emitter diode resistance rue is given as

$$\Rightarrow$$
 $g_m = \frac{\alpha}{r_a}$

The dynamic resistance of the emitter diode is given

as

$$Y_e = \frac{V_T}{T_e}$$

Where Y = Volt equivalent temperature, which is given as $\frac{KT}{9}$.

K = Boltzmannis Constant.

9 = Charge of electron.

substitute above value in 9m

$$g_{m} = \frac{\alpha I_{\varepsilon}}{V_{t}} = \frac{I_{c} - I_{c_{o}}}{V_{t}}$$

Out
$$T_c > T_{co}$$

$$9m = \frac{T_c}{V_T}$$

$$9m = \frac{T_c}{(RT)}$$

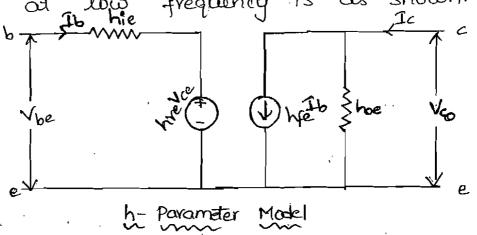
$$9m = \frac{T_c}{(RT)}$$

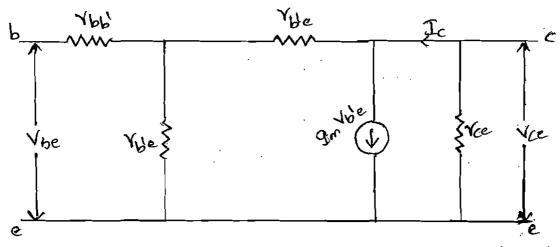
$$9m = \frac{T_c}{(RT)}$$

At room temperature T = 300 K9m = 38.66 Tc

INPUT CONDUCTANCE (96e):-

The hybrid-TT model and h-parameter for CE configuration at low frequency is as shown.





Here the capacitors acts as an open circuit.

From hybrid model of collector current.

As
$$V_{CE} = 0$$
, here $= \frac{T_C}{T_h} - 0$

From hybrid-TI low frequency model

As the value of (YUC = 4MN) >> Ybe

: The loase current I'm completely flows through Yblc.

As the value of Ybc is much more larger, the Collector current Ic = 9m Vbe

$$\frac{\Im_c}{\Im_b} = g_m r_{ble}$$



SPREADING CONDUCTANCE (966):-

From the expression of h-parameter model

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hie =
$$\frac{V_{be}}{T_{b}}$$

Now, applying KVL at input from Vie the hybrid-TI low frequency circuit.

Vbe = T_{b} T_{bb} $+ T_{b}$ T_{be}

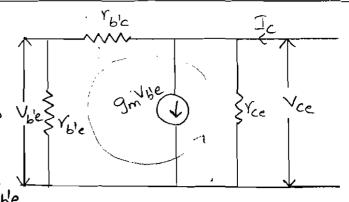
Vbe = T_{b} T_{bb} $+ T_{b}$ T_{be}

From T_{bb} $+ T_{b}$ $+ T_{be}$ $+ T_{be}$

Let us consider the h-parameter model for CE configuration with input open circuit (16=0),

Apply KVL to output loop

As the base current is zero Vies Ybe
the total current flowing
through Ybe is i, and Vbe=Vbe



$$\widehat{I}_{l} = \frac{V_{ce}}{(Y_{b'c} + Y_{b'e})} - 2$$

$$V_{b'e} = \frac{V_{ce} \cdot Y_{b'e}}{(Y_{b'c} + Y_{b'e})}$$

We know that

$$V_{be} = \frac{V_{ce} \cdot Y_{b'e}}{(Y_{b'c} + Y_{b'e})}$$

From 1

hre =
$$\frac{r_{b'e}}{\left[r_{b'c}+r_{b'e}\right]}$$

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$$Y_{b'c} = Y_{b'e} \left(\frac{1}{hre} - 1 \right)$$

OUTPUT CONDUCTANCE (9ce):

From hybrid parameter model

From hybrid -TT low frequency model

Ic =
$$\frac{V_{ce}}{Y_{ce}}$$
 + $9mV_{be}$ + I, V_{ce} $\left[-\frac{V_{ce}Y_{be}}{Y_{bc} + Y_{be}} \right]$ = $\frac{V_{ce}}{Y_{ce}}$ + $9mV_{be}$ + $\frac{V_{ce}}{Y_{bc} + Y_{be}}$

Ic =
$$\frac{V_{ce}}{Y_{ce}} + \frac{g_m V_{ce} Y_{be}}{Y_{bc} + Y_{be}} + \frac{V_{ce}}{Y_{bc} + Y_{be}}$$

Divide with Vce

from (1)

$$hoe = \frac{Ic}{V_{ce}}$$

$$hoe = \frac{I}{V_{ce}} + \frac{9mYbe}{Ybc+Ybe} + \frac{I}{Ybc+Ybe}$$

$$9mYbe=hee$$

Ithre & hee

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HYBRID IT - CAPACITANCE !-

There exists two types of capacitances in a transistor. They are diffusion capacitance and transition capacitance. The transition capacitance Cc = Cbc can be findout by using common base method in which the capacitance exists at the output i e between collector and base by making input current Ie=0.

The diffusion capacitance is a combination of emitter

diffusion capacitance and junction capacitance.

But the effect of emitter diffusion capacitance is more when junction compared to junction capacitance.

The expression of base width charge QB is given as

$$Q_{g} = \frac{1}{2} P'(0) \cdot (A) (\omega) (9) - 0$$

Where $\frac{1}{2}$ pl(0) is average minority charge distribution in base

The expression of diffusion current I is given as

p'(0)

$$T = (A)(9)(D_B) \frac{dP}{dx}$$

Where

$$\frac{dP}{dx} = \frac{P'(0)}{W}$$

$$T = (A)(Q)(DB) \cdot \frac{P^{l}(O)}{W} - 2$$

Now combine 1) and 2 equations

$$(A)(9)(P(0)) = \frac{T \cdot W}{D_R} \qquad \frac{1}{L^{50}}$$

$$\Phi_{\mathcal{B}} = \frac{T}{2} \cdot \frac{\omega^2}{D_{\mathcal{B}}}$$

The emitter diffusion capacitance is given as mate of change of QB with respect to voltage V'.

$$C_{DE} = \frac{dQ_{B}}{dV} = \frac{W^{2}}{2D_{B}} \cdot \frac{dI}{dV}$$

$$E_{DE} = \frac{W^{2}}{2D_{B}} \cdot \frac{1}{M_{e}} \qquad \left[\frac{1}{2} \cdot M_{e} - \frac{dV}{dI} - \frac{V_{T}}{I_{E}} \right]$$

$$\Rightarrow C_{De} = \frac{W^2}{2D_B} \cdot \frac{T_E}{V_T}$$

collector

$$\Rightarrow C_{De} = \frac{\omega^2}{2DB} \left[g_m \right] \qquad \left[Since \ g_m = \frac{T_E}{V_T} \right]$$

From the above expression we can say that the emitter diffusion capacitance is proportional to emitter current T_E . But practically the diffusion capacitance is found by the value of f_T , which is the frequency at which voltage gain drops to unity.

VALIDITY OF HYBRID-IT PARAMETERS:

Assuming that VBE changes so slowly with time that the minority-carrier charge distribution in the base stegion is always triangular.

While by observing the expression of hybrid-IT parameters we can say that these parameters are independent of frequencies of they satisfies

We know that

$$C_{DE} = \frac{\omega^2}{2D_B} \cdot g_m$$

$$\frac{\omega^2}{6p_B} = \frac{C_{DE}}{39m} - 2$$

and

$$C_e = \frac{g_m}{2\pi f_r}$$

$$\frac{Ce}{9m} = \frac{1}{2\pi f_{T}} = \frac{1}{6\pi f_{T}}$$

Substituting
$$2$$
 in 1
 $2\pi f \cdot \frac{Coe}{39m} < < 1$
 $2\pi f \cdot \frac{1}{39m} < < 1$
 $\frac{f}{3fr} < < 1$
 $\frac{f}{3fr} < < 1$

The hybrid TT parameters are valied upto 3ft.

VARIATION OF HYBRID-IT PARAMETERS WITH RESPECT TO

Ic, VCE, AND TEMPERATURE:

The hybrid parameters are dependent on the magnitudes of Ic and Vce and the temperature. The dependence of hybrid parameters such as 9m, rbb, rbe, Ce, Co, he and hie on the collector current magnitude |Ic|, the collector to emitter voltage magnitude |VcE|, and the temperature is shown in the following table:

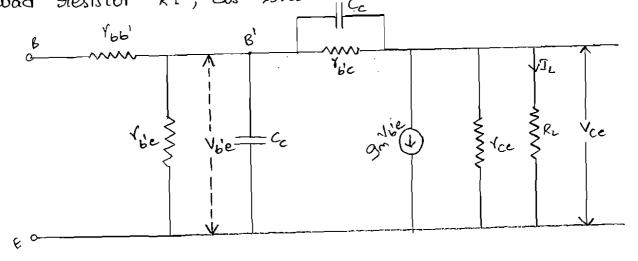
parameter Variation with Increasing T				
Para.	IIcl	l VCE	<u> </u>	
9m	Increases - proportio - nal to [Ic]	Independent	Decreases - Inversely proportional to T i.e YT.	
766°	Decreases - due to conductivity modula- tion of the base		Increases due to decrease in conductivity as a nesult of decrease in the mobility of majority and minority carriers.	

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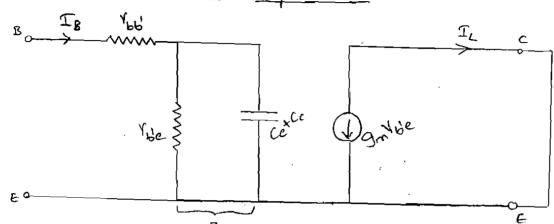
parameter	IIc)	1 VCE1	7
r _{b'e}	Decreases - inversely Proportional to [Ic]	Increases.	Increases.
Ce	i.e 1/1Icl. Increases - proportional to 1Icl	Decreases.	
C _c	Independent	Decreases	Independent
hpe	Increases for smaller values of [Ic] and decreases with higher values of IIcl	increase of transistor	Increases.
hie	Decreases - inversely Proportional to IIII i-e 1/ IIII	Increases	Increases

COMMON EMITTER SHORT CIRCUIT CURRENT GAIN:

Consider a single stage CE transistor amplifier with load Dresistor RI, as shown below.



for the analysis of the short circuit current gain we have to assume $R_L = 0$. With RL = 0, i.e output short circuited rice becomes zero. The ice and Chic appears in parallel when C_L (Chic) appears between base and emitter it is known as 'Miller Capacitance'.



SIMPLIFIED HYBRID -TT MODEL FOR SHORT CIRCUIT CE TRANSISTOR.

Here (Y_{bc}) and $(C_{c}+C_{e})$ are in shunt there exists an equivalent Impedance 'Z'.

$$\frac{z = \text{Ybe} / (c_c + c_e)}{\text{Ybe} \times \frac{1}{\text{jw}(c_c + c_e)}}$$

$$= \frac{\text{Ybe} \times \frac{1}{\text{jw}(c_c + c_e)}}{\text{jw}(c_c + c_e)}$$

Then the circuit can be redrawn as

The state of the stat

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$$A_{I} = \frac{I_{L}}{I_{b}} = \frac{-g_{m}V_{ble}}{I_{b}}$$
Where $V_{ij} = \overline{I}_{ij}$

Where
$$V_{be} = I_b Z$$

$$A_{T} = \frac{-h_{fe}}{1+j2\pi f \gamma_{ble} (c_{c}+c_{e})}$$

As current gain AI is inversely proportional to frequency.

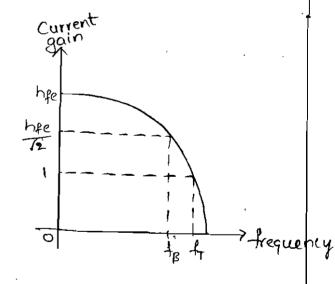
At low frequency, the value of denominator

is approximately equal to 1 Then current gain

can be written as

Magnitude of AI

When of = fB



PARAMETER & :-

It is the frequency obtained when a short.

Circuit CE current gain drops to 3dB (or) 1/2 times of its value.

i.e.

$$\frac{1}{2\pi r_{be}} = \frac{9b^{b}e}{2\pi (C_{e}+C_{e})} = \frac{9b^{b}e}{2\pi (C_{e}+C_{e})} = \frac{9m}{h_{fe}} = \frac{9m}{h_{fe}+C_{e}}$$

$$= \frac{9m}{h_{fe} 2\pi (C_{e}+C_{e})}$$

$$= \frac{9m}{h_{fe} 2\pi (C_{e}+C_{e})}$$

PARAMETER (fx)-

It is the frequency obtained when a short circuit common base current gain drops to sdB (or) $\frac{1}{\sqrt{2}}$ times of its value.

Current gain of co amplifier can be given as

$$|AT| = \frac{h_{fb}}{\sqrt{1 + (f/f_{ec})^2}}$$

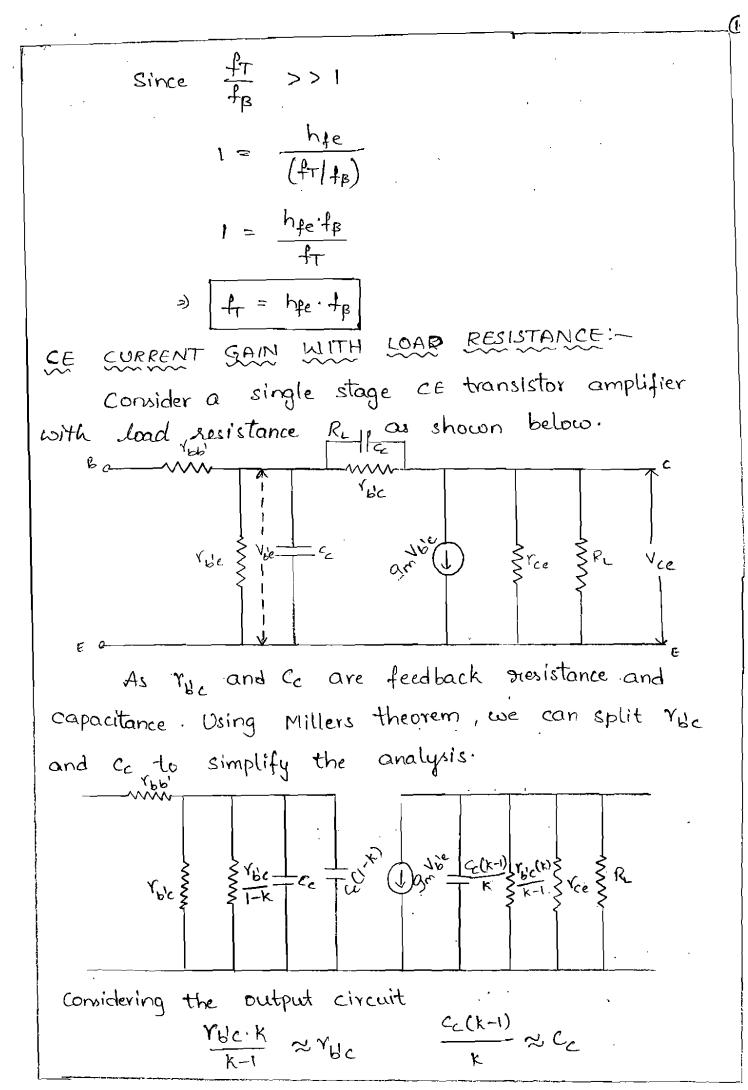
Where for is given as

PARAMETER (ft):-

It is a frequency when a short circuit CE Current gain is equal to i (or) unity.

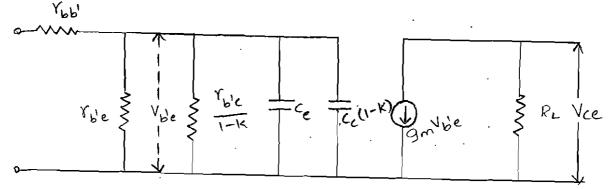
.: The expression of current gain is given as

$$1 = \frac{hfe}{\sqrt{1 + (ff/fg)^2}}$$



As we know that Rce = 80kN, Ybc = 4MD, C=3PF.

The load impedance RL is considered to be very less when compared to Yce, Ybe. From the above approximations the circuit can be riedrawn as



Voltage Gain (Av):-

$$A_V = \frac{V_b}{V_i} = \frac{V_{ce}}{V_{ble}}$$

Considering 9m = 50 mA/v, and RL = 2ksl then

$$K = -\left[(50)(2) \right]$$

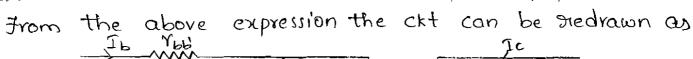
Considering the input circuit:

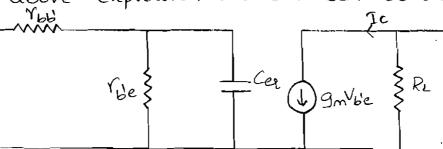
$$\frac{Y_{b'c}}{1-k} = \frac{Y_{b'c}}{101} = \frac{4 \times 10^{16}}{101} = 40k$$

$$C_c(1+K) = C_c(1+9mR_L)$$

= $(3x10^{12})(1+100)$

=





Where
$$Ce_{q} = Ce + Ce (1+9mR_{L})$$
 T_{b}
 T_{b}

Current Gain:
$$A_{I} = \frac{I_{o}}{I_{i}} = \frac{I_{L}}{I_{b}} = \frac{-g_{m}v_{ge}}{I_{b}}$$

where
$$V_{be} = I_b Z$$
.

$$A_{I} = \frac{-g_m \cdot I_b Z}{I_b} = -g_m \cdot Z$$

$$= -g_m \cdot Z_b Z$$

$$A_{I} = \frac{-9m\gamma_{be}}{1+\gamma_{be}(j\omega(c_{c}+c_{e}))} \qquad \omega = 2\pi f$$

As a /f at low frequencies the value of denominator is approximately equal = 1

(1)

GAIN BANDWIDTH PRODUCT:-

FOR VOLTAGE:

We know that

AvsfH

Avs = Voltage source gain

+4 = Band width.

We know that

$$Avs = \frac{-h_feR_L}{R_s + hie}$$

$$(R_s + Y_{bb'})$$
 | $Y_{b'e} \Rightarrow R_{eq} = \frac{Y_{b'e}(R_s + Y_{bb'})}{Y_{b'e} + R_s + Y_{bb'}}$

$$Req = \frac{r_{b'e} \left(R_S + r_{bb'} \right)}{R_S + hie}$$
 [: hie = $r_{b'e} + r_{bb'}$]

(II)

$$C_{eq} = C_e + C_c \left[1 + g_m R_L \right]$$

$$= C_e + C_c g_m R_L \qquad \left[- g_m R_L >> 1 \right]$$

We know that

9m = 2TT-frce [From CE short circuit]

Now substituting the value of 9m in above equation.

$$|AvsfH| = \frac{R_L}{R_S + r_{bb!}} \frac{fT}{(1 + C_c 2\pi f_1 R_L)}$$

FOR CURRENT:-

We know that

$$A_{IS} = \frac{-h_{fe}R_{S}}{R_{S} + h_{ie}}$$

(13)

$$f_{H} = \frac{1}{2\pi Y_{ble} Cea}$$

$$= \frac{-h_{fe}R_{s}}{R_{s} + h_{ie}} \cdot \frac{1}{2\pi Y_{ble} Cea}$$

$$= \frac{-h_{fe}R_{s}}{R_{s} + h_{ie}} \cdot \frac{1}{2\pi Y_{ble} (e_{q})}$$

$$(R_{s} + Y_{bb}) || Y_{ble} \Rightarrow Req = \frac{Y_{ble}[R_{s} + Y_{bb}]}{Y_{ble}[+R_{s} + Y_{bb}]}$$

$$Req = \frac{Y_{ble}[R_{s} + Y_{bb}]}{R_{s} + h_{ie}}$$

$$= \frac{-h_{fe}R_{s}}{R_{s} + h_{ie}} \cdot \frac{1}{2\pi Cea} (Y_{ble}[R_{s} + Y_{bb}])$$

$$= \frac{-h_{fe}R_{s}}{R_{s} + h_{ie}} \cdot \frac{R_{s} + h_{ie}}{2\pi Cea} (Y_{ble}[R_{s} + Y_{bb}])$$

$$A_{fs}H_{h} = \frac{-g_{m}R_{s}}{2\pi Cea} (R_{s} + Y_{bb})$$

$$\Rightarrow |A_{fs}H_{h}| = \frac{g_{m}}{2\pi Cea} \cdot \frac{R_{s}}{R_{s} + Y_{bb}}$$

$$Cea = Ce + \frac{c}{c} (l + g_{m}R_{L}) (\cdot)$$

$$= Ce + Ce \cdot g_{m}R_{L}$$

$$|A_{fs}H_{h}| = \frac{R_{s}}{R_{s} + Y_{bb}} \cdot \frac{g_{m}}{2\pi (Ce + Ce \cdot g_{m}R_{L})}$$

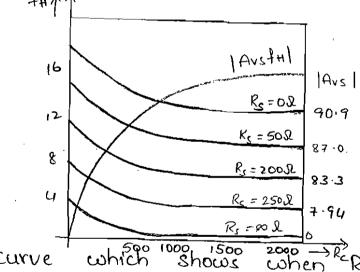
We know that 9m = 2TTfTCe

$$|AISHH| = \frac{R_S}{R_S + Y_{bb}} \cdot \frac{2\pi f_T Ce}{2\pi f_T Ce R_L}$$

$$= \frac{R_S}{R_S + Y_{bb}} \cdot \frac{2\pi f_C f_T}{2\pi f_C f_T}$$

$$|AISHH| = \frac{R_S}{R_S + Y_{bb}} \cdot \frac{f_T}{(1 + C_C 2\pi f_T R_L)}$$

By considering the above gain bandwidth products the plot between RL and fH is given as



The curve which shows when Rs =0 is an ideal

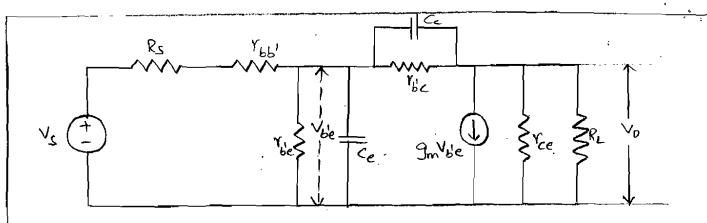
transistor voltage gain, when $R_s = \infty$ is the ideal transistor

Current gain.

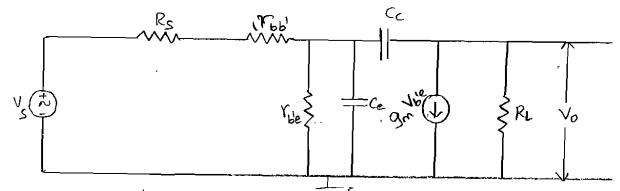
SINGLE STAGE TRANSISTOR CE AMPLIFIER RESPONSEL

Let us consider a single stage CE amplifier with source Vs and finite resistance Rs. The equivalent circuit of such CE stage is shown below.

<u>(1)</u>



As the values of v_{bc} and v_{ce} are very large we can eliminate those two parameters from the circuit. Then circuit converts into



Equivalent Circuit for single stage CE Amplifier

Applying KVL at node b' by using Laplace transforms

At node 'c':-

Vbe
$$\left[9m - SC_c\right] + V_0\left(\frac{1}{R_c} + SC_c\right) = 0$$
 -2

From 2, we get

$$V_{ble} = \frac{-V_0 \left(\frac{1}{R_L} + SC_c \right)}{g_m - SC_c}$$

Now substitute this value of Vie in 1

$$V_SG_S' = \left(g_{b'e} + G_S' + S(C_e + C_c)\right) \left(\frac{-V_o\left(\frac{1}{R_L} + SC_c\right)}{g_m - SC_c}\right) - V_oSC_c$$

$$V_{S}G_{S}' = \left[g_{be} + G_{S}' + S\left(c_{e} + c_{c}\right)\right] \left[\frac{-V_{o} + R_{L}Sc_{c}}{R_{L}g_{m} - Sc_{c}R_{L}}\right] - V_{o}Sc_{c}$$

$$Av_{S} = \frac{V_{o}}{V}$$

$$V_{8}G_{3}'(g_{m}-SC_{c}) = -V_{0}\cdot(/P_{L}+SC_{c})(G_{8e}+G_{8}+S(C_{e}+C_{c})) - V_{0}SC_{c}(g_{m}-SC_{c})$$

$$\frac{V_0}{V_S} = \frac{-G_S'(g_{m}-Sc_c)}{\left(\frac{1}{R_L}+Sc_c}(G_{be}+G_S'+S(C_e+C_c))+Sc_c(g_{m}-Sc_c)\right)}$$

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$$\frac{V_0}{V_S} = \frac{-G_S' R_L \left(9_m - SC_L \right)}{G_S' + 9_B'e} + S \left(c_e + C_L \right) + G_S' R_L SC_L + 9_B'e R_L SC_L + S^2 C_L \left(c_e + C_L \right) R_L + 9_m R_L SC_L - S^2 C_L R_L + 9_m R_L SC_L - S^2 C_L R_L - G_S' R_L \left(9_m - SC_L \right)$$

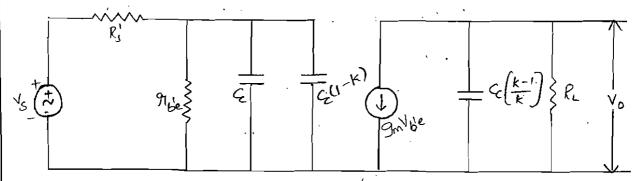
$$-G_S' R_L \left(9_m$$

$$S_0 = \frac{9m}{c_c}$$
 $S_0 = \frac{9m}{c_c}$
 $S_0 = \frac{50 \times 10^{-3}}{3 \times 10^{-12}} = 0.01 \times 10^{-12}$

The poles of the transfer function can be found by finding the noots of the denominator.

SIMPLIFIED MODEL:

Using Miller's theorem we can simplify the analysis of CE amplifier stage. The equivalent circuit is shown below.



As value of K>>1, the output capacitance $C_c\left[\frac{K-1}{K}\right]\approx C_c$

The time constant of output circuit is given as $T_0 = R_L C_L$

= 3x1012 x3x103 = 9nsec.

By eliminating C_c the value of k is given as $V_0 = -9_m V_{ble} R_L$

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$$k = \frac{V_0}{V_{ble}} = -g_m R_L$$

$$k = -g_m R_L$$

Equivalent sussistance at the input R=Rill rbe

$$R_s^1 = R_s + Y_{bb'}$$

= 50+100 = 150

$$R = \frac{150 \times 1 \times 10^3}{150 + 1 \times 10^3} = 130.43 \Omega$$

$$C = C_{e} + C_{c}(1-k) = C_{e} + C_{c}(1+g_{m}R_{L})$$

$$\Rightarrow 100 \times 10^{-12} + 3 \times 10^{-12} \left[1 + 50 \times 10^{3} \times 3 \times 10^{3}\right]$$

$$\Rightarrow 5.53 \times 10^{10} \text{ p.}$$

Time constant of Input circuit

$$T_{in} = RC = (130.43) \times (5.53 \times 10^{10})$$

= 7.21 × 10⁸

As the time constant of the input circuit is greater than output circuit. The bandpass single stage CE amplifier completely depends upon input time constants.

By eliminating the output time constants the eq 3 converts into

$$Av_{s} = \frac{V_{o}}{V_{s}} = \frac{-G_{s}^{l} g_{m}R_{L}}{G_{s}^{l} + g_{bl}e + s(C_{e} + C_{c})}$$

$$\begin{bmatrix} \cdot \cdot \cdot \cdot c_{e} + C_{c}(1-k) \\ - \cdot \cdot \cdot c_{e} \end{bmatrix}$$

The above transfer function looks like

$$Avs = \frac{k_2}{S-S_1}$$

It's a single pole transfer function, the pole of this transfer function is found by equating S=S,

$$\frac{G_{s}' + 9_{b'e} + S_{i}C = 0}{S_{i} = \frac{-(G_{s}' + 9_{b'e})}{C}}$$

$$S_1 = -\frac{(q_3' + 9be)}{c} = \frac{-1}{RC}$$

=
$$\frac{1}{(130-4)(405\times10^{12})}$$
 = -19×10^6 rad/s

The upper 3dB frequency of a single pole function is given by $\frac{1}{2\pi c}$, where τ is the time constant of the circuit; thus.

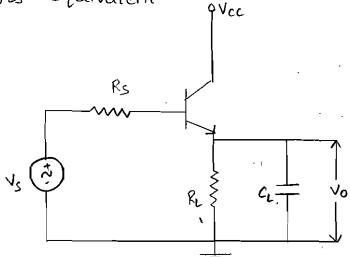
$$f_{H} = \frac{1}{2\pi RC} = \frac{|S_{1}|}{8\pi} \qquad \left(-\frac{1}{2}S_{1} = \frac{-1}{RC}\right)$$

$$= \frac{19 \times 10^{6}}{2\pi} = 3.024 \text{ MHz}$$

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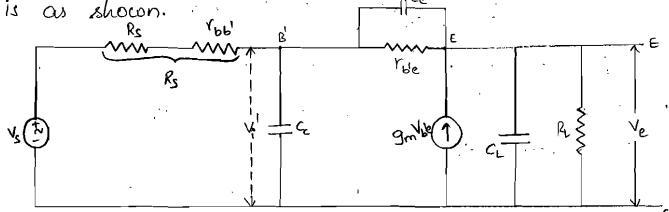
EMITTER FOLLOWER AT HIGH FREQUENCIES:

Let us see the high frequency response of the emitter follower. A capacitor CL is connected as load because of emitter follower is often used to drive capacitive loads. Its equivalent circuit is shown below.



EMITTER FOLLOWER AMPLIFIER

The equivalent circuit of high frequency emitter follower



Assuming that current leaving a node as positive and current entering a node as negative

$$(v_s - v_i') q_s' = v_i' j \omega c_c + (v_i' - v_{in}) (g_{b'e} + j \omega c_e)$$

Applying KCL at Mode E.

$$\begin{aligned} & \left[V_{i}^{1} - V_{E} \right] \left[g_{b'e} + j \omega c_{e} \right] = -g_{m} V_{b'e} + V_{e} \left[\frac{1}{R_{L}} + j \omega c_{L} \right] \\ & 0 = -g_{m} V_{b'e} + V_{e} \left[\frac{1}{R_{L}} + j \omega c_{L} \right] - \left[V_{i}^{1} - V_{e} \right] \left[g_{b'e} - j \omega c_{e} \right] \\ & = -V_{i} \left[g_{b'e} + j \omega c_{e} \right] + V_{e} \left[g_{b'e} + j \omega \left(c_{e} + c_{L} \right) + \frac{1}{R_{L}} \right] - g_{m} V_{b'e} \\ & = V_{i}^{1} - V_{e} \end{aligned}$$
We know that $V_{b'e} = V_{b'} - V_{e}$

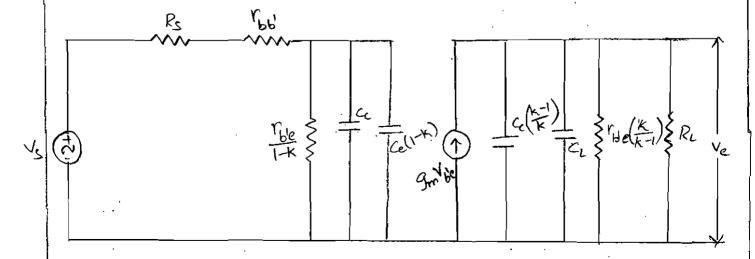
$$= V_{i}^{1} - V_{e}$$

$$0 = -V_{i}^{\prime} \left[9be + 9m + jwc_{e} \right] + V_{e} \left[9be + 9m + \frac{1}{R_{L}} + jw(c_{e} + c_{c}) \right]$$
 $0 = -V_{i}^{\prime} \left[9 + jwc_{e} \right] + V_{e} \left[9 + \frac{1}{R_{L}} + jw(c_{e} + c_{c}) \right]$

By combining these two expressions we get a transfer function with 2 poles. To convert a transfer function into a single pole through transfer function we have to apply millers theorem for emitter follower circuit.

SINGLE POLE ANALYSIS:

By applying Miller's theorem the Cc Circuit converts into



In a common collector the value of K (Voltage gain) As k is voltage gain of common collector amplifier the value of K=1

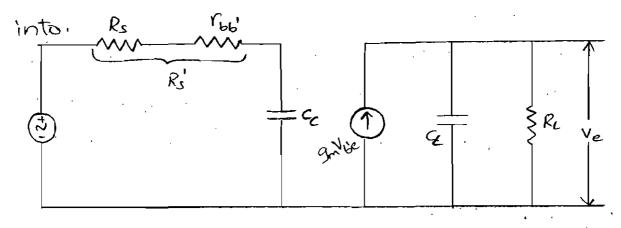
From the output circuit

$$V_{ble}\left(\frac{k}{k-1}\right) = \infty$$
 [so, that resistance is open]

 $C_{ble}\left(\frac{k-1}{k-1}\right) = 0$ [so, C_{ble} is open]

From the input circuit

For these approximations the above circuit converts



Time constant of RC circuit

Tin = $R_s' C_c$ $R_s' = R_s + \frac{\pi}{8} Y_{bb}'$ = 150 x 3 x 10²

Time constant of output circuit is given as $r_0 = R_L C_I$

=450×10-12 = 450Ps.

As the value of CL is very large the time constant of output circuit is for greater than output

The bandpaus of emitter follower depends on the output time constant.

$$\begin{aligned} & = 1 - \overline{t} \\ & = R_{L} \| c_{L} = \frac{R_{L} \cdot j_{\omega} c_{L}}{R_{L} + j_{\omega} c_{L}} \\ & = \frac{R_{L}}{I + j_{\omega} R_{L} c_{L}} \end{aligned}$$

$$\begin{aligned} & = \frac{R_{L}}{I + j_{\omega} R_{L} c_{L}} = \frac{1}{\frac{N_{L}}{N_{L} + j_{\omega} c_{L}}} \end{aligned}$$

$$V_{e} = \frac{I \cdot R_{L}}{I + j_{\omega} R_{L} c_{L}} = \frac{1}{\frac{N_{L}}{N_{L} + j_{\omega} c_{L}}}$$

$$V_{e} = \frac{I \cdot R_{L}}{I + j_{\omega} R_{L} c_{L}}$$

$$[I = 9_{m} V_{b}]_{e}$$

As
$$V_{be} = V_{i}^{1} - V_{e}$$

$$V_{e} = \frac{9mR_{L}\left(v_{i}^{1} - V_{e}\right)}{1 + i\omega R_{i}C_{i}}$$

$$k = \frac{3mR_L}{(1+9mR_L)\left(\frac{1+3coR_LC_L}{1+9mR_L}\right)}$$

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$$K = \left(\frac{3mR_L}{1+9mR_L}\right) \cdot \left(\frac{1}{1+\frac{j\omega R_LC_L}{1+9mR_L}}\right)$$

$$= \frac{9mR_L}{1+\frac{j2\pi fR_LC_L}{1+\frac{j2\pi fR_LC_L}{1+\frac{j2\pi fR_LC_L}{1+\frac{j2\pi fR_LC_L}{1+\frac{j2\pi fR_LC_L}{1+\frac{j2\pi fR_LC_L}}}}$$

$$= \frac{9mR_L}{1+\frac{j2\pi fR_L}{1+\frac{j2\pi fR_LC_L}{1+\frac{j2\pi fR_LC_L}{2\pi fR_LC_L}}}$$

$$k = \frac{1+\frac{jmR_L}{2\pi R_LC_L}$$

$$k = \frac{1+\frac{jmR_L}{2\pi fR_LC_L}$$

$$k = \frac{1}{1+\frac{j(ff_H)}{2\pi fR_LC_L}}$$

$$k = \frac{1}{1+\frac{j(ff_H)}{2\pi fR_LC_L}}$$

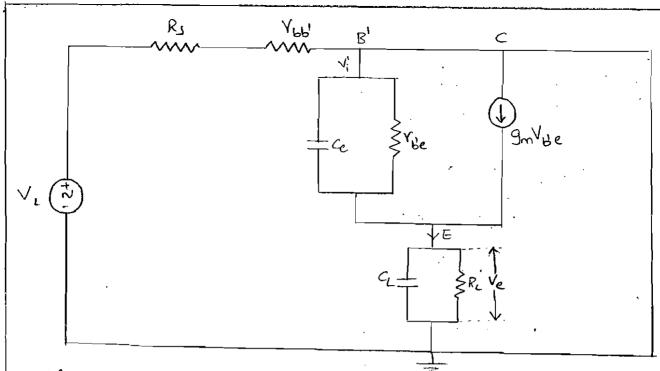
$$f_H = \frac{f_TC_L}{c_L}$$

$$f_H = \frac{f_TC_L}{c_L}$$

$$f_H = \frac{f_TC_L}{c_L}$$

BETTER APPROXIMATION FOR fu!-

The circuit is as shown in below figure. The circuit connections are shown to make them suitable for the derivation of fix.



From the above circuit we have

$$\widehat{T} = \left(v_{i}^{1} - v_{e}\right) \left[g_{b'e} + jwc_{e}\right] + g_{m}\left(v_{i}^{1} - v_{e}\right)$$

$$T = (V_i' - V_e) \left[\underbrace{g_m + g_{e} + j_{e}}_{g} + j_{e} c_e \right]$$

$$I = (V_i' - V_e)[9 + j\omega ce]$$

$$V_e = \left[V_i^1 - V_e\right] \left[9 + j \omega c_e\right] \frac{R_L}{1 + j \omega R_L C_L}$$

$$V_e = \left[1 + \frac{R_L(g+j\omega c_e)}{(+j\omega R_L C_L)} \right] = V_i \left[\frac{R_L(g+j\omega c_e)}{1+j\omega R_L C_L} \right]$$

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$$V_{e} = \frac{R_{L}(g+j\omega c_{e})}{1+j\omega R_{L}C_{L}+R_{L}(g+j\omega c_{e})}$$

$$T+j\omega R_{L}C_{L}+R_{L}(g+j\omega c_{e})$$

$$T+j\omega R_{L}C_{L}+R_{L}(g+j\omega c_{e})$$

$$A_{V} = \frac{V_{e}}{V_{i}} = \frac{R_{L}}{1+j\omega R_{L}C_{L}+R_{L}(g+j\omega c_{e})}$$

$$= \frac{gR_{L}}{1+gR_{L}} = \frac{1+j\omega c_{e}}{1+gR_{L}}$$

$$= \frac{gR_{L}}{1+gR_{L}} = \frac{1+j\omega c_{e}}{1+gR_{L}}$$

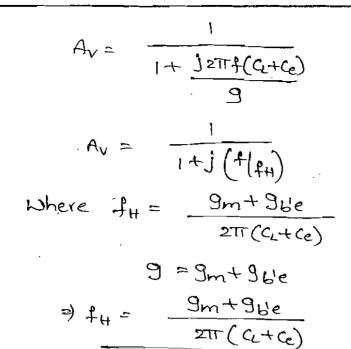
$$A_{V} = \frac{gR_{L}}{gR_{L}} = \frac{1+j\omega c_{e}}{1+j\omega R_{L}(C_{L}+c_{e})}$$

$$A_{V} = \frac{gR_{L}}{1+j\omega C_{L}+c_{e}}$$

$$= \frac{1+j\omega c_{e}}{gR_{L}+j\omega R_{L}(C_{L}+c_{e})}$$

$$= \frac{1+j\omega c_{e}}{gR_{L}+j\omega R_{L}(C_{L}+c_{e})}$$

$$= \frac{g+j\omega c_{e}}{g+j\omega c_{e}}$$



COMMON, SOURCE AMPLIFIER AT HIGH FREQUENCIES:

The above circuit shows the common source amplifier circuit and the small signal equivalent Circuit at high frequencies. The output voltage 'Vo' is given by

Where Iz short circuit current

Z= Impedance between the current terminals. Jo find the 'Z' we have to make input voltage sero (i-e short). Then the current source 9mV; =0. Then the impedance Z is a parallel combination of R, Yd, Ggs,

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$$T_2 = T_1 + T \rightarrow T = T_2 - T_1$$

Where
$$y_L = \frac{1}{R_L}$$

$$g_d = \frac{1}{Y_d}$$

(- ' Z = '/

YOLTAGE GAIN:-

$$\Theta_{V} = \frac{V_{0}}{V_{1}^{*}} = \frac{\mathfrak{I} \cdot \xi}{V_{1}^{*}} = \frac{\mathfrak{I}}{V_{1}^{*} y}.$$

$$A_{V} = \frac{V_{1} \left[Y_{gd} - g_{m} \right]}{V_{1} \cdot Y}$$

$$A_V = \frac{-9m}{Y_L + 9d}$$

$$Av = \frac{-9m}{\frac{1}{R_L} + \frac{1}{\eta_d}}$$

$$A_{V} = \frac{-g_{m}R_{L}R_{L}}{\eta_{d} + R_{L}} \qquad \left[\frac{1}{2} = \frac{\eta_{d}R_{L}}{\eta_{d} + R_{L}} \right]$$

INPUT ADMITTANCE:

At high frequencies the gate and drain are connected to the capacitance Cgd. This circuit can be simplified by using Millers theorem.

The impedance Cgd is replaced by an impedance at the input and the impedance at the output The admittance of miller's capacitance at the input is Ygd[1-k].

The admittance of miller capacitance is at the output is $y_{gd} = \frac{k-1}{k}$

$$Y_i = Y_{gs} + Y_{gs}[1-k]$$

where k = Voltage gain.

Output admittance :- (Yo):-

From the circuit the output admittance obtained by "looking into the drain" with the input voltage becomes zero. If $V_i = 0$, then Y_d , C_{ds} , C_{gd} in parallel Hence the output admittance Y_0 is given as

COMMON DRAIN AMPLIFIER AT HIGH FREQUENCIES:
COMMON DRAIN AMPLIFIER AT HIGH FREQUENCIES AT HIGH FREQU

YOLTAGE GAIN !-

The output voltage vo can be found from the product of the short circuit and the impedance between terminals Sand N. Thus voltage gain is given by.

$$\frac{V_0}{V_i} = \frac{g_m + j_{co} c_{gs}}{R_s + (g_m + g_d + j_{co} c_f)}$$

Where
$$G_T = Cg_S + C_{dS} + CSn$$

: Con = Capacitance from source to ground.

$$A_{V} = \frac{(9m+j\omega c_{gs}) R_{s}}{1+(9m+9d+j\omega c_{T})R_{s}}$$

At low frequencies the gain reduces to.

$$A_V = \frac{9mR_s}{1 + (9m + 9d)R_s}$$

Input Admittance -

The input admittance Y, can be obtained by applying Miller's theorem to Cgs. It is given by

$$Y_i = j\omega c_{gd} + j\omega c_{gs} (1 - Av) = j\omega c_{gd}$$
 [: $Av = 1$]

Output Admittance :-

admittance Yo, with Rs considered output external to the amplifier, is given by

At lower frequencies, the output resistance Rois

$$R_0 = \frac{1}{9m + 9d} = \frac{1}{9m}$$
 Since $9m >> 9d$