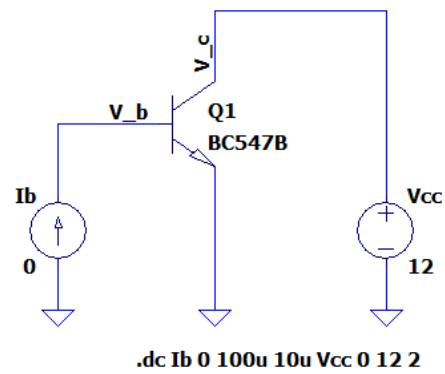


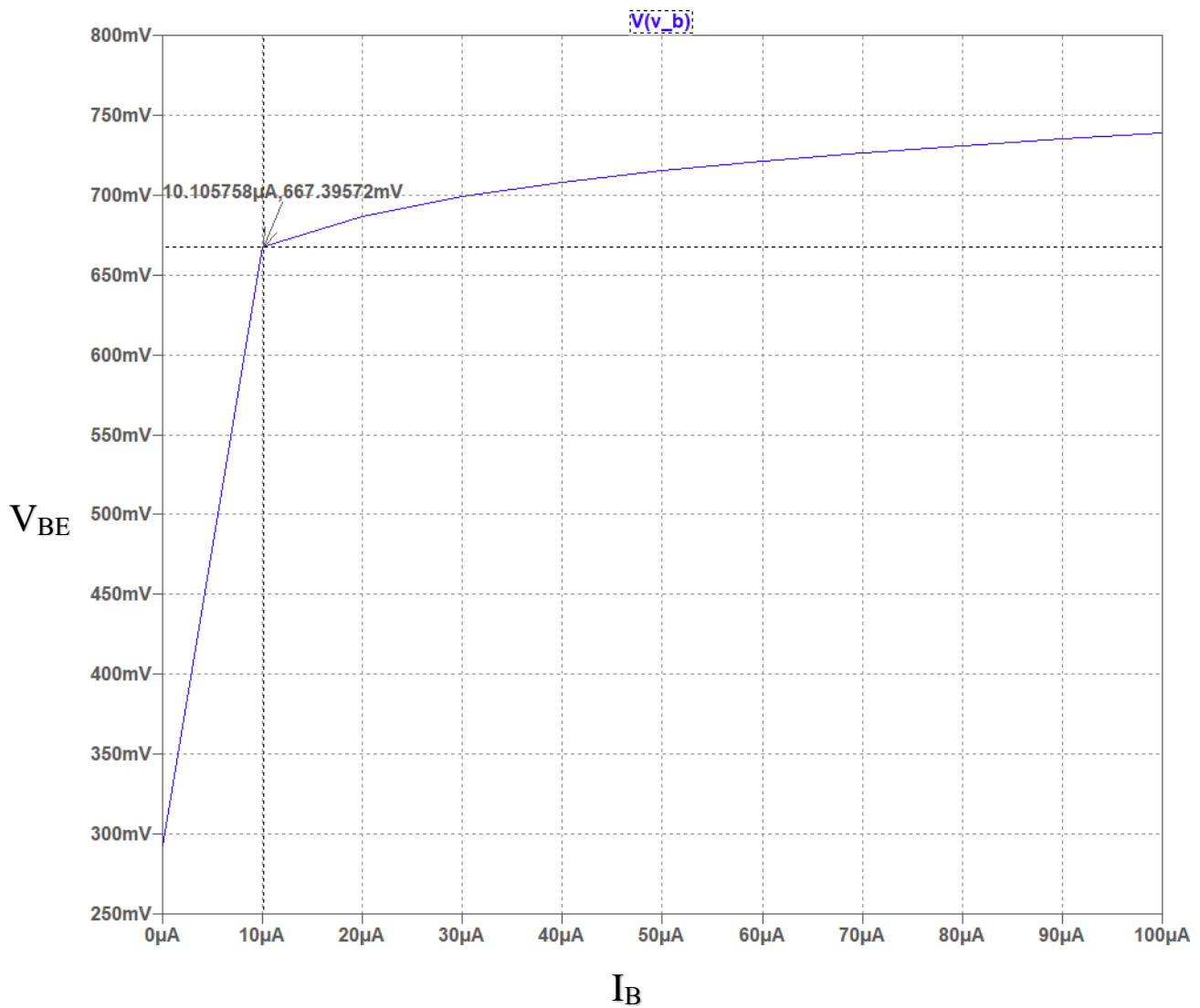
### III)

#### Q.3.1

Circuit Diagram:



$V_{BE}$  vs  $I_B$ :

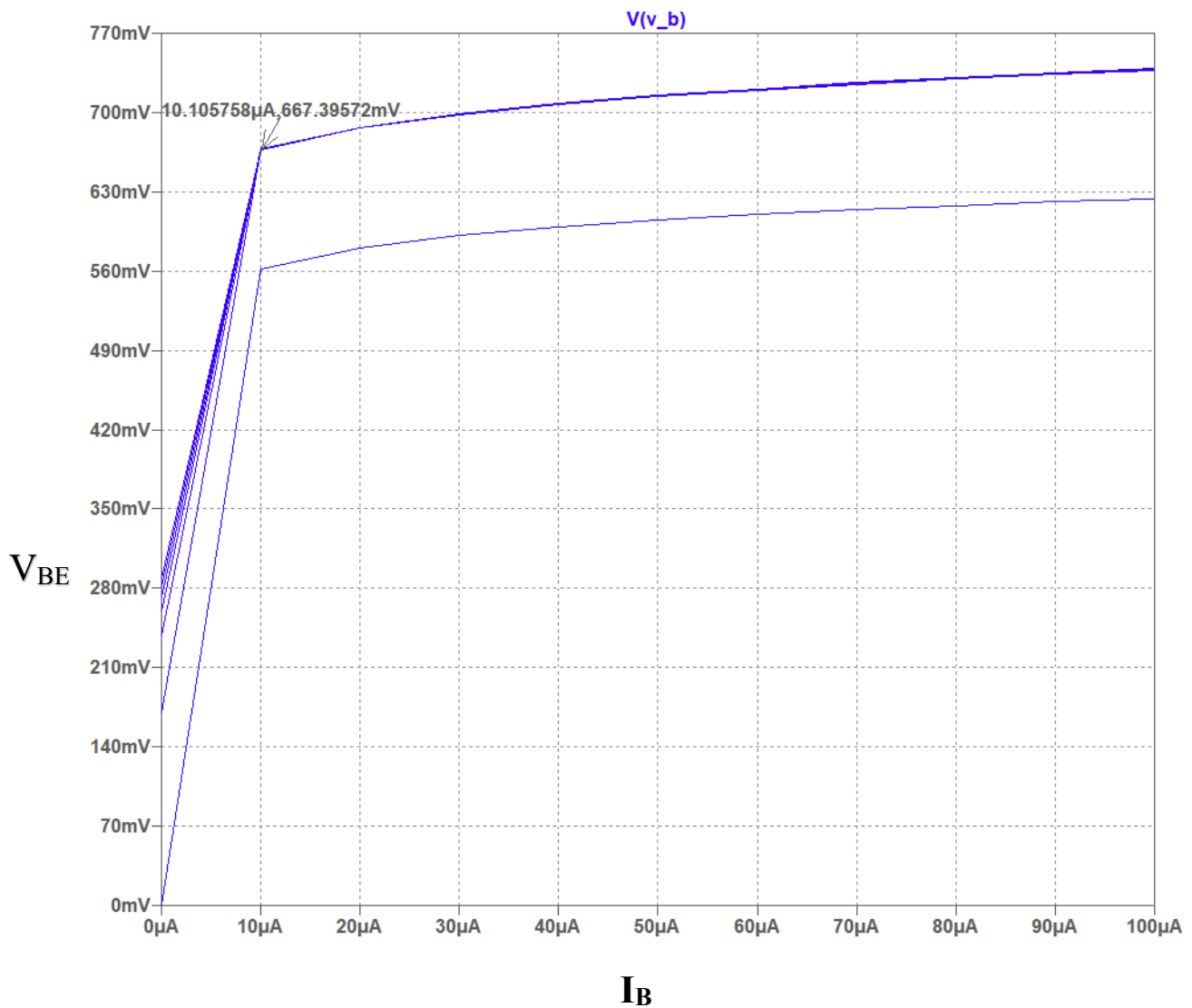


X Axis:  $I_B$  (ranging from 0 to  $100\mu\text{A}$  in steps of  $10\mu\text{A}$ )

Y Axis:  $V_{BE}$

Obtained Emitter Base Junction Voltage =  $667.39572\text{mV}$

$V_{BE}$  vs  $I_B$  and  $V_{CC}$ :



X Axis:  $I_B$  (ranging from 0 to  $100\mu\text{A}$  in steps of  $10\mu\text{A}$ )

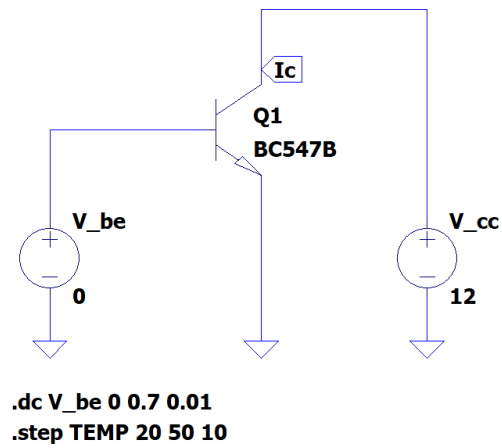
Y Axis:  $V_{BE}$

$V_{CC}$  ranging from 0 to  $12\text{V}$  in steps of  $2\text{V}$

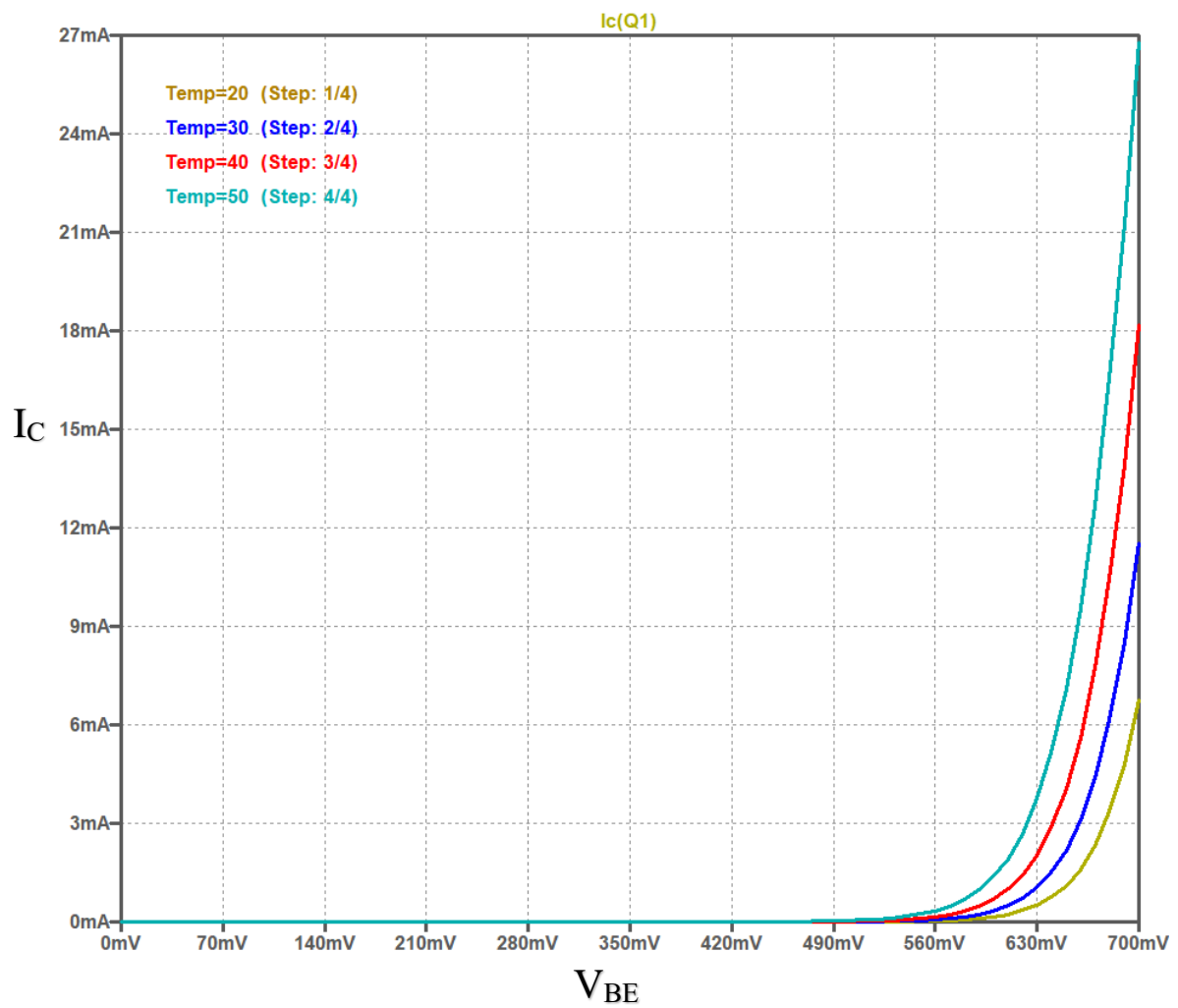
The different plot lines converging together after  $I_B$  shows that  $V_{BE}$  does not depend on  $V_{CC}$  while in Forward Active mode. The single separate plot line is for  $V_{CC} = 0$ , as it represents Saturation mode.

### Q.3.2

Circuit Diagram:



$I_C$  vs  $V_{BE}$  and Temperature:



X Axis:  $V_{BE}$  ranging from 0 to 700mV in steps of 10mV

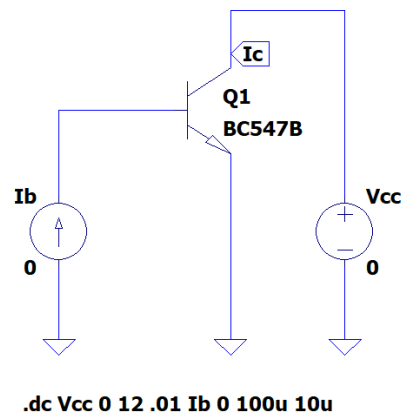
Y Axis:  $I_C$

Calculated for temperatures 20°C, 30°C, 40°C and 50°C

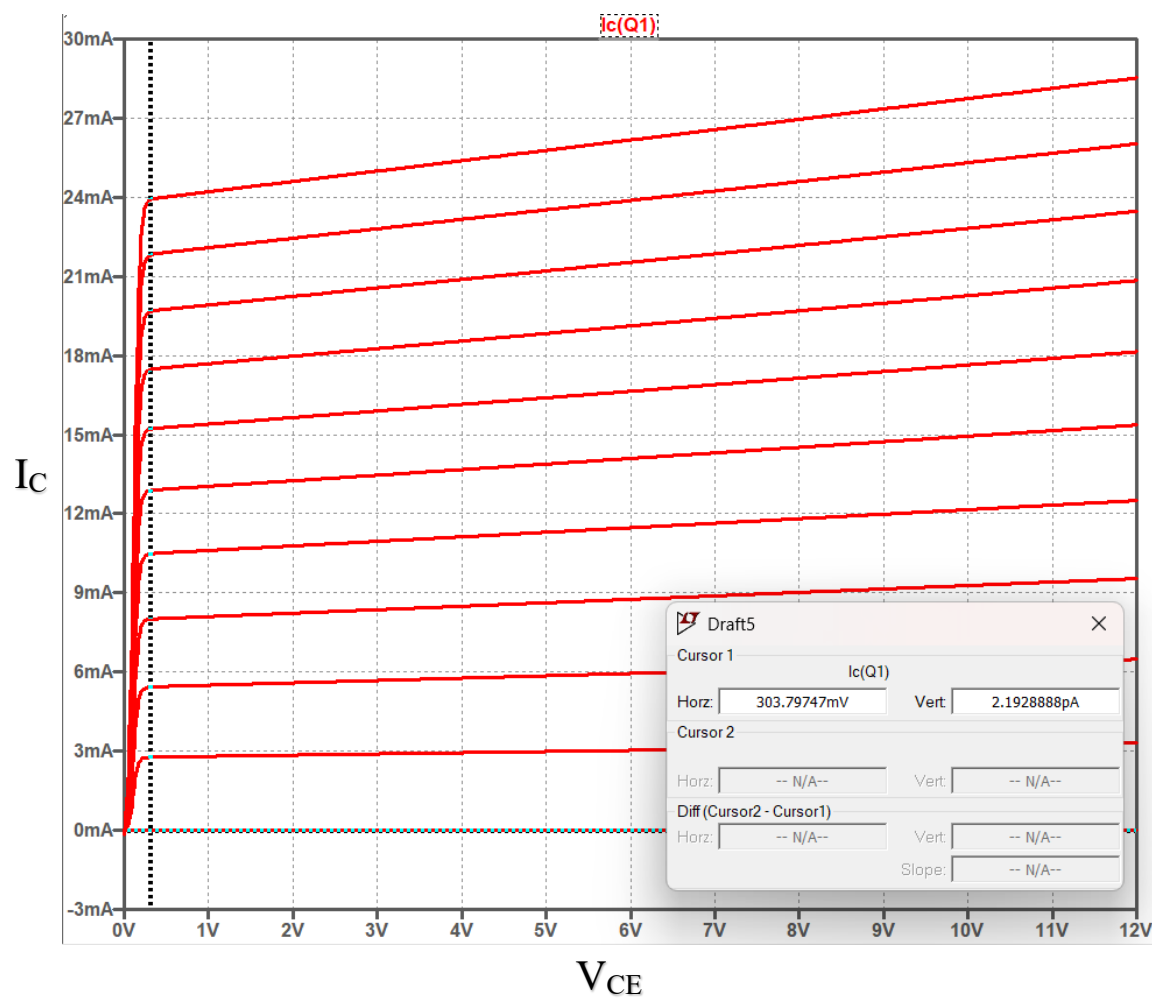
This calculation shows that collector current increases with temperature during Forward Active Mode of operation

### Q.3.3

#### Circuit Diagram:



#### Ic vs V<sub>CE</sub> and I<sub>B</sub>:



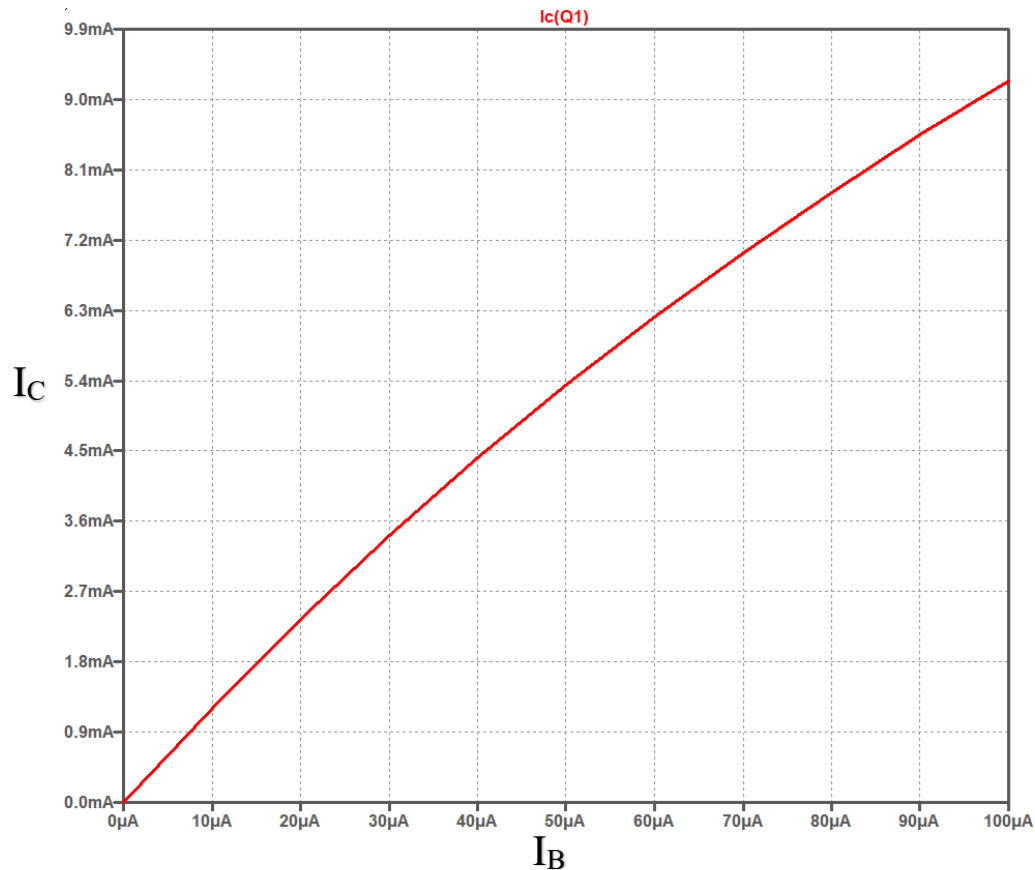
X Axis:  $V_{CE}$  (ranging from 0 to 12V in steps of 0.01V)

Y Axis:  $I_C$

$I_B$  ranging from 0 to 100 $\mu$ A in steps of 10 $\mu$ A

The boundary between the Saturation region and Forward Active region is roughly at  $V_{CE} = 303.79747\text{mV}$ , as shown by the cursor in the plot.

$I_C$  vs  $I_B$  at  $V_{CE} = 100\text{mV}$  (Saturation):

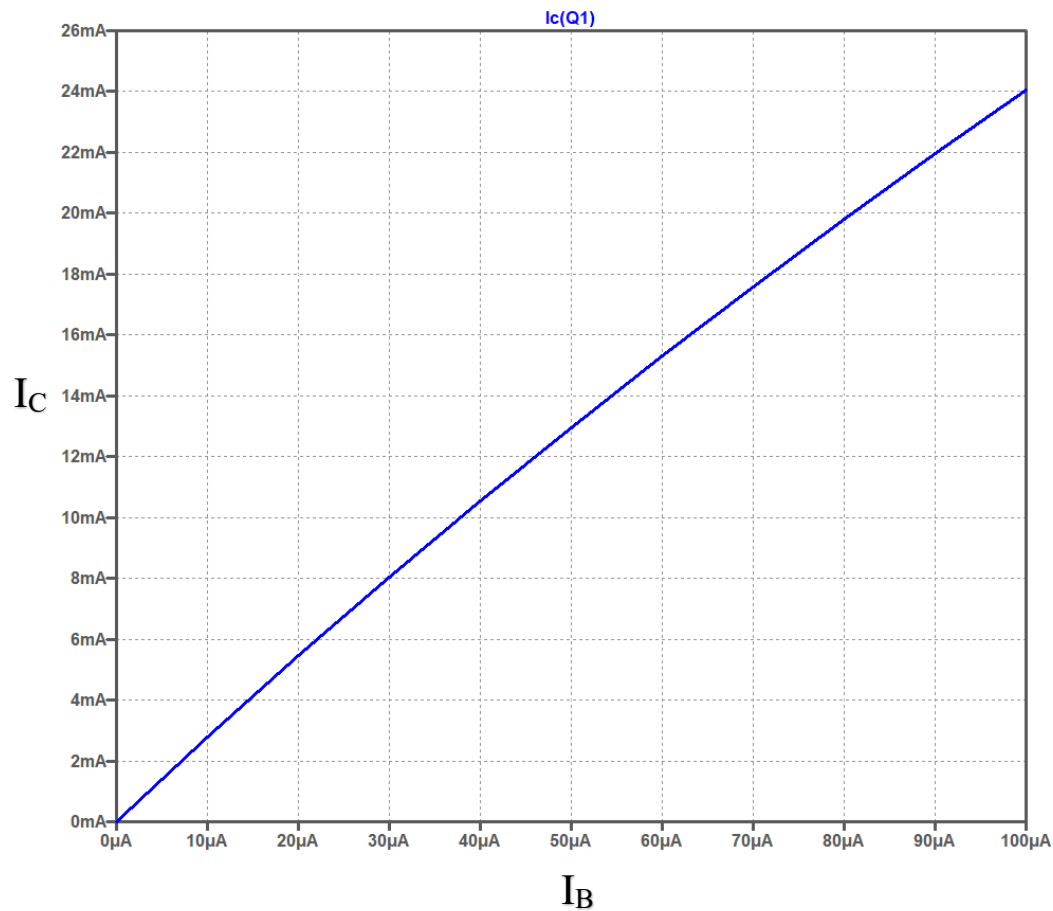


$I_C$  at  $I_B = 50\mu\text{A}$ : 5.34046 mA

$I_C$  at  $I_B = 60\mu\text{A}$ : 6.21322 mA

$$\beta = \frac{\Delta I_C}{\Delta I_B} = 87.276$$

$I_C$  vs  $I_B$  at  $V_{CE} = 600\text{mV}$  (Forward Active):



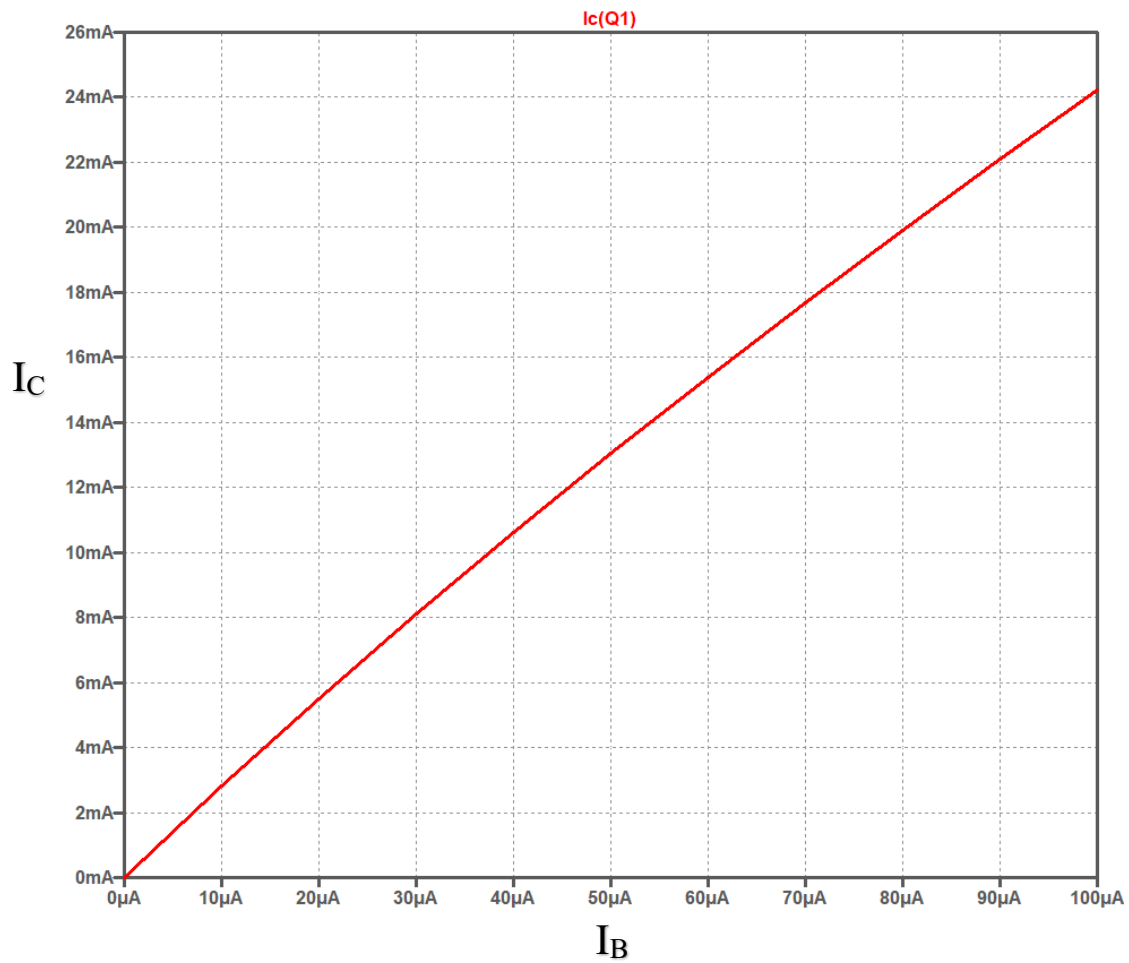
$I_C$  at  $I_B = 50\mu\text{A}$ : 12.9573 mA

$I_C$  at  $I_B = 60\mu\text{A}$ : 15.2992 mA

$$\beta = \frac{\Delta I_C}{\Delta I_B} = 234.19$$

Clearly,  $\beta$  during Forward Active is much higher than during Saturation. This is because the collector current decreases a lot in Saturation mode, due to the Collector Base Junction being in forward bias conducting in the opposite direction.

$I_C$  vs  $I_B$  at  $V_{CE} = 1V$ :

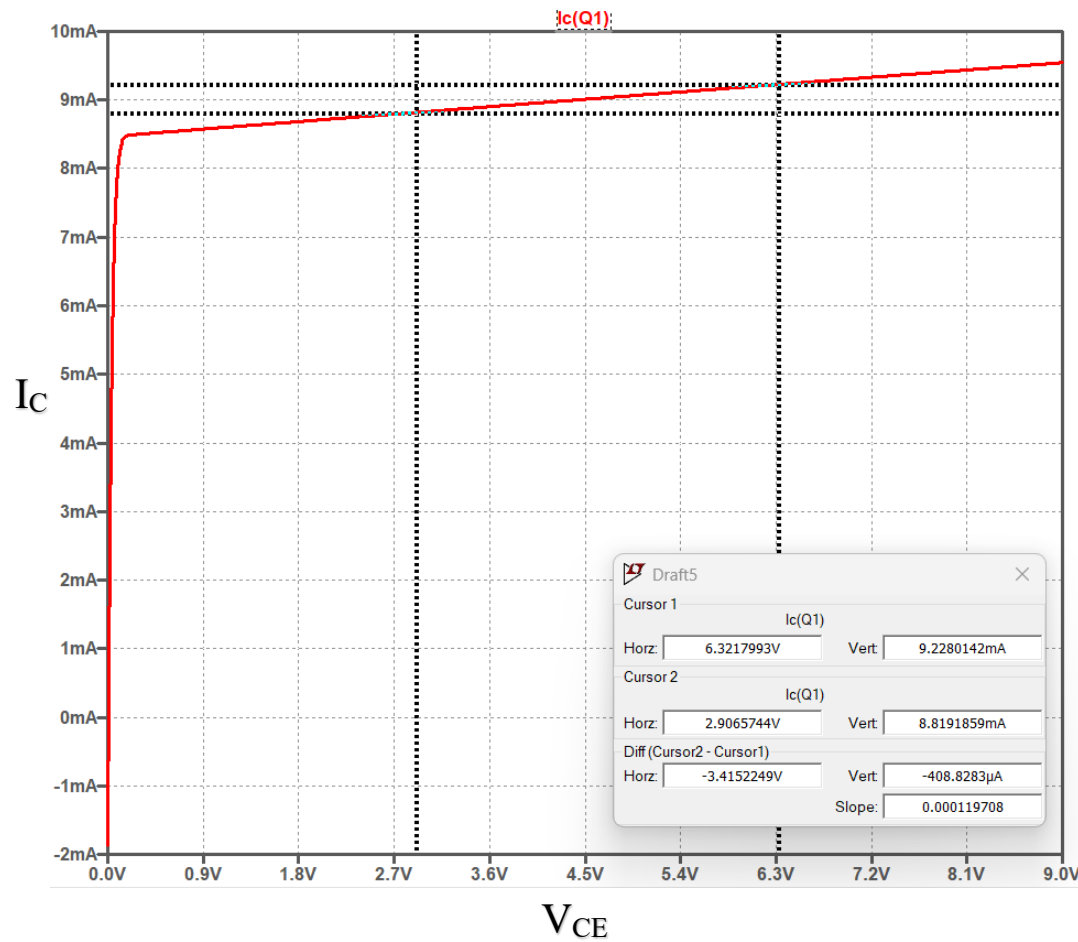


$I_B$ (uA)	$I_C$ (mA)	$\beta$
10	2.80528	280.528
20	5.5026	275.13
30	8.10143	270.0477
40	10.6117	265.2925
50	13.0418	260.836
60	15.3989	256.6483
70	17.6893	252.7043
80	19.9181	248.9763
90	22.0902	245.4467
100	24.2097	242.097

We see a gradual decrease in the value of  $\beta$ , with increasing  $I_C$ . This shows the presence of Early effect, since an increase in  $I_B$  will cause the base width to decrease, (due to increase in  $V_B$ ), which leads to an increase in  $I_C$ , with respect to the expected value, i.e., Early effect.



To find Early voltage, we need the plot of  $I_C$  vs  $V_{CE}$  at constant  $V_{BE}$ ,



Slope of the Graph = 0.000119708 A/V,

Take  $V_{CE}$  as the average of the 2 points measured, i.e., 4.6141867 V.

$I_C$  at the edge of Forward Active Mode = 8.442923mA

We know that,

$$Slope = \frac{I_C}{V_A + V_{CE}}$$

$$\Rightarrow V_A = \frac{I_C}{Slope} - V_{CE}$$

$$\Rightarrow V_A = 65.91512 \text{ V}$$

Therefore, the Early voltage is estimated to be 65.91512 V.

