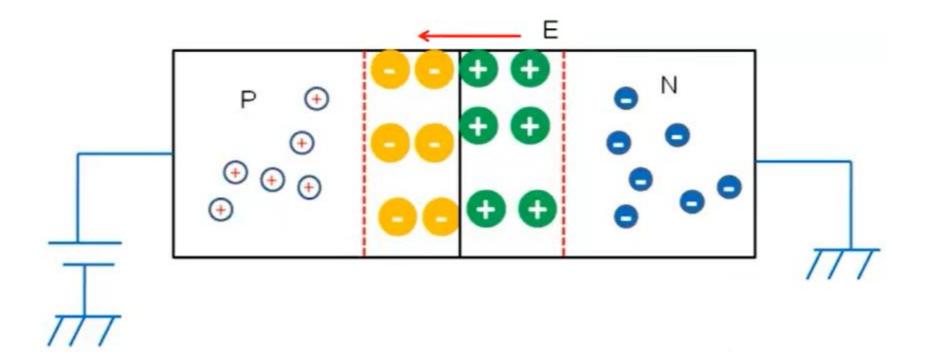
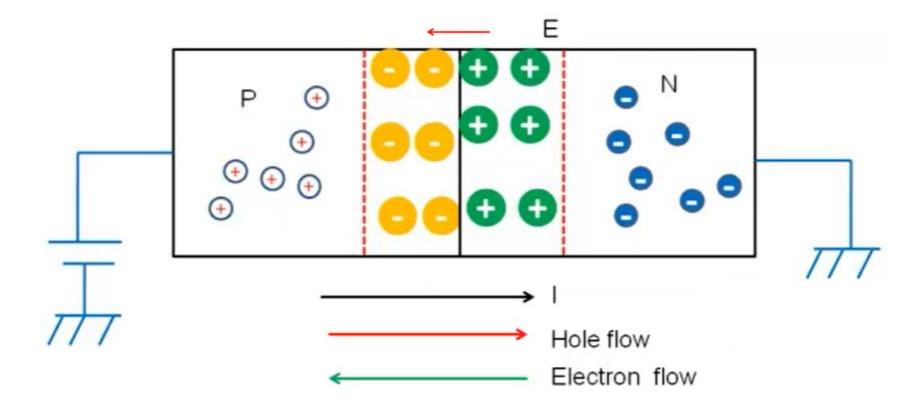
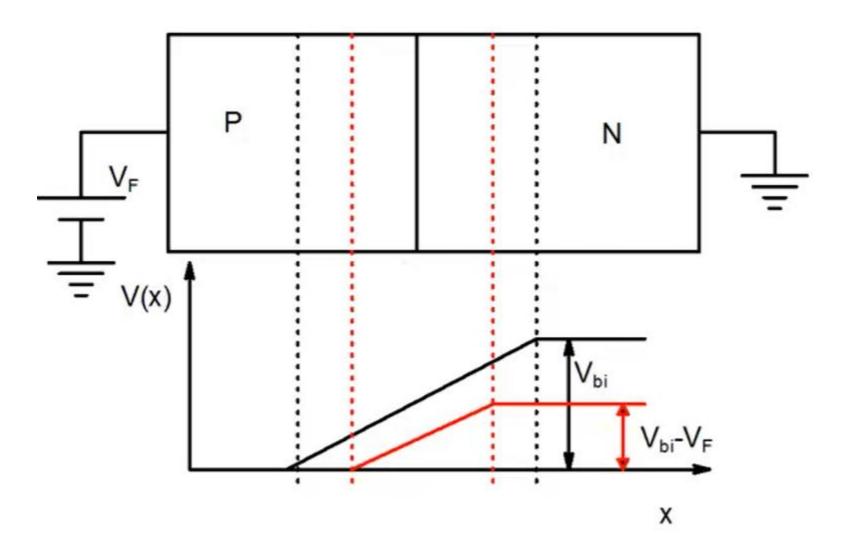
Forward Bias



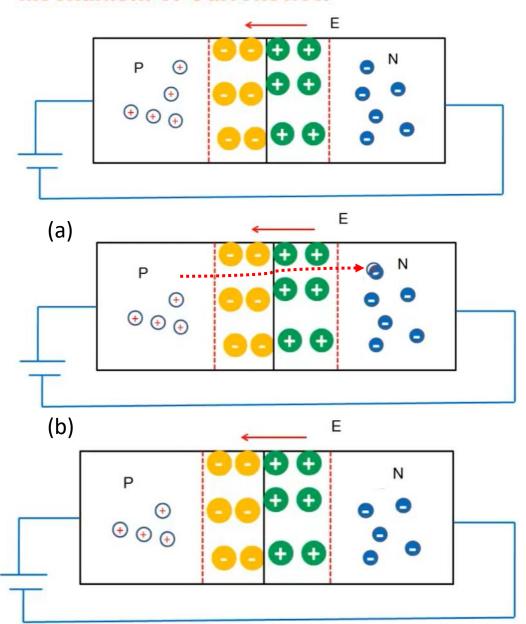
Forward Bias

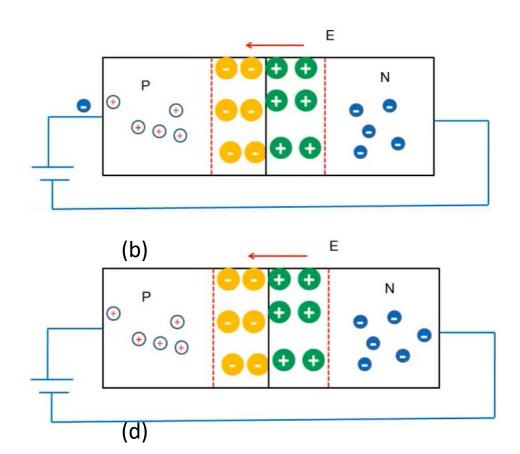


Application of forward bias lowers the built-in potential and allows holes and electrons to cross the junction and result in current flow

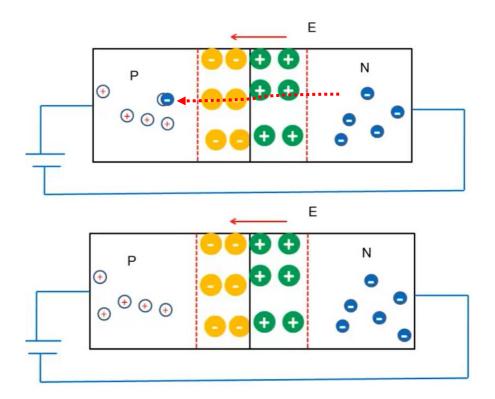


Mechanism of Current flow



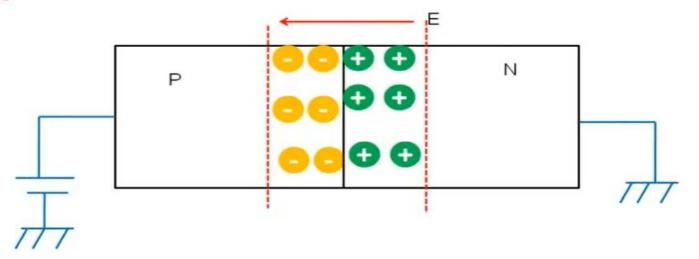


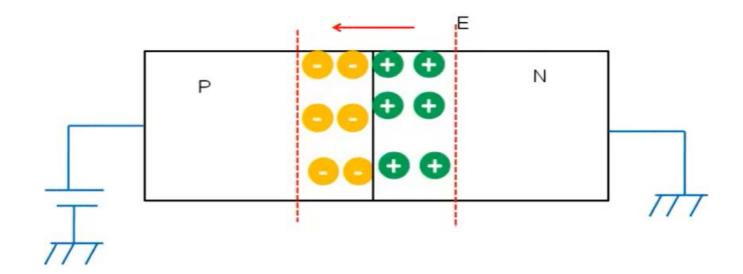
Mechanism of Current flow



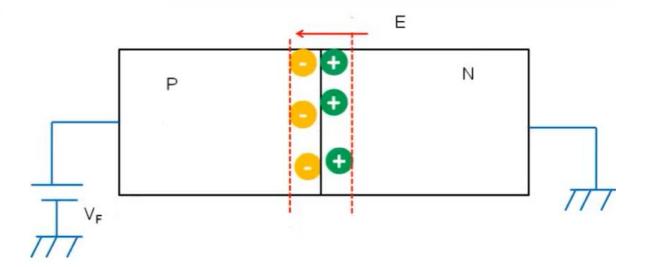
Current flows through carrier injection and recombination

Forward Bias





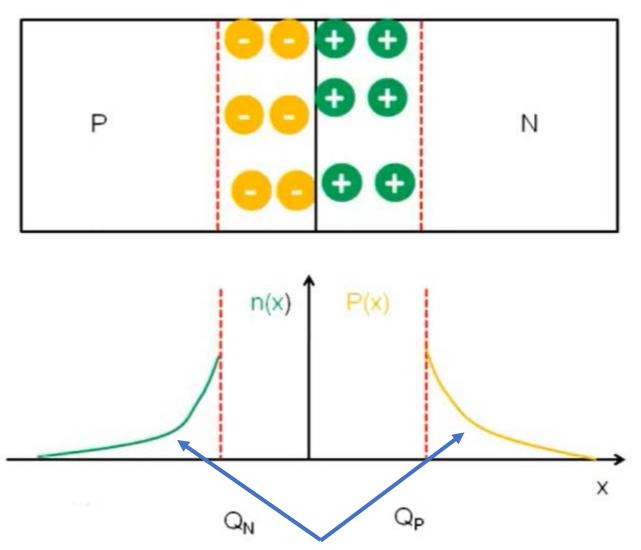
Forward Bias



Application of forward bias reduces depletion width.

Change in depletion charge with voltage gives rise to junction capacitance (also called depletion capacitance) C_J

Excess carriers in P and N regions

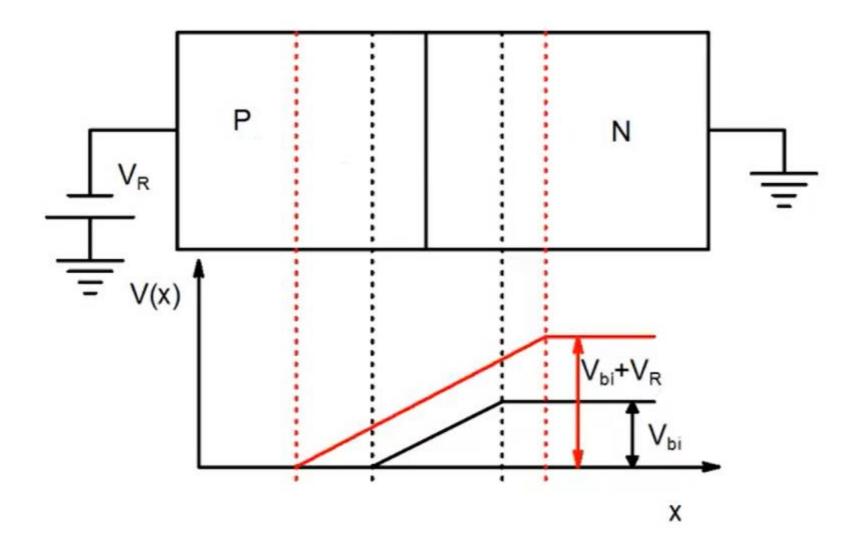


Reverse Bias ⊕ ⊕ ⊕ ⊕ Hole flow Electron flow Ε Because of very few electrons in p-type and holes in N-type current is

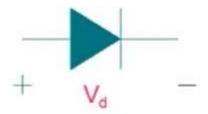
very small!

Application of reverse bias increases the built-in potential

Reverse Bias



Diode: I-V Characteristics



$$I = I_s \times \{ \exp(\frac{V_D}{nV_T}) - 1 \}$$

 $I_s : \text{Reverse Saturation Current}$
 $V_T = kT/q \cong 26mV \text{ at T} = 300 \text{ K}$

n is called ideality factor and is equal to 1 for ideal diodes

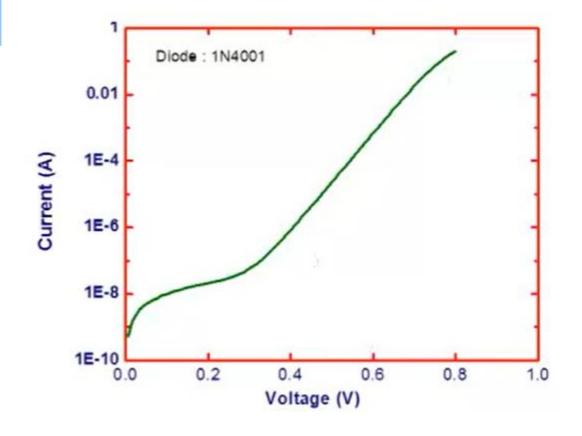
Forward Bias

$$I = I_s \times \{ e \times p(\frac{v_d}{V_T}) - 1 \}$$

$$v_d >> V_T = 26 m V$$

$$\ln(I) = \ln(I_s) + \frac{v_d}{V_T}$$

$$I \cong I_s \times exp(\frac{v_d}{V_T})$$

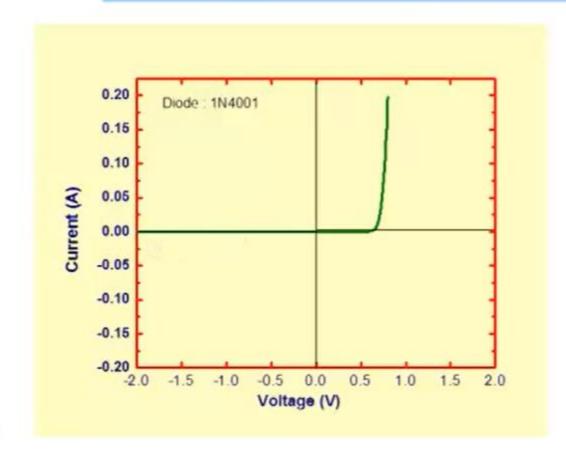


Reverse Bias

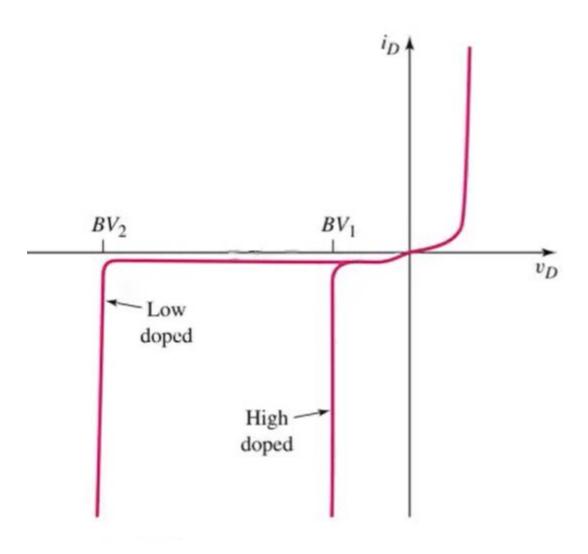
$$I = I_S \times \{ e \times p(\frac{v_d}{V_T}) - 1 \}$$

$$v_d = -v_R$$

$$I = I_s \times \{ exp(-\frac{v_R}{V_T}) - 1 \} \cong -I_s$$

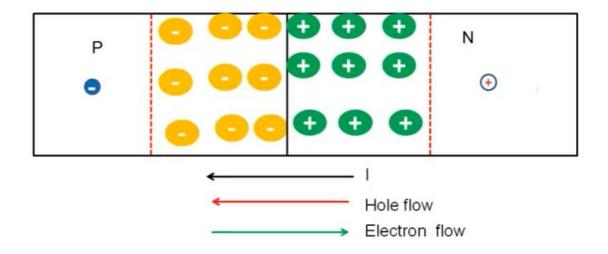


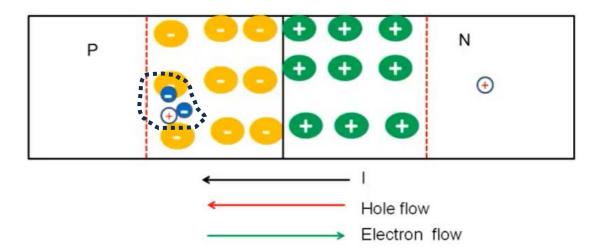
Breakdown

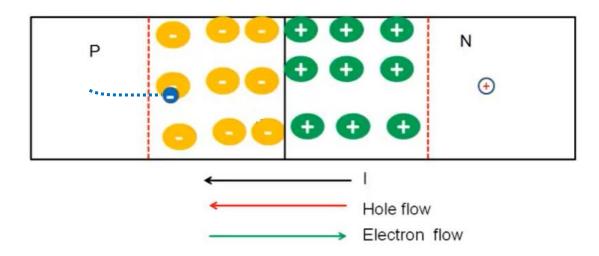


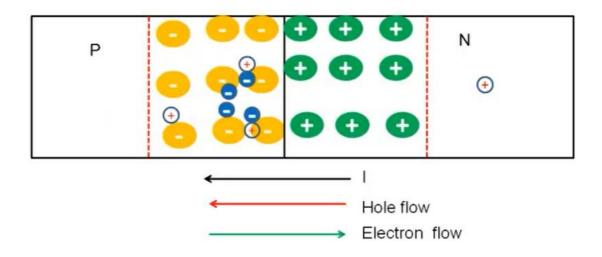
Impact Ionization ••• (Si) •••• (Si) 🕨 Si 🕶 Si 🕶 Si 🚥 Si 🛑 Si Si P P Si • 🕟 (Si) 📀 (Si) ••• (P) ••• (Si) ••• Electrons shared by neighbouring Phosphorus Electrons shared by neighbouring atom Phosphorus atom (Si) (Si) Si ••• (Si) 🕨 (S) 🕶 (S) 🚥 (S) 🛑 (Si) Si ••• Si ••• P nosphorus Electrons shared by neighbouring Si (Si) .

Breakdown Mechanism: Avalanche

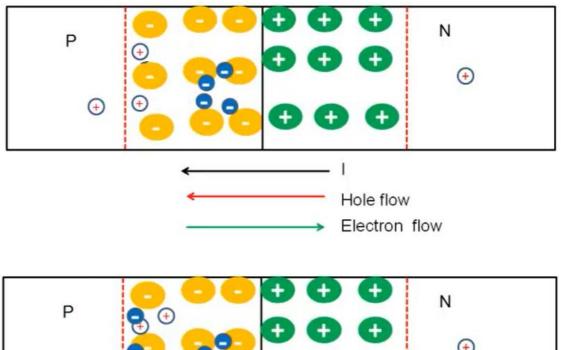


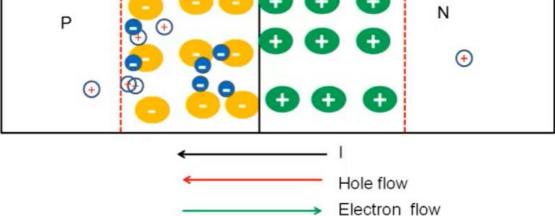






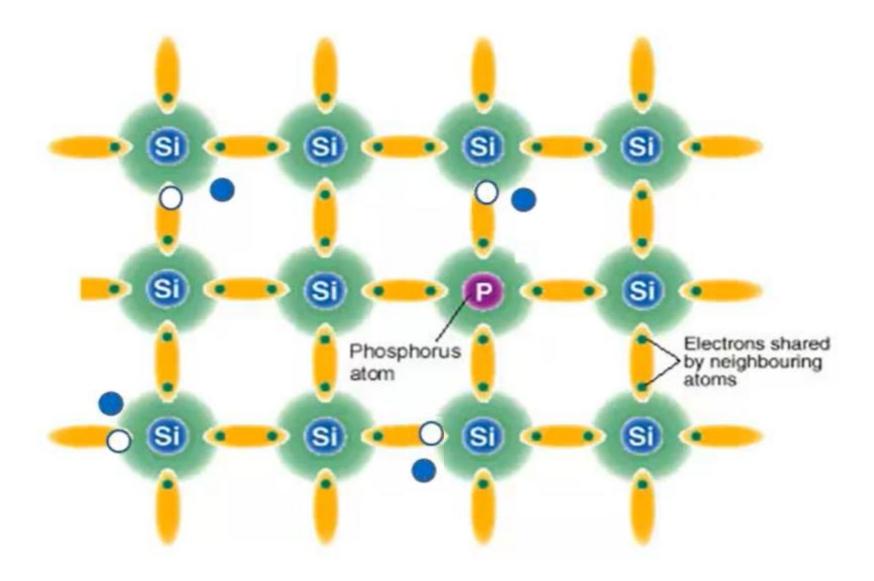
Breakdown Mechanism: Avalanche



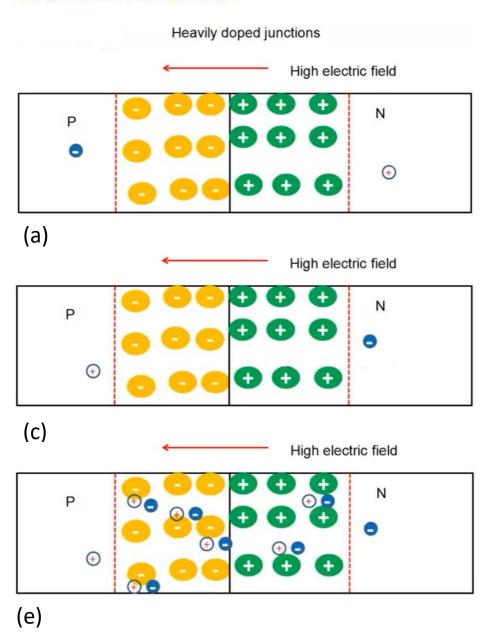


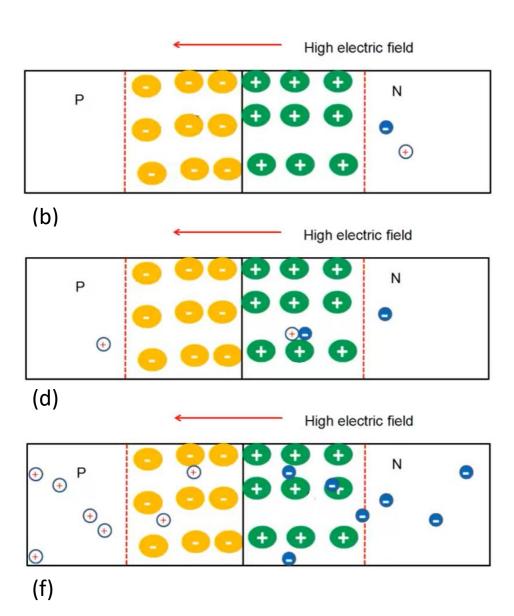
A multiplication of carriers takes place due to impact ionization in the depletion region, as a result of which large current begins to flow.

Ionization can also occur due to electric field



Breakdown: Zener

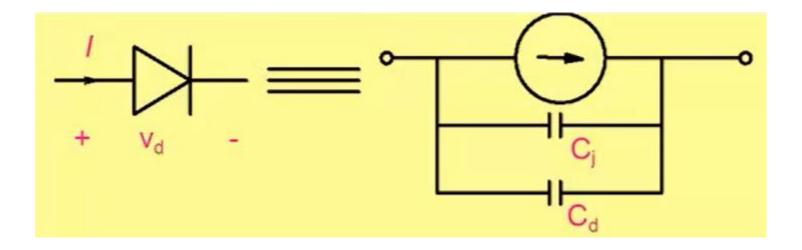




Electric field "**breaks the bond**" creating electron and hole pairs. These carriers move to give rise to large current.

Most of the common pn junction diodes breakdown due to avalanche mechanism. Even diodes which are called zener diodes have avalanche breakdown

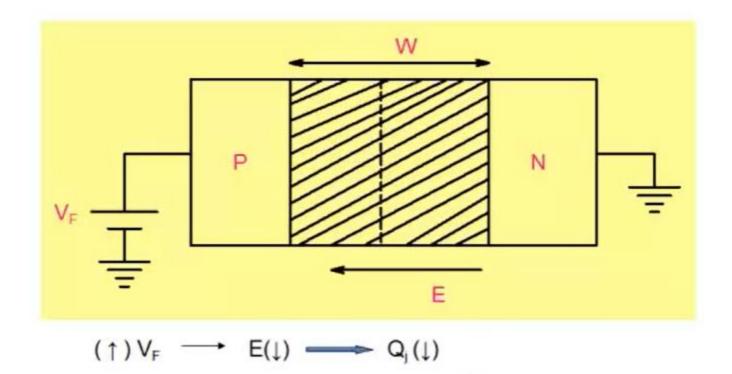
Diode Model: Forward Bias



$$I = I_S \times \{ e \times p \left(\frac{V_d}{n V_T} \right) - 1 \}$$

For dc and low frequency ac circuits, the effect of diode capacitance can be neglected

Junction Capacitance



$$C_j = -\frac{\partial Q_j}{\partial V_F}$$

$$C_j = \frac{\varepsilon_s}{W} \times Area$$

$$C_{j} = \frac{C_{j0}}{\left(1 - \frac{V_{d}}{V_{bi}}\right)^{m}}$$

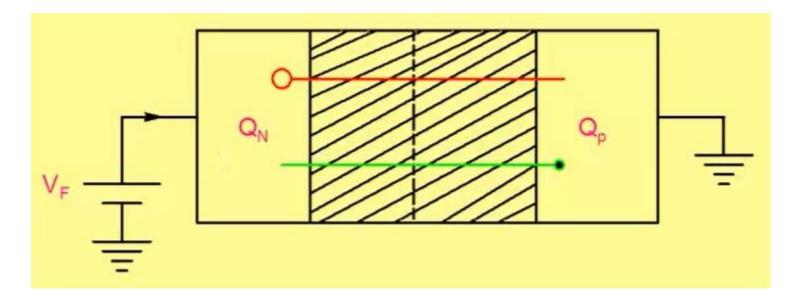
Cio: Zero bias junction Capacitance

V_{bi}: built-in potential

m: grading coefficient (~ 0.5, 0.33)

- Junction capacitance increases with forward bias
- Junction capacitance increases with doping
- Junction capacitance increases with Area

Diffusion Capacitance

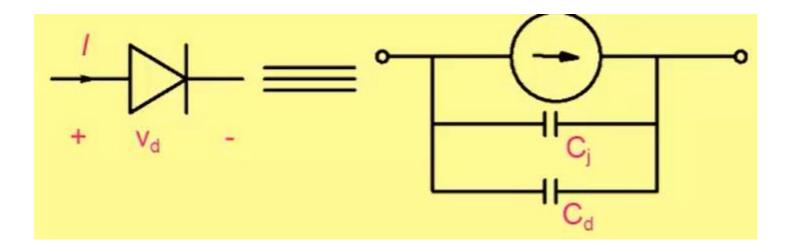


$$C_{d} = \frac{\partial (Q_{P} + |Q_{N}|)}{\partial V_{F}}$$

$$C_{d} = \frac{I_{F}}{V_{T}} \cdot \tau$$

τ: transit time

Diode Model: Forward Bias



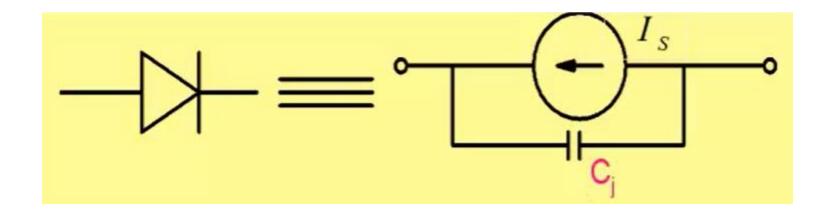
$$I = I_s \times \{ e \times p(\frac{V_d}{nV_T}) - 1 \}$$

$$C_{j} = \frac{C_{j0}}{\left(1 - \frac{V_{d}}{V_{bi}}\right)^{m}}$$

$$C_d = \frac{I_F}{V_T} \cdot \tau$$

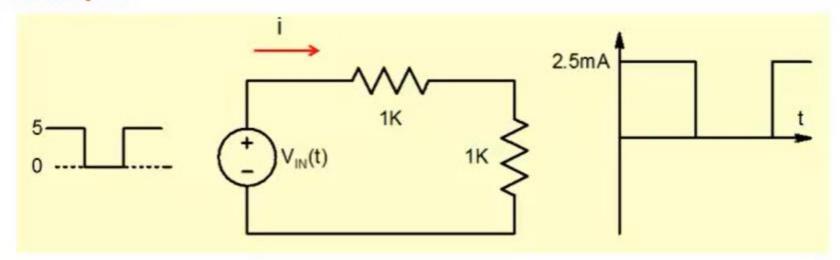
No. of parameters: 3+3+1

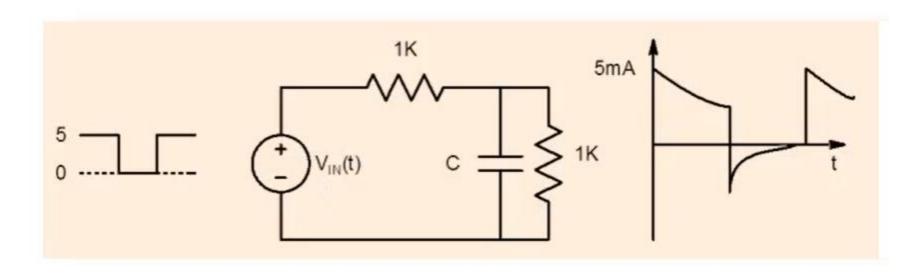
Diode Model: Reverse Bias

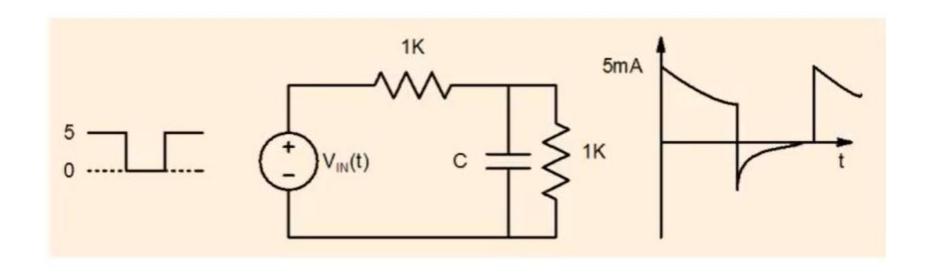


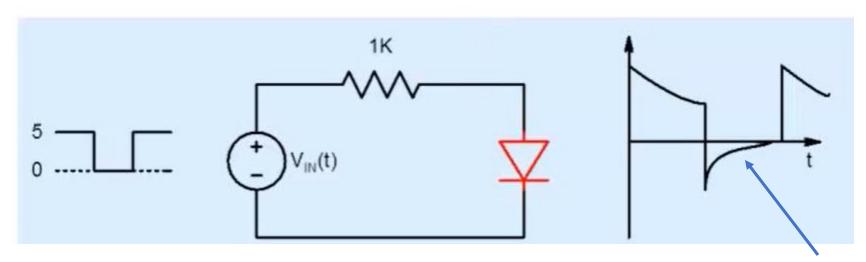
Because of capacitance, a diode even though reverse biased, can carry significant current momentarily

Example



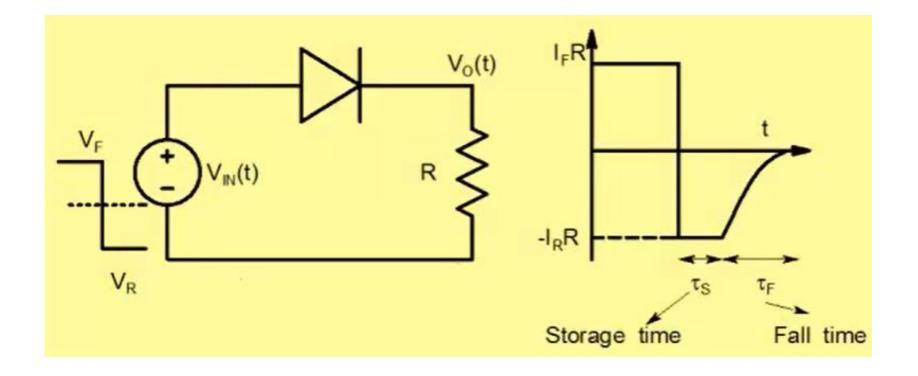






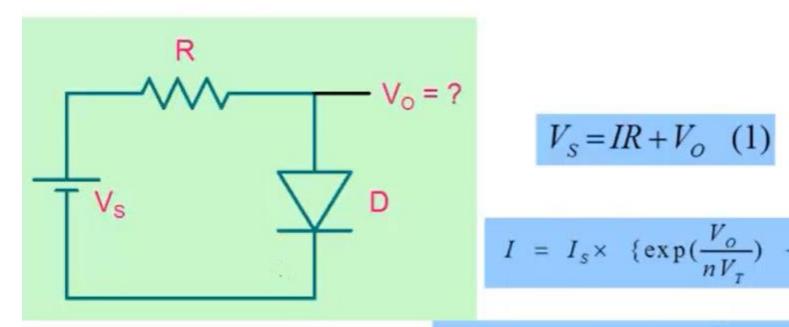
Current in reverse direction

Transient Response



Diode does not switch off instantly but remains conducting for a period called reverse recovery time which is sum of storage and fall delay times..

Analysis using non-linear diode model is not easy



$$V_{S} = IR + V_{O} \quad (1)$$

$$I = I_s \times \left\{ \exp\left(\frac{V_o}{nV_T}\right) - 1 \right\} (2)$$

$$\Rightarrow V_O = nV_T \times \ln(\frac{I}{I_s} + 1) \quad (3)$$

$$\Rightarrow V_S = IR + nV_T \times \ln(\frac{I}{I_S} + 1) \quad (4)$$

Iterative Method:

$$V_{S} = IR + V_{O} \quad (1)$$

$$I = I_s \times \left\{ \exp\left(\frac{V_o}{nV_T}\right) - 1 \right\} (2)$$

Assume

$$V_{o} = 0.6 \text{V}$$

Calculate

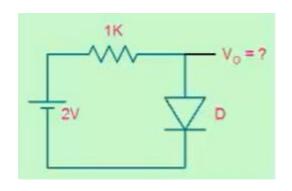
$$I = \frac{V_s - V_o}{R}$$

Re-calculate

$$V_O = nV_T \times \ln(I/I_S + 1)$$

Convergence:

$$\frac{\Delta I}{I} \le \varepsilon$$



$$I = I_S \times \{ \exp(\frac{V}{V_T}) - 1 \}$$

$$I_S = 2 \times 10^{-15} A$$

$$V_T = kT / q \cong 26 \text{ mV} \text{ at T} = 300 \text{K}$$

Assume
$$V_o$$

$$I = \frac{V_{S} - V_{O}}{R}$$

$$V_o = nV_T \times \ln(I/I_s + 1)$$

$$V_0 = 0.5$$

$$V_0 = 0.711$$

$$I = 1.289 \times 10^{-3}$$

$$V_0 = 0.711$$

 $I = 1.5 \times 10^{-3}$

$$V_0 = 0.707$$

$$V_0 = 0.707$$

 $V_0 = 0.707$

 $I = 1.293 \times 10^{-3}$

CONVERGENCE

$$I = I_S \times \{ \exp(\frac{V}{V_T}) - 1 \}$$

$$I_S = 2 \times 10^{-15} A$$

$$V_T = kT / q \cong 26 \text{ mV} \text{ at T} = 300 \text{K}$$

Assume
$$V_o$$

$$V_0 = 1.0$$

$$V_0 = 0.7$$

$$V_0 = 0.707$$

$$I = \frac{V_{S} - V_{O}}{R}$$

$$I = 1.0 \times 10^{-3}$$
 $I = 1.3 \times 10^{-3}$

$$I = 1.3 \times 10^{-3}$$

$$I = 1.293 \times 10^{-3}$$

$$V_O = nV_T \times \ln(I/I_s + 1)$$

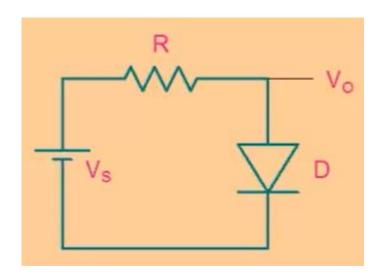
$$V_0 = 0.7$$

$$V_0 = 0.707$$
 $V_0 = 0.707$

$$V_0 = 0.707$$

CONVERGENCE to the same Result

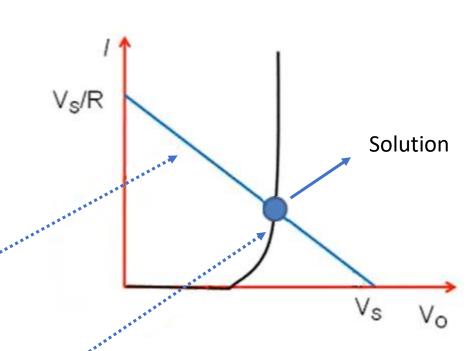
Graphical Method: Method of Load Line

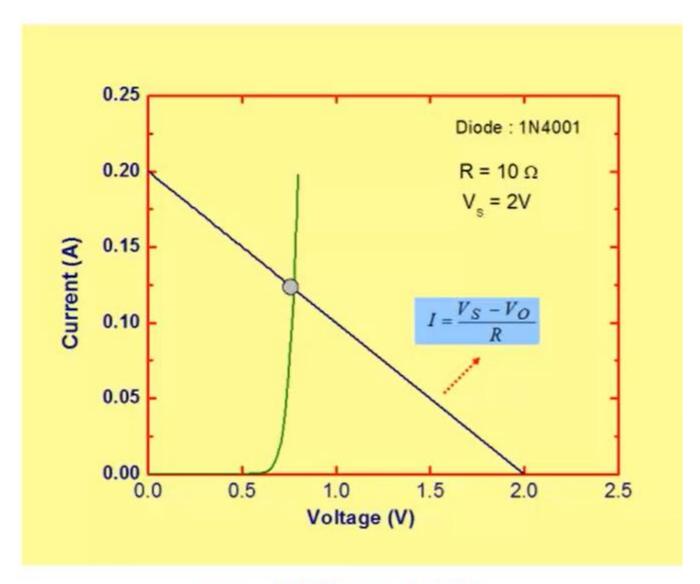


$$V_s = I \times R + V_o$$

$$I = \frac{V_s - V_o}{R}$$

$$I = I_S \times \left\{ \exp\left(\frac{V_O}{nV_T}\right) - 1 \right\}$$





 $V_0 = 0.77V$; I = 0.12A