

EXTENDS *Naturals, Sequences, FiniteSets, TLC*

CONSTANTS *N, MaxClock*

Pid $\triangleq 1 \dots N$

ClockVal $\triangleq 0 \dots \text{MaxClock} + 1$

Message $\triangleq [time : \text{ClockVal}, type : \{ \text{"Request"}, \text{"Release"}, \text{"AckReq"} \}]$

--algorithm *LogicalClocks*

variables

channel = $[source \in \text{Pid} \mapsto [destination \in \text{Pid} \mapsto \langle \rangle]]$,

crit = $\{\}$

define

LogClockLt(*reqs*, *p*, *q*) \triangleq

$\vee reqs[q] = 0$

$\vee reqs[p] < reqs[q]$

$\vee reqs[p] = reqs[q] \wedge p < q$

ChanHead(*dst*, *type*) \triangleq

$\{src \in \text{Pid} : \wedge Len(channel[src][dst]) > 0$

$\wedge Head(channel[src][dst]).type = type$

$\}$

Max(*a*, *b*) \triangleq IF *a* \leq *b* THEN *b* ELSE *a*

end define

macro *Receive*(*type*, *clock*, *src*, *time*)**begin**

with *s* \in *ChanHead*(*self*, *type*) **do**

src := *s* ;

time := *Head*(*channel*[*src*][*self*]).*time* ;

channel := [*channel* EXCEPT ![*src*][*self*] = *Tail*(*channel*[*src*][*self*])]

end with

end macro

macro *Broadcast*(*clock*, *msg*)**begin**

channel :=

[*channel* EXCEPT ![*self*] =

[*dst* \in *Pid* \mapsto

IF *dst* = *self* THEN *channel*[*self*][*self*]

ELSE *Append*(*channel*[*self*][*dst*], *msg*)]

end macro

macro *SendTo*(*clock*, *dst*, *msg*)**begin**

channel :=

[*channel* EXCEPT ![*self*][*dst*] = *Append*(*channel*[*self*][*dst*], *msg*)]

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end macro

macro EnterCritSec()begin
  crit := crit  $\cup$  {self}
end macro

macro ExitCritSec()begin
  crit := crit  $\setminus$  {self}
end macro

process Proc  $\in$  Pid
variables
  clock = 1,
  acks = {},
  requests = [pid  $\in$  Pid  $\mapsto$  0],
  time,
  src
begin
  loop: while TRUE do
    either
      when requests[self] = 0 ;
        Broadcast(clock, [time  $\mapsto$  clock, type  $\mapsto$  "Request"]);
        requests := [requests EXCEPT ![self] = clock];
        acks := {self}
      or
        Receive("AckReq", clock, src, time);
        clock := Max(clock, time);
        acks := acks  $\cup$  {src}
      or
        when  $\wedge$  self  $\notin$  crit
           $\wedge$  acks = Pid
           $\wedge \forall p \in$  Pid : p  $\neq$  self  $\Rightarrow$ 
            LogClockLt(requests, self, p);
            EnterCritSec();
        or
          when self  $\in$  crit ;
            requests := [requests EXCEPT ![self] = 0];
            ExitCritSec();
            acks := {};
            Broadcast(clock, [time  $\mapsto$  clock, type  $\mapsto$  "Release"])
        or
          Receive("Request", clock, src, time);
          requests := [requests EXCEPT ![src] = time];
          clock := Max(clock, time);
          L2: SendTo(clock, src, [time  $\mapsto$  clock + 1, type  $\mapsto$  "AckReq"])
    end either
  end while
end process

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or
  Receive("Release", clock, src, time);
  clock := Max(clock, time);
  requests := [requests EXCEPT ![src] = 0];
end either ;
tic: clock := clock + 1
end while ;
end process

end algorithm

BEGIN TRANSLATION (chksum(pcal) = "eb5ff142" ∧ chksum(tla) = "bc5fba51")
CONSTANT defaultInitValue
VARIABLES channel, crit, pc

define statement
LogClockLt(reqs, p, q)  $\triangleq$ 
  ∨ reqs[q] = 0
  ∨ reqs[p] < reqs[q]
  ∨ reqs[p] = reqs[q] ∧ p < q

ChanHead(dst, type)  $\triangleq$ 
  {src ∈ Pid : ∧ Len(channel[src][dst]) > 0
    ∧ Head(channel[src][dst]).type = type
  }

Max(a, b)  $\triangleq$  IF a ≤ b THEN b ELSE a

VARIABLES clock, acks, requests, time, src

vars  $\triangleq$  ⟨channel, crit, pc, clock, acks, requests, time, src⟩

ProcSet  $\triangleq$  (Pid)

Init  $\triangleq$  Global variables
  ∧ channel = [source ∈ Pid ↦ [destination ∈ Pid ↦ ⟨⟩]]
  ∧ crit = {}
  Process Proc
    ∧ clock = [self ∈ Pid ↦ 1]
    ∧ acks = [self ∈ Pid ↦ {}]
    ∧ requests = [self ∈ Pid ↦ [pid ∈ Pid ↦ 0]]
    ∧ time = [self ∈ Pid ↦ defaultInitValue]
    ∧ src = [self ∈ Pid ↦ defaultInitValue]
    ∧ pc = [self ∈ ProcSet ↦ "loop"]

loop(self)  $\triangleq$  ∧ pc[self] = "loop"
  ∧ ∨ ∧ requests[self][self] = 0
  ∧ channel' = [channel EXCEPT ![self] =
    [dst ∈ Pid ↦

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$$\begin{aligned}
& \text{IF } dst = self \text{ THEN } channel[self][self] \\
& \quad \text{ELSE } Append(channel[self][dst], ([time \mapsto clock[self], type \mapsto \dots]) \\
& \wedge requests' = [requests \text{ EXCEPT } ![self] = [requests[self] \text{ EXCEPT } ![self] = clock[self]]] \\
& \wedge acks' = [acks \text{ EXCEPT } ![self] = \{self\}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"tic"}] \\
& \wedge \text{UNCHANGED } \langle crit, clock, time, src \rangle \\
\vee \wedge \exists s \in ChanHead(self, \text{"AckReq"}) : \\
& \quad \wedge src' = [src \text{ EXCEPT } ![self] = s] \\
& \quad \wedge time' = [time \text{ EXCEPT } ![self] = Head(channel[src'[self]][self]).time] \\
& \quad \wedge channel' = [channel \text{ EXCEPT } ![src'[self]][self] = Tail(channel[src'[self]][self])] \\
& \wedge clock' = [clock \text{ EXCEPT } ![self] = Max(clock[self], time'[self])] \\
& \wedge acks' = [acks \text{ EXCEPT } ![self] = acks[self] \cup \{src'[self]\}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"tic"}] \\
& \wedge \text{UNCHANGED } \langle crit, requests \rangle \\
\vee \wedge \wedge self \notin crit \\
& \quad \wedge acks[self] = Pid \\
& \quad \wedge \forall p \in Pid : p \neq self \Rightarrow \\
& \quad \quad LogClockLt(requests[self], self, p) \\
& \quad \wedge crit' = (crit \cup \{self\}) \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"tic"}] \\
& \quad \wedge \text{UNCHANGED } \langle channel, clock, acks, requests, time, src \rangle \\
\vee \wedge self \in crit \\
& \quad \wedge requests' = [requests \text{ EXCEPT } ![self] = [requests[self] \text{ EXCEPT } ![self] = 0]] \\
& \quad \wedge crit' = crit \setminus \{self\} \\
& \quad \wedge acks' = [acks \text{ EXCEPT } ![self] = \{\}] \\
& \quad \wedge channel' = [channel \text{ EXCEPT } ![self] = \\
& \quad \quad [dst \in Pid \mapsto \\
& \quad \quad \quad \text{IF } dst = self \text{ THEN } channel[self][self] \\
& \quad \quad \quad \text{ELSE } Append(channel[self][dst], ([time \mapsto clock[self], type \mapsto \dots]) \\
& \quad \quad \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"tic"}] \\
& \quad \quad \wedge \text{UNCHANGED } \langle clock, time, src \rangle \\
& \quad \vee \wedge \exists s \in ChanHead(self, \text{"Request"}) : \\
& \quad \quad \wedge src' = [src \text{ EXCEPT } ![self] = s] \\
& \quad \quad \wedge time' = [time \text{ EXCEPT } ![self] = Head(channel[src'[self]][self]).time] \\
& \quad \quad \wedge channel' = [channel \text{ EXCEPT } ![src'[self]][self] = Tail(channel[src'[self]][self])] \\
& \quad \wedge requests' = [requests \text{ EXCEPT } ![self] = [requests[self] \text{ EXCEPT } ![src'[self]] = time'[self]]] \\
& \quad \wedge clock' = [clock \text{ EXCEPT } ![self] = Max(clock[self], time'[self])] \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"L2"}] \\
& \quad \wedge \text{UNCHANGED } \langle crit, acks \rangle \\
& \quad \vee \wedge \exists s \in ChanHead(self, \text{"Release"}) : \\
& \quad \quad \wedge src' = [src \text{ EXCEPT } ![self] = s] \\
& \quad \quad \wedge time' = [time \text{ EXCEPT } ![self] = Head(channel[src'[self]][self]).time] \\
& \quad \quad \wedge channel' = [channel \text{ EXCEPT } ![src'[self]][self] = Tail(channel[src'[self]][self])] \\
& \quad \wedge clock' = [clock \text{ EXCEPT } ![self] = Max(clock[self], time'[self])] \\
& \quad \wedge requests' = [requests \text{ EXCEPT } ![self] = [requests[self] \text{ EXCEPT } ![src'[self]] = 0]]
\end{aligned}$$

$$\begin{aligned}
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"tic"}] \\
& \wedge \text{UNCHANGED } \langle crit, acks \rangle \\
tic(self) & \triangleq \wedge pc[self] = \text{"tic"} \\
& \wedge clock' = [clock \text{ EXCEPT } ![self] = clock[self] + 1] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"loop"}] \\
& \wedge \text{UNCHANGED } \langle channel, crit, acks, requests, time, src \rangle \\
L2(self) & \triangleq \wedge pc[self] = \text{"L2"} \\
& \wedge channel' = [channel \text{ EXCEPT } ![self][src[self]] = Append(channel[self][src[self]], ([time \mapsto clock \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"tic"}] \\
& \wedge \text{UNCHANGED } \langle crit, clock, acks, requests, time, src \rangle \\
Proc(self) & \triangleq loop(self) \vee tic(self) \vee L2(self) \\
Next & \triangleq (\exists self \in Pid : Proc(self)) \\
Spec & \triangleq Init \wedge \Box [Next]_{vars} \\
& \text{END TRANSLATION} \\
View & \triangleq \langle channel, crit, clock, acks, requests, pc \rangle \\
MutualExclusion & \triangleq Cardinality(crit) < 2
\end{aligned}$$
