## Sriharsha Annamaneni

EDUCATION Florida Institute of Technology, Melbourne, FL GPA: 3.7/4.0

Master of Science, Electrical Engineering 2016

Manipal Institute of Technology, Manipal, India GPA: 6.9/10

Bachelor of Engineering, Electronics and Communication Engineering, 2014

INDUSTRIAL EXPERIENCE

Research Engineer, Sirena Technologies, Bangalore Nov 2019 - Present

• Wake up word detection, Built an offline trigger word detector using Time Delay

- Neural Networks

   Face Recognition, Built Deep Neural Network for recognizing facial images cap-
- Face Recognition, Built Deep Neural Network for recognizing facial images captured by a camera, compared it with the images in the database and retrieve information of the detected person.
- Automatic Speech Recognition, Building a Robust ASR model for Indian English using existing ASR architecture Deepspeech2

RESEARCH EXPERIENCE Research Fellow, IIIT Hyderabad

Nov 2017 - May 2019

With Prof. C.V. Jawahar and Dr. Girish Varma

- Deep Learning, specifically Model Compression techniques and Semantic Segmentation for Autonomous Navigation on Indian Roads
- Road Audit system design, retrieving the location of defects of the road not only due to regular wear and tear but also because of extreme events like storms over a period of time using video and GPS data

Undergraduate Thesis, BARC, India

Jan 2014 - Jun 2014

with Dr. Siddhartha Mukhopadhyay and Dr. Debmalya Mukherjee

- Compression of Magnetic Flux Leakage Signals Data Collected by Instrumented Pipeline Inspection Gauge.
- The algorithm involves Principal Component Analysis and Wavelets

PUBLICATIONS

[1] Efficient Semantic Segmentation using Gradual Grouping

Nikitha Vallurapalli\*, **Sriharsha Annamaneni**\*, Girish Varma\*, CV Jawahar\*, Manu Mathew, Soyeb Nagori , eprint arXiv:1806.08522

CVPR Workshop, 2018(oral), Best Runner-up Award

[2] Development of antenna deployment circuit for nano-satellites

Pramath Keny\*, Arya Menon\*, Madhura Rao\*, Urvang Gaitonde\*, Animesh Gupta\*,

Annamaneni Sriharsha\*

Station subsystem

European Conference on Circuit Theory and Design (ECCTD), 2013

EXPERIENCE Head of Communication and Ground Parikshit Student Satellite Team

Feb 2012 - Dec 2013 Manipal

 Programmed cc1101 and ADF7021-N Transceivers using MSP430 microcontroller will be used for onboard satellite communication

COMPUTER SKILLS

**Tools**: MATLAB, Python, Pytorch, TensorFlow, Keras, LaTeX, OpenCV, Sci-Kit Learn, Django, PostgreSQL, Jupyter Notebook, numba, aws rekognition, spacy, Pomegranate, Pytorchlightning, Spark

Applications: Vi/Vim, Git, Slurm