Sriharsha Annamaneni
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Education

Florida Institute of Technology, Melbourne, FL Dec 2016
Master of Science in Electrical Engineering, CGPA 3.7

Relevant Coursework

Theory of stochastic signals, Speech Recognition, Computer vision, Linear Optimization, Digital Image Processing, Digital Signal Processing, IC Comp Aided analysis, Nonlinear Optimization, Linear Systems, Digital Communication

Independent Coursework

Visual Perception and the Brain, Robotics: Aerial Robotics, Robotics: Computational Motion Planning, Synapses Neurons and Brains, Image and video Processing, Control of Mobile Robotics, Self-Driving Car Nanotechnology and Nanosensors, Computational Neuroscience, Digital Signal Processing, The Brain and Space

Skills

Proficiency: MATLAB, C++, C, Python, OpenCV **Fluency**: pspice, Julia, TensorFlow, Keras
Familiarity: MiniZinc

Projects

Finding Lane Lines
Developed a Computer Vision Pipeline for detecting Lane lines using Python and OpenCV

German Traffic Sign Classifier
Built a Deep Convolutional Neural Network for classifying the Traffic Sign Dataset using Tensorflow

Advance Lane Finding
Developed a Computer Vision Pipeline for detecting Lane lines using Python and OpenCV

Vehicle Detection and Tracking
Developed a Computer Vision Pipeline for detecting vehicles on road using Python and OpenCV

POD: Discovering Primary Objects in Videos Based on Evolutionary Refinement of Object Recurrence, Background, and Primary Object Models

Video Object Segmentation Aggregation

Experience

Bhabha Atomic Research Centre, Bombay, India Jan 2014-May 2014
Research Intern
Developed three stage compression algorithm for Instrumented Pipeline Inspection Gauge for Oil and Gas Pipelines inspection using MATLAB

Satish Dhawan Space Research Centre, Andhra Pradesh, India June 2013
Research Intern
Created case Study of S Band TTC and communication systems of Indian Space Research organization Telemetry Tracking and command centre network

Parikshit Student Satellite team, Manipal, India Feb 2012-Dec 2013
Head for communication and ground station of student satellite team
Designed Ground station and onboard satellite communication system
Signed Memorandum of Understanding with Indian Space Research Organization for Launching satellite.

Vector Training Institute, Hyderabad, India June 2011- July 2011
Designed Unique ID card design for Personal Data Transactions using smartcard Technology which works as pan card, Voter ID, ATM card

Publications

Co-authored paper "Development of Antenna Deployment Circuit for Nanosatellites" IEEE conference on circuit theory and design Sept 2013, Dresden, Germany