Sriharsha Annamaneni

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EDUCATION

Florida Institute of Technology, Melbourne, FL GPA: 3.7/4.0
Master of Science, Electrical Engineering 2016

Manipal Institute of Technology, Manipal, India GPA: 6.9/10
Bachelor of Engineering, Electronics and Communication Engineering, 2014

RESEARCH EXPERIENCE

IIIT Hyderabad Nov 2017-Present
With Prof. C.V. Jawahar and Dr. Girish Varma
- Working on Deep Learning, Specifically Model Compression techniques and Semantic Segmentation for Autonomous Navigation on Indian Roads

Undergraduate Thesis, BARC, India Jan 2014 - Jun 2014
with Dr. Siddhartha Mukhopadhyay and Debmalya Mukherjee
- Compression of Magnetic Flux Leakage Signals Data Collected by Instrumented Pipeline Inspection Gauge.
- The algorithm involves Principal Component Analysis and Wavelets

PUBLICATIONS

[1] Efficient Semantic Segmentation using Gradual Grouping
Nikitha Vallurapalli*, **Sriharsha Annamaneni***, Girish Varma*, CV Jawahar*, Manu Mathew, Soyeb Nagori , eprint arXiv:1806.08522
CVPR Workshop, 2018(oral), **Best Runner-up Award**

[2] Development of antenna deployment circuit for nano-satellites
Pramath Keny*, Arya Menon*, Madhura Rao*, Urvang Gaitonde*, Animesh Gupta*, **Annamaneni Sriharsha***
European Conference on Circuit Theory and Design (ECCTD), 2013

EXPERIENCE

Head of Communication and Ground Station subsystem Parikshit Student Satellite Team
Feb 2012 - Dec 2013 Manipal
- Programmed cc1101 and ADF7021-N Transceivers using MSP430 microcontroller will be used for onboard satellite communication

COMPUTER SKILLS

Languages: C/C++, MATLAB, Python, Pytorch,TensorFlow, Keras, LaTeX, OpenCV, Sci-Kit Learn
Applications: Vi/Vim, Git, Slurm

WORKSHOP AND SUMMER SCHOOLS

- Volunteer for Summer schools on Computer Vision and Machine Learning held in IIIT Hyderabad 2018
- Attended Neuro Inspired Computational Elements Workshop held in University of California, Berkeley 2016
- Attended Workshop on Brain Circuits, Memory and Computation held in Columbia University, Newyork 2016