SRIJAN PABBI

Phone: +91-9560832363
E-mail: Srijanpabbi@gmail.com
Linkedin: linkedin.com/in/srijanpabbi
Visit: http://srijanpabbi.in (https://srijanpabbi.blogspot.com/)
Docs:https://drive.google.com/open?id=12W1GoIqVjEWfuC9umZYCaEYg6dHFakmV

EDUCATION

University of Delhi, New Delhi (Aug 2015 - July 2019)
Netaji Subhas Institute of Technology (NSIT), CGPA: 8.525
{Upgraded to Netaji Subhas University of Technology, w.e.f. 09/26/2018} Percentage: 77.75%
B.E., Electronics and Communication Engineering

All India Senior School Certificate Examination (Class 12) CBSE April 2015
Physics, Chemistry, Math, Computer Science, English Percentage: 94.6%
BGSIPS, Sector-5, Dwarka, New Delhi

STANDARDIZED TESTS

GRE	323 out of 340	Quant: 168/170, Verbal: 155/170, AWA: 4.5/6.0
TOEFL-IBT	111 out of 120	Reading: 29/30, Listening: 28/30, Speaking: 29/30, Writing: 25/30

RELEVANT COURSES

Computer Engineering: Computer Systems and Organisation, Microprocessors, Operating Systems, Computer Software Engineering, C/C++ Programming-I to V.
Electronics Engineering: Digital Circuits & Systems-I and II, Digital Signal Processing, Circuits and Systems-I (Signals and Systems), Control Engineering, Communication Principles (Probability Theory), Bipolar & MOS Analog Integrated Circuits, Linear Integrated Circuits, Network Analysis.

CERTIFICATES

-> UC San Diego Data Structures and Algorithms Specialization (In Progress) (Course: Algorithmic Toolbox **[CERT])**
-> Xilinx Certified Training Program on "Static Timing Analysis & Embedded Systems Design". **[CERT]**
-> Udemy Machine Learning A-ZTM: Hands-On Python & R In Data Science. **[CERT]**
-> Coding Blocks Certificate of Completion Java Crux Course: Data Structures and Algorithms in Java.**[CERT]**

SKILL SET

Programming: C++, Java, Python, Bash, Assembly, SQL, MATLAB, LATEX.
Design: Embedded on Xilinx SoC (Verilog, VHDL, C++), Digital & Analog Circuits
Tools: Vi/Vim, Eclipse, Visual Studio, Git, Cadence(Capture)
Operating Systems: Linux, Windows.

WORK EXPERIENCE

Research Engineer (July 2019 - Present) **Centre for Development of Telematics (C-DOT), Govt. of India New Delhi, India**

Developing a real-time 200G regenerator system for processing OTU 4 (Optical transport unit) frame overheads.
Tasks: Circuit Design and Writing Software for SoC (Processor + FPGA).

INTERNSHIPS

Software R&D Intern Dec 2018 - Feb 2019 **Mentor Graphics Noida, Uttar Pradesh, India**
(EC416 Practical Training) [CERT] [CREDIT]
Developed a signal & exit handler, annotation infrastructure for software messages and file/data encryption infrastructure in C++ for Symphony, Mentor Graphics' latest High-Performance Mixed-Signal Platform.

Project Trainee, Internship May - July 2018 **Texas Instruments Inc. Bangalore, Karnataka, India**
(EC409 Practical Training) [CERT] [CREDIT]
Theoretical & Experimental Analysis of Class-D (LSR) Output Filtering Circuitry: In Low Power Audio Amplifiers Team of TI on a research project for performance benchmarking of the audio amplifier ICs.

Internship June - July 2016 **Texas Instruments Centre for Embedded Product Design (TI-CEPD), NSIT**
Summer internship program on Microcontroller based Embedded System Design under the guidance of Assoc. Prof. D.V. Gadre at TI-CEPD, NSIT. (cepd.in)

ACADEMIC PROJECTS

Open Source Scientific Instrumentation (EC415 PROJECT): Undergrad B.E. Project. **Feb - May 2019**

Advisor: Assoc. Prof. Dhananjay. V. Gadre

PCR Thermocycler:
1. Nichrome for Heating & Peltier for both Heating & Cooling
2. 500W Halogen Lamp for Heating & DC Fan for Cooling

IOT Wireless Biochemical Reaction Multimeter:
Firebase Data Tracking, Android App
1. pH meter with auto calibration
2. Wireless reaction temperature meter

Mathematical Alarm Clock (EC316 Microprocessor): **[GIT]** **Jan - May 2018**
Designed an 8085 microprocessor based Alarm clock which prompts a mathematical question to shut off the alarm.

Arduinodaya Project Board (EC320 Practical Training): [SCOPE] [SINCGRID STORE] **Aug - Dec 2017**
Advisor: Assoc. Prof. D. V. Gadre.
Designed a project board for education and training purpose, based on Arduino platform with ATMEGA 328P micro-controller having capability of making over 30 standalone projects on it. Now it is being sold by a startup "Sincgrid" as a pedagogy product.

Talkative Tom - Hardware & Embedded Software Implementation: [VIDEO] [DOC] **Aug 2016 - Jul 2017**
Advisor: Assoc. Prof. D. V. Gadre
Design, fabrication and testing of an audio storing & playback device with pitch modification using MSP-430 TI-microcontroller / Atmega328P microcontroller, SPI Serial Flash for Storage & class-D amplifier for speaker

Basic File Compression Software In Python: Using Modified Human Encoding **[GIT]** **Feb 2015**

LEADERSHIP & VOLUNTEER EXPERIENCE, TALKS

Student Contributor **Centre for Electronics Design and Technology (CEDT) NSIT** **June 2016 – May 2019**
CEDT is a "Maker Space/Open Access Lab" at NSIT. Our work here includes making academic projects, pedagogy focused tools, mentor students, conduct workshops and trainings within and outside NSIT. **[LINK]**

General Secretary **Junoon The Photography Club of NSIT, New Delhi, India** **Jan 2016 - May 2019**
My major contribution has been, leading and managing event covering teams every year, including but not limited to Moksha (NSIT's cultural fest), organising online photography competitions, workshops, guiding juniors as per my own experiences, finding sponsors, networking with people and other photographers.

Presenter **IEEE-CAS Bangalore Innovation & DIY Electronics Conference TI, Bangalore, India** **Jun 2018**
Presented Talkative Tom Project. Awarded as one of the 10 best projects. **[CERT]**

Instructor & Mentor **Tinkering Workshop for Army Goodwill Schools at IIT Jammu, India** **Jan 2018**
Workshop for J&K children studying in Army goodwill schools, far out of reach of the modern life in metros, oblivious to the idea of tinkering. Demonstrated simple physics experiments and projects from our lab to stimulate young engineer in them. **[LINK]**

Instructor & Mentor **Arduinodaya Workshop, CEDT NSIT, New Delhi, India** **Jan 2018**
Workshop on Embedded Systems design, electronics design, efficient software development (parallel programming), communication protocols etc. using the pedagogy project board Arduinodaya. **[LINK]**

Instructor & Mentor **TI Internship programme, Chitkara University, Himachal Pradesh, India** **July - Aug 2017**
The program was organised on Chitkara Campus, we moved our whole teaching setup for conducting a month long internship program. I taught TI's MSP430 and Embedded System Concepts to engineering students there.**[CERT] [LINK]**

Instructor & Mentor **TI-CEPD Internship Program NSIT, New Delhi, India** **June - July 2017**
Mentored in the TI's Beagle-bone Black track of the TI-CEPD Summer Internship and helped them implement an individual project based on their learning. **[CERT] [POST]**

INTERESTS AND HOBBIES

Nature, Portrait and Food Photography **[LINK]**: Apart from contributing to Junoon (college photography club), I also go on photography trips including trekking in mountains & visiting bird sanctuaries. I have also done several food photography projects.

Cooking: I keep on exploring new dishes and have perfected my own recipes for Pizza, Lasagne, Cream Tomato Pasta, and many more.