VAIBHAV MAJETY

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CAREER OBJECTIVE
Looking for an entry level job where I can use my skill set for the benefit of company and further enhance my skill set according to corporate needs.

ACADEMIC QUALIFICATIONS

Qualification	Board/University	Year of Passing	Percentage/GPA
B. Tech , E.C.E	K L University	2017	7.48/10 (till date)
Intermediate(BIEAP)	Sri Chaitanya Jr. College	2013	82.5%
SSC	Sri Chaitanya Techno School	2011	89.84%

TECHNICAL SKILLS

Programming Languages Known: C, Assembly Language, Verilog and Core Java (Basics).
Software's known: Xilinx ISE, Multisim, HFSS, DSCH and Lab-View.

PROJECTS

- Employee Structured System using C.
    In this project an employee structure is used to maintain and show the data of employees in a firm.

- Automatic Railway Gate Control System Using 8051 microcontroller.
    In this project railway gate control is automated that reduces the risk of lives.

- Comparative analysis of power and area of Vedic and Array Multipliers using Xilinx.
    In this project the architecture of multipliers that are constructed using Vedic and array principles are compared to produce an efficient multiplier according to requirement of user.

INDUSTRIAL EXPOSURE
Undergone one month training in Electronics Corporation of India Limited (ECIL) in embedded stream during period of 1st June 2015 to 30th June 2015.

EXTRA CURRICULAR ACTIVITES

- Worked as a member in AIESEC in Vijayawada
- Worked as Treasurer for PULSE, Student body of ECE department.

HOBBIES
- Swimming
- Singing
- Reading Novels and Cooking.

DECLERATION
 I do hereby declare that the above information is true to the best of my knowledge.

Place and Date: Vaibhav Majety