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|  | High Speed 16-Bit Three Operand Approximate Parallel Prefix Adder (AxPPA) using Vertex-5 XC5VLX50-2ff1153 FPGA | |
| Brent Kung Adder | Kogge Stone Adder |
| Number of Slice LUTs | 24 | 32 |
| Number of Occupied Slices | 19 | 21 |
| Number of Bonded IOBs | 64 | 64 |
| Delay (ns) | 6.838 | 6.428 |
| Power (mW) | 0.525 | 0.525 |

