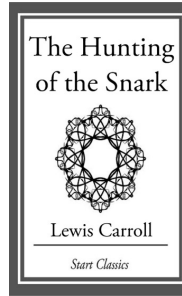


What is a SNARK?

(no, it is not an imaginary animal)

Dan Boneh
Stanford University



What is a SNARK ? (intuition)

SNARK: a succinct proof that a certain statement is true

Example statement: “I know an m such that $\text{SHA256}(m) = 0$ ”

- **SNARK:** the proof is “**short**” and “**fast**” to verify
[if m is 1GB then the trivial proof (the message m) is neither]
- **zk-SNARK:** the proof “reveals nothing” about m

zk-SNARK: many blockchain applications

Private Tx on a public blockchain:

- Tornado cash, Zcash, IronFish
- Private Dapps: Aleo

Compliance:

- Private proofs of solvency and compliance
- Zero-knowledge taxes



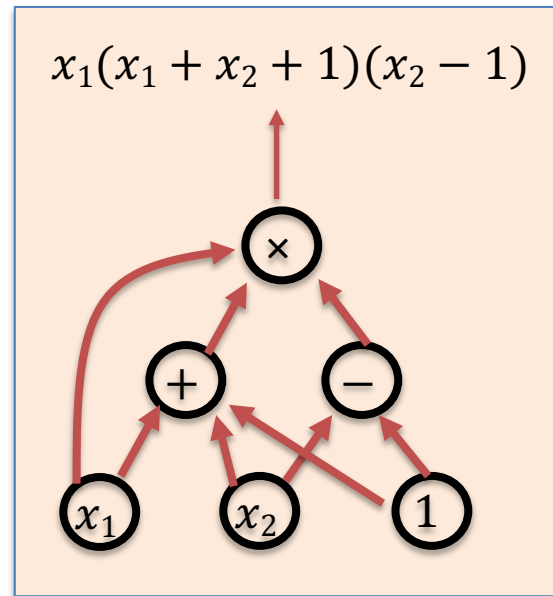
Scalability: Rollup systems with validity proofs

Cryptographic Background

(1) arithmetic circuits

- Fix a finite field $\mathbb{F} := \{0, \dots, p-1\}$ for some prime $p > 2$.
- **Arithmetic circuit:** $C: \mathbb{F}^n \rightarrow \mathbb{F}$
 - directed acyclic graph (DAG) where internal nodes are labeled $+$, $-$, or \times
inputs are labeled $1, x_1, \dots, x_n$
 - defines an n -variate polynomial with an evaluation recipe

$$|C| = \# \text{ gates in } C$$



Interesting arithmetic circuits

Examples:

- $C_{\text{hash}}(h, \mathbf{m})$: outputs 0 if $\text{SHA256}(\mathbf{m}) = h$, and $\neq 0$ otherwise

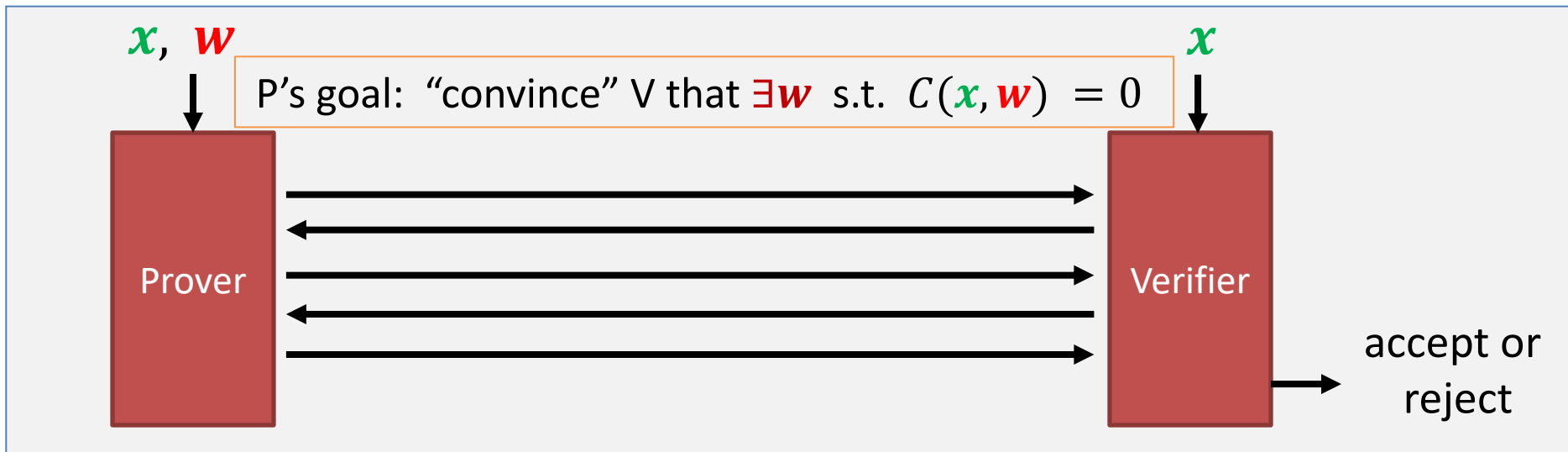
$$C_{\text{hash}}(h, \mathbf{m}) := (h - \text{SHA256}(\mathbf{m})) , \quad |C_{\text{hash}}| \approx 20\text{K gates}$$

- $C_{\text{sig}}(\text{pk}, m, \sigma)$: outputs 0 if σ is a valid ECDSA signature on m with respect to pk

(2) Argument systems (for NP)

Public arithmetic circuit: $C(\mathbf{x}, \mathbf{w}) \rightarrow \mathbb{F}$

public statement in \mathbb{F}^n $\xrightarrow{\quad}$ secret witness in \mathbb{F}^m



(non-interactive) Preprocessing argument systems

Public arithmetic circuit: $C(\mathbf{x}, \mathbf{w}) \rightarrow \mathbb{F}$

public statement in \mathbb{F}^n

secret witness in \mathbb{F}^m

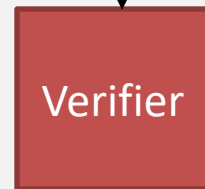
Preprocessing (setup): $S(C) \rightarrow$ public parameters (S_p, S_v)

$S_p, \mathbf{x}, \mathbf{w}$



proof π

S_v, \mathbf{x}



accept or
reject

Preprocessing argument System

A preprocessing argument system is a triple (S, P, V) :

- $S(C) \rightarrow$ public parameters (S_p, S_v) for prover and verifier
- $P(S_p, \mathbf{x}, \mathbf{w}) \rightarrow$ proof π
- $V(S_v, \mathbf{x}, \pi) \rightarrow$ accept or reject

Argument system: requirements (informal)

Prover $P(S_p, \mathbf{x}, \mathbf{w})$

Verifier $V(S_v, \mathbf{x}, \pi)$



Complete: $\forall x, w: C(\mathbf{x}, \mathbf{w}) = 0 \Rightarrow \Pr[V(S_v, x, P(S_p, \mathbf{x}, \mathbf{w})) = \text{accept}] = 1$

Knowledge sound: $V \text{ accepts} \Rightarrow P \text{ "knows" } \mathbf{w} \text{ s.t. } C(\mathbf{x}, \mathbf{w}) = 0$

P^* does not "know" $\mathbf{w} \Rightarrow \Pr[V(S_v, x, \pi) = \text{accept}] < \text{negligible}$

Optional: **Zero knowledge:** $(C, S_p, S_v, \mathbf{x}, \pi)$ "reveal nothing" about \mathbf{w}

SNARK: a Succinct ARgument of Knowledge

A succinct preprocessing argument system is a triple (S, P, V) :

- $S(C) \rightarrow$ public parameters (S_p, S_v) for prover and verifier
- $P(S_p, \mathbf{x}, \mathbf{w}) \rightarrow$ short proof π ; $|\pi| = O(\log(|C|), \lambda)$
- $V(S_v, \mathbf{x}, \pi)$ fast to verify ; $\text{time}(V) = O(|x|, \log(|C|), \lambda)$

short “summary” of circuit

Why preprocess C ??

SNARK: a Succinct ARgument of Knowledge

A succinct preprocessing argument system is a triple (S, P, V) :

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SNARK: (S, P, V) is **complete**, **knowledge sound**, and **succinct**

zk-SNARK: (S, P, V) is a SNARK and is **zero knowledge**

The trivial argument system

- (a) Prover sends w to verifier,
- (b) Verifier checks if $C(x, w) = 0$ and accepts if so.

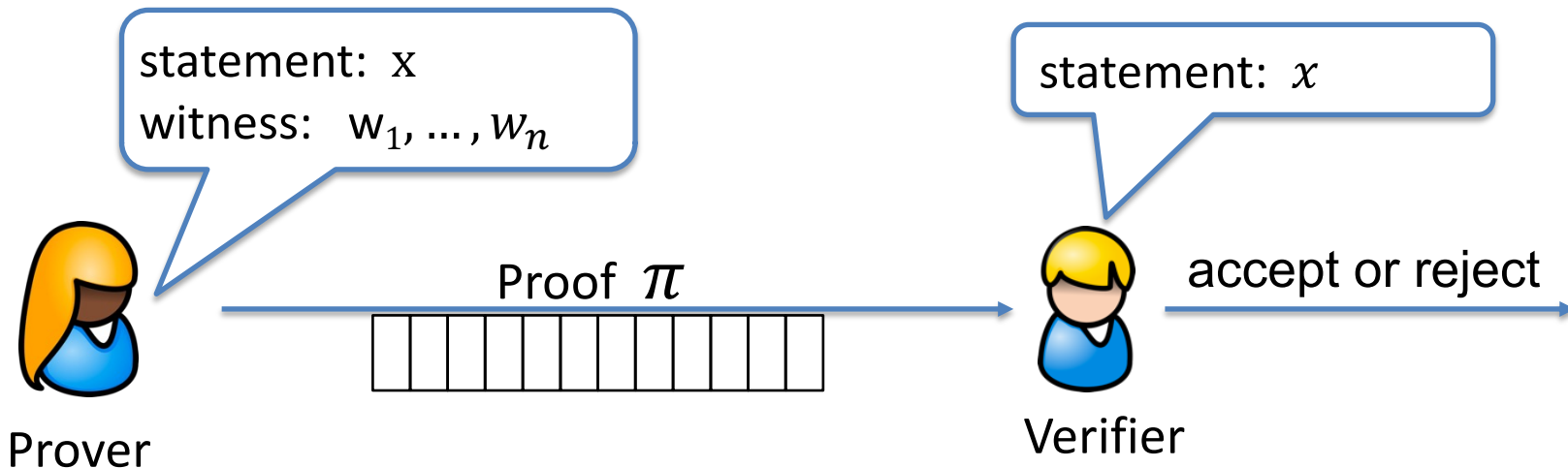
Problems with this:

- (1) w might be secret: prover does not want to reveal w to verifier
- (2) w might be long: we want a “short” proof
- (3) computing $C(x, w)$ may be hard: we want a “fast” verifier

Back to our first example ...

Prover: I know (w_1, \dots, w_n) such that $H(w_1, \dots, w_n) = x$

SNARK: $\text{size}(\pi)$ and $\text{VerifyTime}(\pi)$ is $O(\log n)$!!



Types of preprocessing Setup

Recall setup for circuit C : $S(C; r) \rightarrow$ public parameters (S_p, S_v)

 random bits


Types of setup:

trusted setup per circuit: $S(C; r)$ random r must be kept secret from prover
prover learns $r \Rightarrow$ can prove false statements

trusted but universal (updatable) setup: secret r is independent of C

$S = (S_{init}, S_{index})$: $\underbrace{S_{init}(\lambda; r) \rightarrow pp}_{\text{one-time}}, \underbrace{S_{index}(pp, C) \rightarrow (S_p, S_v)}_{\text{no secret data from prover}}$

transparent setup: $S(C)$ does not use secret data (no trusted setup)

better


Significant progress in recent years (partial list)

	size of proof π	size of S_p (beyond C)	verifier time (for a common task)	trusted setup?
Groth'16	≈ 200 Bytes $O_\lambda(1)$	$O_\lambda(C)$	≈ 3 ms $O_\lambda(1)$	yes/ per circuit
Plonk/Marlin	≈ 400 Bytes $O_\lambda(1)$	$O_\lambda(C)$	≈ 6 ms $O_\lambda(1)$	yes/ universal

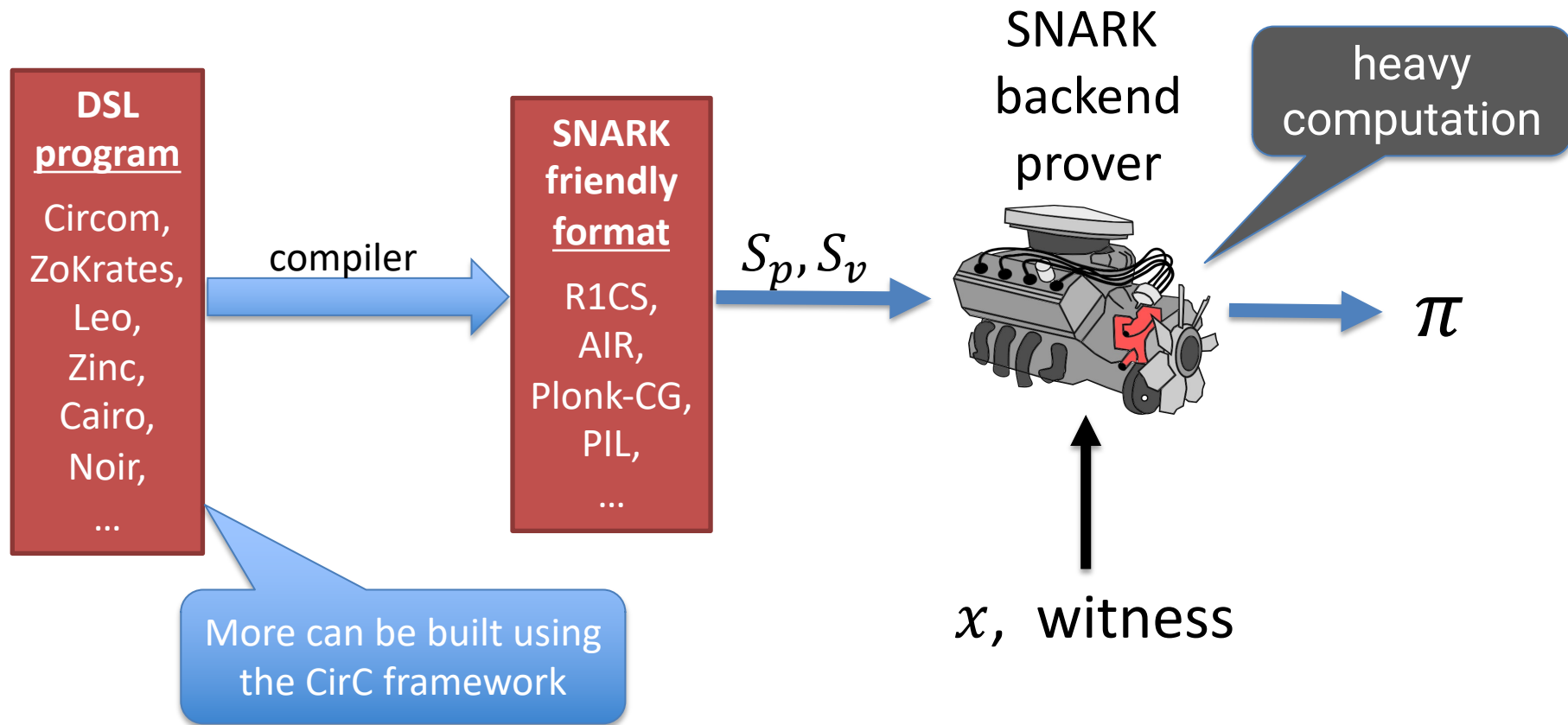
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Bulletproofs	≈ 1.5 KB $O_\lambda(\log C)$	$O_\lambda(1)$	≈ 1.5 sec $O_\lambda(C)$	no
STARK	≈ 80 KB $O_\lambda(\log^2 C)$	$O_\lambda(1)$	≈ 10 ms $O_\lambda(\log C)$	no
DARK	≈ 10 KB $O_\lambda(\log C)$	$O_\lambda(1)$	$O_\lambda(\log C)$	no
⋮	⋮			⋮

Significant progress in recent years (partial list)

	size of proof π	size of S_p (beyond C)	verifier time (for a common task)	trusted setup?
Groth'16	$\approx 200 \text{ B}$		$\approx 2 \text{ ms}$	yes/
Plonk/Marlin	<div>Prover time is almost linear in C</div>			
Bulletproofs				
STARK				
DARK	$\approx 10 \text{ KB}$ $O_\lambda(\log C)$	$O_\lambda(1)$	$O_\lambda(\log C)$	no
\vdots	\vdots			\vdots

A SNARK software system



How to define “knowledge soundness”
and “zero knowledge”?

Definitions: (1) knowledge sound

Goal: if V accepts then P “knows” w s.t. $C(x, w) = 0$

What does it mean to “know” w ??

informal def: P knows w , if w can be “extracted” from P



Definitions: (1) knowledge sound

Formally: (S, P, V) is **knowledge sound** for a circuit C if

for every poly. time adversary $A = (A_0, A_1)$ such that

$$S(C) \rightarrow (S_p, S_v), \quad (x, \text{state}) \leftarrow A_0(S_p), \quad \pi \leftarrow A_1(S_p, x, \text{state}):$$

$$\Pr[V(S_v, x, \pi) = \text{accept}] > 1/10^6 \quad (\text{non-negligible})$$

there is an efficient **extractor** E (that uses A_1 as a black box) s.t.

$$S(C) \rightarrow (S_p, S_v), \quad (x, \text{state}) \leftarrow A_0(S_p), \quad w \leftarrow E^{A_1(S_p, x, \text{state})}(S_p, x):$$

$$\Pr[C(x, w) = 0] > 1/10^6 - \epsilon \quad (\text{for a negligible } \epsilon)$$

Definitions: (2) Zero knowledge



Where is
Waldo?



Definitions: (2) Zero knowledge (simplified)

(S, P, V) is **zero knowledge** if for every $x \in \mathbb{F}^n$
proof π “reveals nothing” about w , other than its existence

What does it mean to “reveal nothing” ??

Informal def: π “reveals nothing” about w if the verifier can
generate π **by itself** \Rightarrow it learned nothing new from π

(S, P, V) is **zero knowledge** if there is an efficient alg. **Sim**
s.t. $(S_p, S_v, \pi) \leftarrow \mathbf{Sim}(C, x)$ “look like” the real S_p, S_v and π .

Main point: $\mathbf{Sim}(C, x)$ simulates π without knowledge of w

Definitions: (2) Zero knowledge (simplified)

Formally: (S, P, V) is (honest verifier) **zero knowledge** for a circuit C

if there is an efficient simulator ***Sim*** such that

for all $x \in \mathbb{F}^n$ s.t. $\exists w: C(x, w) = 0$ the distribution:

(C, S_p, S_v, x, π) : where $(S_p, S_v) \leftarrow S(C)$, $\pi \leftarrow P(S_p, x, \mathbf{w})$

is indistinguishable from the distribution:

(C, S_p, S_v, x, π) : where $(S_p, S_v, \pi) \leftarrow \mathbf{Sim}(C, x)$

Quick review

A zk-SNARK for a circuit C :

- For a public statement x , prover outputs a proof that “convinces” verifier that prover knows w s.t. $C(x, w) = 0$.
- Proof is short and fast to verify

What is it good for?

- Private payments and private Dapp logic (e.g., Aleo)
- Private compliance and L2 scalability

More to think about: private DAO? private governance?

How to build a zk-SNARK?

Next segment

END OF MODULE