





OUR EXAMPLE CODE



```
package main

import "fmt"

func main() {
    fmt.Println("hello world!")
}
```



THE GO COMPILER



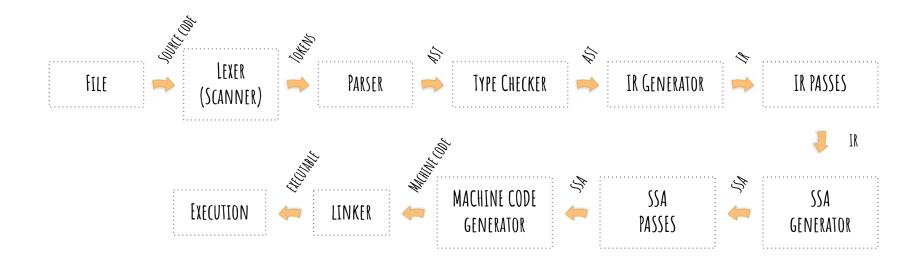


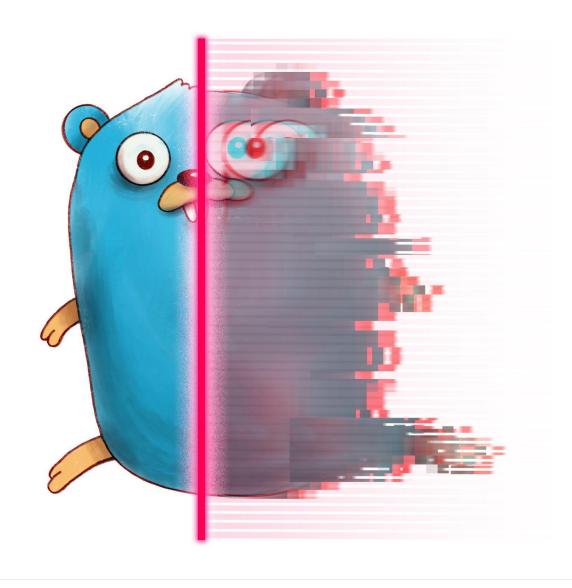


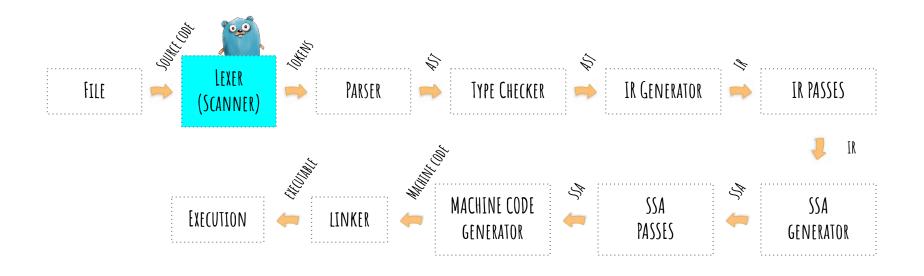
THE GO COMPILER



PHASES OF THE GO COMPILER







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Line 1: package (package)

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package main
import "fmt"

func main() {
    fmt.Println("hello world!")
}
```

```
Line 1: package (package)
Line 1: IDENT (main)
Line 1: ; ()
```



```
package main

import "fmt"

func main() {
    fmt.Println("hello world!")
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```
Line 1: package (package)
Line 1: IDENT (main)
Line 1: ; ()
Line 3: import (import)
Line 3: STRING ("fmt")
Line 3: ; ()
```

```
package main

import "fmt"

func main() {
    fmt.Printin("hello world!")
}
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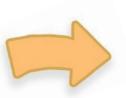


```
Line 1: package (package)
Line 1: IDENT (main)
Line 1: ; ()
Line 3: import (import)
Line 3: STRING ("fmt")
Line 3: ; ()
Line 5: func (func)
Line 5: IDENT (main)
Line 5: ( ()
Line 5: ) ()
Line 5: { ()
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package main

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Line 3: STRING ("fmt")
Line 3: ; ()
Line 5: func (func)
Line 5: IDENT (main)
Line 5: ( ()
Line 5: ) ()
Line 5: { ()
Line 6: IDENT (fmt)
Line 6: . ()
Line 6: IDENT (Println)
Line 6: ( ()
Line 6: STRING ("hello-world!")
Line 6: ) ()
Line 6: ; ()
Line 7: } ()
Line 7: ; ()
```

```
case ',':
  tok = token.SEMICOLON
  lit = ";"

case '.':
  tok = token.PERIOD
  if s.ch == '.' && s.peek() == '.' {
     s.next()
     s.next() // consume last '.'
     tok = token.ELLIPSIS
  }
```

```
func (s *Scanner) switch2(tok0, tok1 token.Token) token.Token {
   if s.ch == '=' {
        s.next()
        return tok1
   }
   return tok0
}
...
case '*':
   tok = s.switch2(token.MUL, token.MUL_ASSIGN)
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case ';':
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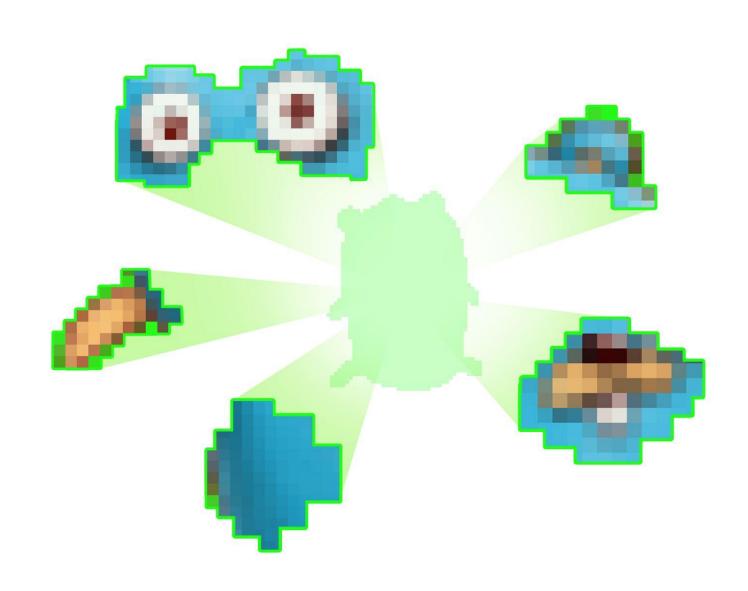
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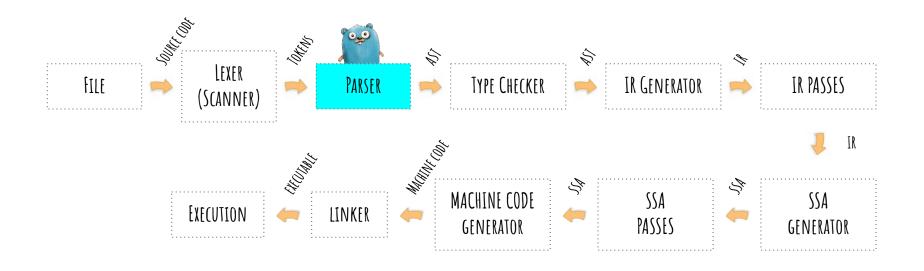
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package main

import "fmt"

func main() {
    fmt.Println("hello world!")
}
```

```
0 *ast.File {
   . Package: helloworld.go:1:1
     Name: *ast.Ident {
        NamePos: helloworld.go:1:9
        Name: "main"
  . Decls: []ast.Decl (len = 2) {
 7 . . 0: *ast.GenDecl {
 8 . . TokPos: helloworld.go:3:1
9 . . . Tok: import
10 . . Lparen: -
11 . . . Specs: []ast.Spec (len = 1) {
12 . . . 0: *ast.ImportSpec {
13 . . . . Path: *ast.BasicLit {
14 . . . . . . ValuePos: helloworld.go:3:8
15 . . . . . Kind: STRING
16 . . . . . . Value: "\"fmt\""
17 . . . . . .
18 . . . EndPos: -
19 . . . . }
20 . . . }
21 . . . Rparen: -
22 . . }
23 . . 1: *ast.FuncDecl {
24 . . . Name: *ast.Ident {
25 . . . NamePos: helloworld.go:5:6
26 . . . Name: "main"
27 . . . Obj: *ast.Object {
28 . . . Kind: func
29 . . . . Name: "main"
30 . . . Decl: *(obj @ 23)
31 . . . . . }
32 . . . }
33 . . . Type: *ast.FuncType {
34 . . . Func: helloworld.go:5:1
35 . . . Params: *ast.FieldList {
36 . . . Opening: helloworld.go:5:10
37 . . . . Closing: helloworld.go:5:11
38 . . . . . }
39 . . . }
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40 . . . Body: *ast.BlockStmt {
41 . . . Lbrace: helloworld.go:5:13
42 . . . List: []ast.Stmt (len = 1) {
43 . . . . . 0: *ast.ExprStmt {
   . . . . . X: *ast.CallExpr {
  . . . . . . Fun: *ast.SelectorExpr {
  . . . . . . . X: *ast.Ident {
   . . . . . . . . NamePos: helloworld.go:6:2
   . . . . . . . . Name: "fmt"
   . . . . . . . Sel: *ast.Ident {
51 . . . . . . . . NamePos: helloworld.go:6:6
  . . . . . . . . Name: "Println"
  . . . . . . Lparen: helloworld.go:6:13
   . . . . . . Args: []ast.Expr (len = 1) {
   . . . . . . . . 0: *ast.BasicLit {
   . . . . . . . . ValuePos: helloworld.go:6:14
   . . . . . . . . Kind: STRING
   . . . . . . . . . Value: "\"hello-world!\""
. . . . . . Ellipsis: -
                   Rparen: helloworld.go:6:28
66 . . . . . .
  . . . Rbrace: helloworld.go:7:1
70 . . }
71 . }
72 . Scope: *ast.Scope {
73 . . Objects: map[string]*ast.Object (len = 1) {
74 . . . "main": *(obj @ 27)
75 . . }
77 . Imports: []*ast.ImportSpec (len = 1) {
78 . . 0: *(obj @ 12)
79 . }
80 . Unresolved: []*ast.Ident (len = 1) {
81 . . 0: *(obj @ 46)
82 . }
83 }
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import "fmt"

func main() {
    fmt.Println("hello world!")
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   . . . . . . . . NamePos: helloworld.go:6:2
                          Name: "fmt"
                  . . Sel: *ast.Ident {
             . . . . NamePos: helloworld.go:6:6
                          Name: "Println"
   . . . . . . Lparen: helloworld.go:6:13
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Line 1: IDENT (main)
Line 1: ; ()
Line 3: import (import)
Line 3: STRING ("fmt")
Line 3: ; ()
Line 5: func (func)
Line 5: IDENT (main)
Line 5: ( ()
Line 5: ) ()
Line 5: { ()
Line 6: IDENT (fmt)
Line 6: . ()
Line 6: IDENT (Println)
Line 6: ( ()
Line 6: STRING ("hello-world!")
Line 6: ) ()
Line 6: ; ()
Line 7: } ()
Line 7: ; ()
```



```
0 *ast.File {
1    . Package: helloworld.go:1:1
2    . Name: nil
6    . Decls: []ast.Decl (len = 0) {
71    . }
72    . Scope: *ast.Scope {
76    . }
77    . Imports: []*ast.ImportSpec (len = 0) {
79    . }
80    . Unresolved: []*ast.Ident (len = 0) {
82    . }
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Line 3: import (import)
Line 3: STRING ("fmt")
Line 3: ; ()
Line 5: func (func)
Line 5: IDENT (main)
Line 5: ( ()
Line 5: ) ()
Line 5: { ()
Line 6: IDENT (fmt)
Line 6: . ()
Line 6: IDENT (Println)
Line 6: ( ()
Line 6: STRING ("hello-world!")
Line 6: ) ()
Line 6: ; ()
Line 7: } ()
Line 7: ; ()
```



```
0 *ast.File {
1    . Package: helloworld.go:1:1
2    . Name: nil
6    . Decls: []ast.Decl (len = 0) {
71    . }
72    . Scope: *ast.Scope {
76    . }
77    . Imports: []*ast.ImportSpec (len = 0) {
79    . }
80    . Unresolved: []*ast.Ident (len = 0) {
82    . }
83 }
```

```
Line 1: package (package)
Line 1: IDENT (main)
Line 1: ; ()
Line 3: import (import)
Line 3: STRING ("fmt")
Line 3: ; ()
Line 5: func (func)
Line 5: IDENT (main)
Line 5: ( ()
Line 5: ) ()
Line 5: { ()
Line 6: IDENT (fmt)
Line 6: . ()
Line 6: IDENT (Println)
Line 6: ( ()
Line 6: STRING ("hello-world!")
Line 6: ) ()
Line 6: ; ()
Line 7: } ()
Line 7: ; ()
```



```
0 *ast.File {
1    . Package: helloworld.go:1:1
2    . Name: *ast.Ident {
3    . NamePos: helloworld.go:1:9
4    . Name: "main"
5    . }
6    . Decls: []ast.Decl (len = 0) {
71    . }
72    . Scope: *ast.Scope {
76    . }
77    . Imports: []*ast.ImportSpec (len = 0) {
79    . }
80    . Unresolved: []*ast.Ident (len = 0) {
82    . }
83 }
```

```
Line 1: package (package)
Line 1: IDENT (main)
Line 1: ; ()
Line 3: import (import)
Line 3: STRING ("fmt")
Line 3: ; ()
Line 5: func (func)
Line 5: IDENT (main)
Line 5: ( ()
Line 5: ) ()
Line 5: { ()
Line 6: IDENT (fmt)
Line 6: . ()
Line 6: IDENT (Println)
Line 6: ( ()
Line 6: STRING ("hello-world!")
Line 6: ) ()
Line 6: ; ()
Line 7: } ()
Line 7: ; ()
```



```
0 *ast.File {
   . Package: helloworld.go:1:1
 2 . Name: *ast.Ident {
        NamePos: helloworld.go:1:9
   . . Name: "main"
6 . Decls: []ast.Decl (len = 1) {
7 . . 0: *ast.GenDecl {
8 . . TokPos: helloworld.go:3:1
9 . . Tok: import
10 . . . Lparen: -
11 . . . Specs: []ast.Spec (len = 0) {
12 . . . . 0: *ast.ImportSpec {
20 . . . }
21 . . . Rparen: -
22 . . }
72 . Scope: *ast.Scope {
77 . Imports: []*ast.ImportSpec (len = 0) {
79 . }
80 . Unresolved: []*ast.Ident (len = 0) {
82 . }
83 }
```

```
Line 1: package (package)
Line 1: IDENT (main)
Line 1: ; ()
Line 3: import (import)
Line 3: STRING ("fmt")
Line 3: ; ()
Line 5: func (func)
Line 5: IDENT (main)
Line 5: ( ()
Line 5: ) ()
Line 5: { ()
Line 6: IDENT (fmt)
Line 6: . ()
Line 6: IDENT (Println)
Line 6: ( ()
Line 6: STRING ("hello-world!")
Line 6: ) ()
Line 6: ; ()
Line 7: } ()
Line 7: ; ()
```



```
0 *ast.File {
 1 . Package: helloworld.go:1:1
 2 . Name: *ast.Ident {
        NamePos: helloworld.go:1:9
 4 . . Name: "main"
 6 . Decls: []ast.Decl (len = 1) {
 7 . . 0: *ast.GenDecl {
 8 . . TokPos: helloworld.go:3:1
9 . . Tok: import
10 . . Lparen: -
11 . . . Specs: []ast.Spec (len = 1) {
12 . . . 0: *ast.ImportSpec {
13 . . . . Path: *ast.BasicLit {
14 . . . . . ValuePos: helloworld.go:3:8
15 . . . . . Kind: STRING
16 . . . . . . Value: "\"fmt\""
17 . . . . . .
18 . . . EndPos: -
19 . . . . }
20 . . . }
21 . . . Rparen: -
22 . . }
71 . }
72 . Scope: *ast.Scope {
76 . }
77 . Imports: []*ast.ImportSpec (len = 1) {
78 . . 0: *(obj @ 12)
79 . }
80 . Unresolved: []*ast.Ident (len = 0) {
82 . }
83 }
```

```
// ImportSpec = [ "." | PackageName ] ImportPath .
// ImportPath = string_lit .
func (p *parser) importDecl (group *Group) Decl {
       if trace {
              defer p.trace("importDecl")()
       d := new(ImportDecl)
       d.pos = p.pos()
       d.Group = group
       d.Pragma = p.takePragma()
       switch p.tok {
       case _Name:
              d.LocalPkgName = p.name()
       case Dot:
              d.LocalPkgName = NewName(p.pos(), ".")
              p.next()
       d.Path = p.oliteral()
       if d.Path == nil {
              p.syntaxError("missing import path")
              p.advance(_Semi, _Rparen)
              return d
       if !d.Path.Bad && d.Path.Kind != StringLit {
              p.syntaxError("import path must be a string")
              d.Path.Bad = true
       // d.Path.Bad || d.Path.Kind == StringLit
       return d
```

```
// ImportSpec = [ "." | PackageName ] ImportPath .
// ImportPath = string lit .
func (p *parser) importDecl (group *Group) Decl {
       if trace {
              defer p.trace("importDecl")()
       d := new(ImportDecl)
       d.pos = p.pos()
       d.Group = group
       d.Pragma = p.takePragma()
       switch p.tok {
       case _Name:
              d.LocalPkgName = p.name()
       case Dot:
              d.LocalPkgName = NewName(p.pos(), ".")
              p.next()
       d.Path = p.oliteral()
       if d.Path == nil {
              p.syntaxError("missing import path")
              p.advance(_Semi, _Rparen)
              return d
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       // d.Path.Bad || d.Path.Kind == StringLit
       return d
```

```
// ImportSpec = [ "." | PackageName ] ImportPath .
// ImportPath = string_lit .
func (p *parser) importDecl (group *Group) Decl {
       if trace {
              defer p.trace("importDecl")()
       d := new(ImportDecl)
       d.pos = p.pos()
       d.Group = group
       d.Pragma = p.takePragma()
       switch p.tok {
       case _Name:
              d.LocalPkgName = p.name()
       case Dot:
              d.LocalPkgName = NewName(p.pos(), ".")
              p.next()
       d.Path = p.oliteral()
       if d.Path == nil {
              p.syntaxError("missing import path")
              p.advance(_Semi, _Rparen)
              return d
       if !d.Path.Bad && d.Path.Kind != StringLit {
              p.syntaxError("import path must be a string")
              d.Path.Bad = true
       // d.Path.Bad || d.Path.Kind == StringLit
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```

```
// ImportSpec = [ "." | PackageName ] ImportPath .
// ImportPath = string_lit .
func (p *parser) importDecl (group *Group) Decl {
       if trace {
              defer p.trace("importDecl")()
       d := new(ImportDecl)
       d.pos = p.pos()
       d.Group = group
       d.Pragma = p.takePragma()
       switch p.tok {
       case _Name:
              d.LocalPkgName = p.name()
       case _Dot:
              d.LocalPkgName = NewName(p.pos(), ".")
              p.next()
       d.Path = p.oliteral()
       if d.Path == nil {
              p.syntaxError("missing import path")
              p.advance(_Semi, _Rparen)
              return d
       if !d.Path.Bad && d.Path.Kind != StringLit {
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              d.Path.Bad = true
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```
// ImportSpec = [ "." | PackageName ] ImportPath .
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       if trace {
              defer p.trace("importDecl")()
       d := new(ImportDecl)
       d.pos = p.pos()
       d.Group = group
       d.Pragma = p.takePragma()
       switch p.tok {
       case _Name:
              d.LocalPkgName = p.name()
       case Dot:
              d.LocalPkgName = NewName(p.pos(), ".")
              p.next()
       d.Path = p.oliteral()
       if d.Path == nil {
              p.syntaxError("missing import path")
              p.advance(_Semi, _Rparen)
               return d
       if !d.Path.Bad && d.Path.Kind != StringLit {
              p.syntaxError("import path must be a string")
              d.Path.Bad = true
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```
// ImportSpec = [ "." | PackageName ] ImportPath .
// ImportPath = string_lit .
func (p *parser) importDecl (group *Group) Decl {
       if trace {
              defer p.trace("importDecl")()
       d := new(ImportDecl)
       d.pos = p.pos()
       d.Group = group
       d.Pragma = p.takePragma()
       switch p.tok {
       case _Name:
              d.LocalPkgName = p.name()
       case Dot:
              d.LocalPkgName = NewName(p.pos(), ".")
              p.next()
       d.Path = p.oliteral()
       if d.Path == nil {
              p.syntaxError("missing import path")
              p.advance(_Semi, _Rparen)
              return d
       if !d.Path.Bad && d.Path.Kind != StringLit {
              p.syntaxError("import path must be a string")
              d.Path.Bad = true
       // d.Path.Bad || d.Path.Kind == StringLit
       return d
```

```
// FunctionDecl = "func" FunctionName [ TypeParams ] ( Function | Signature ) .
// FunctionName = identifier .
// Function
               = Signature FunctionBody .
// MethodDecl = "func" Receiver MethodName (Function | Signature ) .
               = Parameters .
func (p *parser) funcDeclOrNil () *FuncDecl {
       if trace {
              defer p.trace("funcDecl")()
       f := new(FuncDecl)
       f.pos = p.pos()
       f.Pragma = p.takePragma()
       if p.got( Lparen) {
              rcvr := p.paramList(nil, nil, _Rparen, false)
              switch len(rcvr) {
              case 0:
                      p.error("method has no receiver")
              default:
                      p.error("method has multiple receivers")
                      fallthrough
              case 1:
                      f.Recv = rcvr[0]
       if p.tok != Name {
              p.syntaxError ("expecting name or (")
              p.advance ( Lbrace, Semi)
              return nil
       f.Name = p.name()
       context := ""
       if f.Recv != nil {
              context = "method" // don't permit (method) type parameters in funcType
       f.TParamList, f.Type = p.funcType (context)
       if p.tok == Lbrace {
              f.Body = p.funcBody()
       return f
```

```
// FunctionDecl = "func" FunctionName [ TypeParams ] ( Function | Signature ) .
// FunctionName = identifier
               = Signature FunctionBody .
// MethodDecl = "func" Receiver MethodName ( Function | Signature ) .
               = Parameters .
func (p *parser) funcDeclOrNil () *FuncDecl {
       if trace {
              defer p.trace("funcDecl")()
       f := new(FuncDecl)
       f.pos = p.pos()
       f.Pragma = p.takePragma()
       if p.got( Lparen) {
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              switch len(rcvr) {
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              f.Body = p.funcBody()
       return f
```

```
// FunctionDecl = "func" FunctionName [ TypeParams ] ( Function | Signature ) .
// FunctionName = identifier .
// Function
               = Signature FunctionBody .
// MethodDecl = "func" Receiver MethodName (Function | Signature ) .
               = Parameters .
func (p *parser) funcDeclOrNil () *FuncDecl {
       if trace {
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       f := new(FuncDecl)
       f.pos = p.pos()
       f.Pragma = p.takePragma()
       if p.got( Lparen) {
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              switch len(rcvr) {
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                      p.error("method has no receiver")
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                      p.error("method has multiple receivers")
                      fallthrough
              case 1:
                      f.Recv = rcvr[0]
       if p.tok != Name {
              p.syntaxError ("expecting name or (")
              p.advance ( Lbrace, Semi)
              return nil
       f.Name = p.name()
       context := ""
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              context = "method" // don't permit (method) type parameters in funcType
       f.TParamList, f.Type = p.funcType (context)
       if p.tok == Lbrace {
              f.Body = p.funcBody()
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```

```
// FunctionDecl = "func" FunctionName [ TypeParams ] ( Function | Signature ) .
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               = Signature FunctionBody .
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              defer p.trace("funcDecl")()
       f := new(FuncDecl)
       f.pos = p.pos()
       f.Pragma = p.takePragma()
       if p.got( Lparen) {
               rcvr := p.paramList(nil, nil, Rparen, false)
               switch len(rcvr) {
                      p.error("method has no receiver")
              default:
                      p.error("method has multiple receivers")
                      fallthrough
              case 1:
                      f.Recv = rcvr[0]
       if p.tok != Name {
              p.syntaxError ("expecting name or (")
              p.advance( Lbrace, Semi)
              return nil
       f.Name = p.name()
       context := ""
       if f.Recv != nil {
              context = "method" // don't permit (method) type parameters in funcType
       f.TParamList, f.Type = p.funcType (context)
       if p.tok == Lbrace {
              f.Body = p.funcBody()
       return f
```

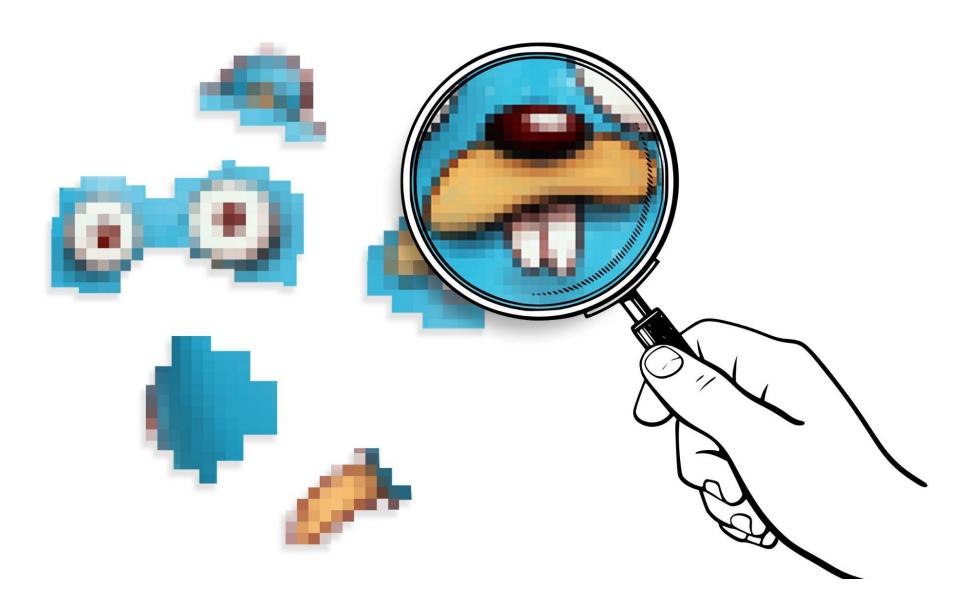
```
// \ {\it FunctionDecl} = \ {\it "func"} \ {\it FunctionName} \ [ \ {\it TypeParams} \ ] \ ( \ {\it Function} \ | \ {\it Signature} \ ) \ .
// FunctionName = identifier .
// Function
                = Signature FunctionBody .
// MethodDecl = "func" Receiver MethodName (Function | Signature ) .
                = Parameters .
func (p *parser) funcDeclOrNil () *FuncDecl {
       if trace {
               defer p.trace("funcDecl")()
       f := new(FuncDecl)
       f.pos = p.pos()
       f.Pragma = p.takePragma()
       if p.got( Lparen) {
               rcvr := p.paramList(nil, nil, _Rparen, false)
               switch len(rcvr) {
               case 0:
                       p.error("method has no receiver")
               default:
                       p.error("method has multiple receivers")
                       fallthrough
               case 1:
                       f.Recv = rcvr[0]
       if p.tok != Name
               p.syntaxError("expecting name or (")
               p.advance (Lbrace, Semi)
               return nil
       f.Name = p.name()
       context := ""
       if f.Recv != nil {
               context = "method" // don't permit (method) type parameters in funcType
       f.TParamList, f.Type = p.funcType (context)
       if p.tok == Lbrace {
               f.Body = p.funcBody()
       return f
```

```
// FunctionDecl = "func" FunctionName [ TypeParams ] ( Function | Signature ) .
// FunctionName = identifier .
// Function
               = Signature FunctionBody .
// MethodDecl = "func" Receiver MethodName (Function | Signature ) .
               = Parameters .
func (p *parser) funcDeclOrNil () *FuncDecl {
       if trace {
              defer p.trace("funcDecl")()
       f := new(FuncDecl)
       f.pos = p.pos()
       f.Pragma = p.takePragma()
       if p.got( Lparen) {
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              switch len(rcvr) {
              case 0:
                      p.error("method has no receiver")
              default:
                      p.error("method has multiple receivers")
                      fallthrough
              case 1:
                      f.Recv = rcvr[0]
       if p.tok != Name {
              p.syntaxError ("expecting name or (")
              p.advance ( Lbrace, Semi)
              return nil
       f.Name = p.name()
       context := ""
       if f.Recv != nil
              context = "method" // don't permit (method) type parameters in funcType
       f.TParamList, f.Type = p.funcType(context)
       if p.tok == Lbrace {
              f.Body = p.funcBody()
       return f
```

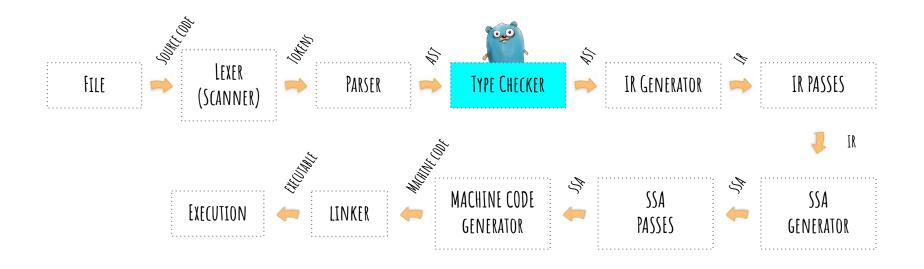
```
// FunctionDecl = "func" FunctionName [ TypeParams ] ( Function | Signature ) .
// FunctionName = identifier .
// Function
               = Signature FunctionBody .
// MethodDecl = "func" Receiver MethodName (Function | Signature ) .
               = Parameters .
func (p *parser) funcDeclOrNil () *FuncDecl {
       if trace {
              defer p.trace("funcDecl")()
       f := new(FuncDecl)
       f.pos = p.pos()
       f.Pragma = p.takePragma()
       if p.got( Lparen) {
              rcvr := p.paramList(nil, nil, _Rparen, false)
              switch len(rcvr) {
              case 0:
                      p.error("method has no receiver")
              default:
                      p.error("method has multiple receivers")
                      fallthrough
              case 1:
                      f.Recv = rcvr[0]
       if p.tok != Name {
              p.syntaxError ("expecting name or (")
              p.advance( Lbrace, Semi)
              return nil
       f.Name = p.name()
       context := ""
       if f.Recv != nil {
              context = "method" // don't permit (method) type parameters in funcType
       f.TParamList, f.Type = p.funcType (context)
       if p.tok == Lbrace {
              f.Body = p.funcBody()
       return f
```

```
// FunctionDecl = "func" FunctionName [ TypeParams ] ( Function | Signature ) .
// FunctionName = identifier .
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               = Signature FunctionBody .
// MethodDecl = "func" Receiver MethodName (Function | Signature ) .
               = Parameters .
func (p *parser) funcDeclOrNil () *FuncDecl {
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              defer p.trace("funcDecl")()
       f := new(FuncDecl)
       f.pos = p.pos()
       f.Pragma = p.takePragma()
       if p.got( Lparen) {
              rcvr := p.paramList(nil, nil, _Rparen, false)
              switch len(rcvr) {
              case 0:
                      p.error("method has no receiver")
              default:
                      p.error("method has multiple receivers")
                      fallthrough
              case 1:
                      f.Recv = rcvr[0]
       if p.tok != Name {
              p.syntaxError ("expecting name or (")
              p.advance (Lbrace, Semi)
              return nil
       f.Name = p.name()
       context := ""
       if f.Recv != nil {
              context = "method" // don't permit (method) type parameters in funcType
       f.TParamList, f.Type = p.funcType (context)
       if p.tok == Lbrace {
              f.Body = p.funcBody()
       return f
```

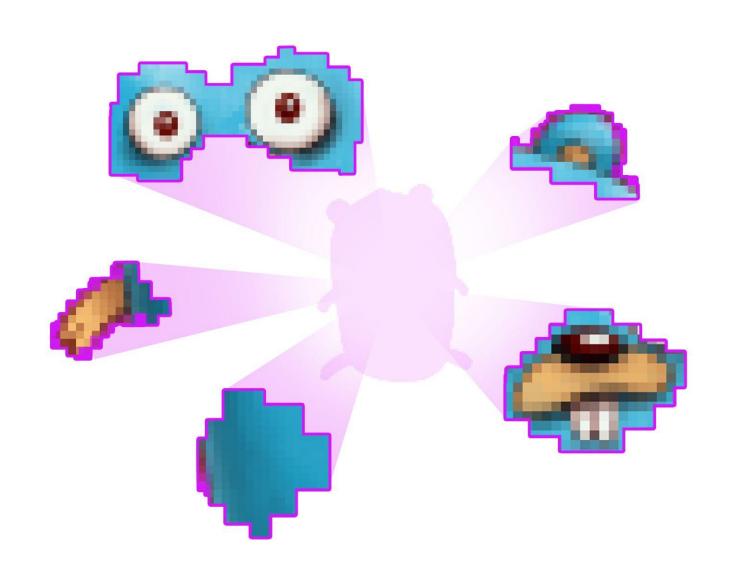
TYPE CHECKER

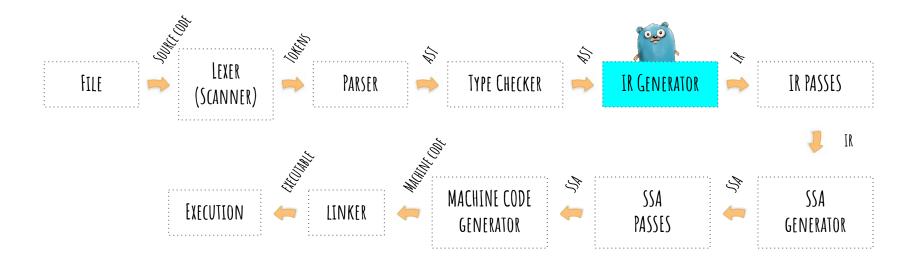


TYPE CHECKER



THE IR





```
package main
import "fmt"

func main() {
   fmt.Println("hello world!")
}
```

```
*ir.Package
       Imports: []*types.Pkg (1 entries) {
         0: *types.Pkg {
                  map[.inittask:fmt..inittask Append:fmt.Append ...]
             Direct: true
   . Decls: []ir.Node (1 entries) {
   . . 0: *ir.Func
   . . . Body: ir.Nodes (1 entries)
                0: *ir.CallExpr
                        FieldTrack: map[]
                         Endlineno: $GOROOT/src/fmt/print.go:295:1
                        WBPos: <unknown line number >
                         ABI: ABIInternal
                   Args: ir. Nodes (1 entries)
                      0: *ir.ConvExpr {
                        X: *ir.ConstExpr {}
                Defn: *(@14)
             Parents: []ir.ScopeID (0 entries) {}
            Marks: []ir.Mark (0 entries) {}
             FieldTrack: map[]
             Endlineno: ../hello.go:7:1
            WBPos: <unknown line number >
            ABI: ABIInternal
55 . }
```

```
package main
import "fmt"

func main() {
   fmt.Println("hello world!")
}
```

```
Imports: []*types.Pkg (1 entries) {
                   map[.inittask:fmt..inittask Append:fmt.Append ...]
12
   . Decls: []ir.Node (1 entries) {
   . . 0: *ir.Func
   . . . Body: ir.Nodes (1 entries)
                0: *ir.CallExpr {
                         FieldTrack: map[]
                         Endlineno: $GOROOT/src/fmt/print.go:295:1
                         WBPos: <unknown line number >
                         ABI: ABIInternal
                  Args: ir.Nodes (1 entries)
                      0: *ir.ConvExpr {
                        X: *ir.ConstExpr {}
               Defn: *(@14)
            Parents: []ir.ScopeID (0 entries) {}
            Marks: []ir.Mark (0 entries) {}
             FieldTrack: map[]
            Endlineno: ../hello.go:7:1
            WBPos: <unknown line number >
            ABI: ABIInternal
```

```
package main
import "fmt"

func main() {
   fmt.Println("hello world!")
}
```

```
*ir.Package
       Imports: []*types.Pkg (1 entries) {
         0: *types.Pkg {
                  map[.inittask:fmt..inittask Append:fmt.Append ...]
             Direct: true
   . Decls: []ir.Node (1 entries) {
   . . . Body: ir.Nodes (1 entries)
                0: *ir.CallExpr
                         FieldTrack: map[]
                         Endlineno: $GOROOT/src/fmt/print.go:295:1
                         WBPos: <unknown line number >
                         ABI: ABIInternal
                   Args: ir.Nodes (1 entries)
                      0: *ir.ConvExpr {
                         X: *ir.ConstExpr {}
               Defn: *(@14)
             Parents: []ir.ScopeID (0 entries) {}
            Marks: []ir.Mark (0 entries) {}
             FieldTrack: map[]
             Endlineno: ../hello.go:7:1
            WBPos: <unknown line number >
            ABI: ABIInternal
55 . }
```

```
package main
import "fmt"

func main() {
    fmt.Println("hello world!")
}
```

```
Imports: []*types.Pkg (1 entries) {
      0: *types.Pkg {
               map[.inittask:fmt..inittask Append:fmt.Append ...]
         Direct: true
. Decls: []ir.Node (1 entries) {
. . 0: *ir.Func
        Body: ir.Nodes (1 entries) {
                     Endlineno: $GOROOT/src/fmt/print.go:295:1
                     WBPos: <unknown line number >
                     ABI: ABIInternal
               Args: ir. Nodes (1 entries) {
                  0: *ir.ConvExpr {
                     X: *ir.ConstExpr {}
            Defn: *(@14)
         Parents: []ir.ScopeID (0 entries) {}
         Marks: []ir.Mark (0 entries) {}
         FieldTrack: map[]
         Endlineno: ../hello.go:7:1
         WBPos: <unknown line number >
         ABI: ABIInternal
```

```
func (q *irgen) constDecl(out *ir.Nodes, decl *syntax.ConstDecl) {
     g.pragmaFlags (decl.Pragma, 0)
     for , name := range decl.NameList {
           name, obj := q.def(name)
          // For untyped numeric constants, make sure the value
           // representation matches what the rest of the
           // compiler (really just iexport) expects.
          // TODO (mdempsky): Revisit after #43891 is resolved.
          val := obj.(*types2.Const).Val()
           switch name.Type() {
           case types.UntypedInt, types.UntypedRune:
                val = constant.ToInt(val)
           case types.UntypedFloat:
                val = constant.ToFloat(val)
           case types.UntypedComplex:
                val = constant.ToComplex(val)
           name.SetVal(val)
          out.Append(ir.NewDecl(q.pos(decl), ir.ODCLCONST, name))
```

```
func (g *irgen) funcDecl(out *ir.Nodes, decl *syntax.FuncDecl) {
       ... // Omitted code
      fn := ir.NewFunc(g.pos(decl))
      fn.Nname, = g.def(decl.Name)
      fn.Nname.Func = fn
      fn.Nname.Defn = fn
       ... // Omitted code
      if decl.Name.Value == "init" && decl.Recv == nil {
             g.target.Inits = append(g.target.Inits, fn)
      saveHaveEmbed := g.haveEmbed
      saveCurDecl := g.curDecl
      g.curDecl = ""
      q.later(func() {
             defer func(b bool, s string)
                    q.haveEmbed = b
                    q.curDecl = s
             } (g.haveEmbed, g.curDecl)
             g.haveEmbed = saveHaveEmbed
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             if fn.Type().HasTParam() {
                    g.topFuncIsGeneric = true
             g.funcBody(fn, decl.Recv, decl.Type, decl.Body)
             g.topFuncIsGeneric = false
             if fn.Type().HasTParam() && fn.Body != nil {
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                          Cost: 1,
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                          Body: fn.Body,
             out.Append(fn)
      })
```

```
func (q *irgen) constDecl(out *ir.Nodes, decl *syntax.ConstDecl) {
     g.pragmaFlags (decl.Pragma, 0)
     for , name := range decl.NameList {
           name, obj := q.def(name)
          // For untyped numeric constants, make sure the value
           // representation matches what the rest of the
           // compiler (really just iexport) expects.
          // TODO (mdempsky): Revisit after #43891 is resolved.
          val := obj.(*types2.Const).Val()
           switch name.Type() {
           case types.UntypedInt, types.UntypedRune:
                val = constant.ToInt(val)
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                val = constant.ToFloat(val)
           case types.UntypedComplex:
                val = constant.ToComplex(val)
           name.SetVal(val)
          out.Append(ir.NewDecl(q.pos(decl), ir.ODCLCONST, name))
```

```
func (g *irgen) funcDecl(out *ir.Nodes, decl *syntax.FuncDecl) {
       ... // Omitted code
      fn := ir.NewFunc(g.pos(decl))
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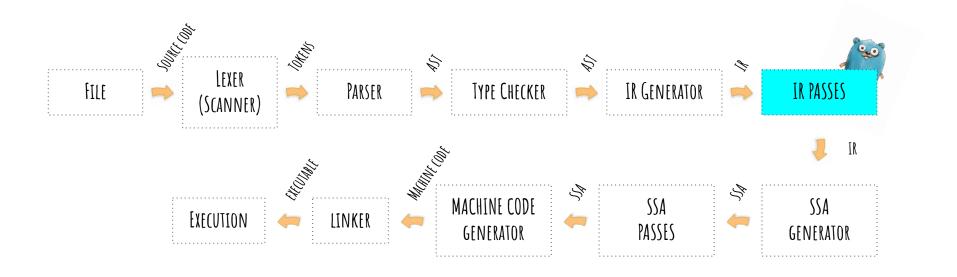
```
func (g *irgen) funcDecl(out *ir.Nodes, decl *syntax.FuncDecl) {
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             out.Append(fn)
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                          Cost: 1,
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                          Body: fn.Body,
             out.Append(fn)
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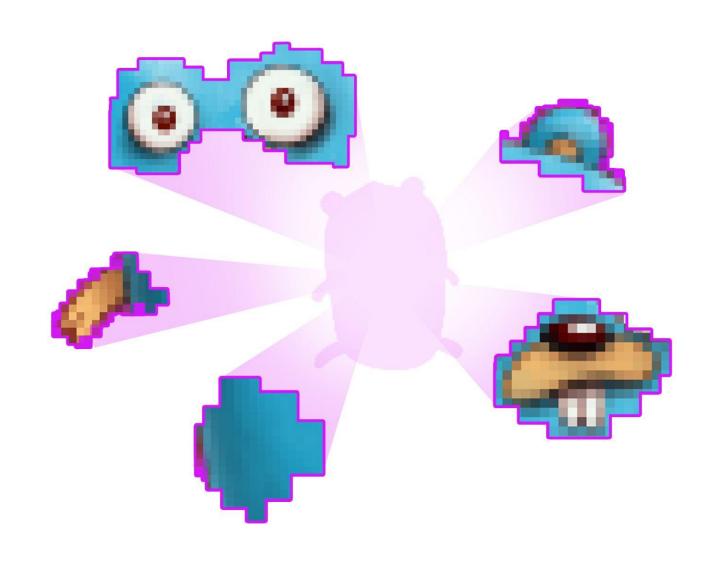
THE IR PASSES

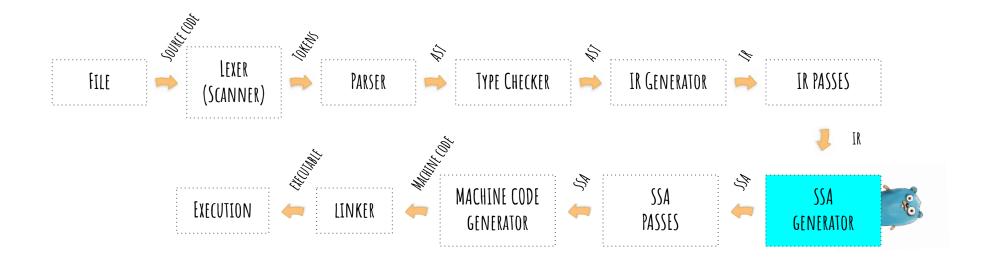
- Dead code elimination
- Function call inlining
- Devirtualize functions
- Escape analysis

TOO MUCH INFO? TAKE A BREAK.

LOOK... HERE IS A KITTEN







```
package main

import "fmt"

func main() {
    fmt.Println("hello world!")
}
```



```
b1:
        v1 (?) = InitMem <mem>
        v2 (?) = SP <uintptr>
        v3 (?) = SB < uintptr>
        v4 (?) = ConstInterface <any>
        v5 (?) = ArrayMake1 <[1]any> v4
        v6 (6) = VarDef < mem > {.autotmp 8} v1
        v7 (6) = LocalAddr <*[1]any> {.autotmp 8} v2 v6
        v8 (6) = Store < mem > {[1]any} v7 v5 v6
        v9 (6) = LocalAddr < [1] any {.autotmp 8} v2 v8
        v10 (?) = Addr <*uint8> {type.string} v3
        v11 (?) = Addr <*string> \{main..stmp 0\} v3
        v12 (6) = IMake < any > v10 v11
        v13 (6) = NilCheck <void> v9 v8
        v14 \ (?) = Const64 < int > [0] \ (fmt.n[int], fmt..autotmp_0[int])
        v15 (?) = Const64 < int > [1]
        v16 (6) = PtrIndex <*any> v9 v14
        v17 (6) = Store < mem > {any} v16 v12 v8
        v18 (6) = NilCheck < void > v9 v17
        v19 (6) = Copy <*any> v9
        v20 (6) = IsSliceInBounds <bool> v14 v15
        v25 (?) = ConstInterface <error> (fmt.err[error], fmt..autotmp 1[error])
        v28 (?) = Addr <*uint8> {go.itab.*os.File,io.Writer} v3
        v29 (?) = Addr <**os.File> {os.Stdout} v3
If v20 \rightarrow b2 \ b3 \ (likely) \ (6)
b2: ← b1
        v23 (6) = Sub64 < int > v15 v14
        v24 (6) = SliceMake <[]any> v19 v23 v23 (fmt.a[[]any])
        v26 (6) = Copy < mem > v17
        v27 (+6) = InlMark < void > [0] v26
        v30 (294) = Load <*os.File> v29 v26
        v31 (294) = IMake <io.Writer> v28 v30
        v32 (294) = StaticLECall <int, error, mem> {AuxCall{fmt.Fprintln}} [40] v31 v24 v26
        v33 (294) = SelectN < mem > [2] v32
        v34 (294) = SelectN < int > [0] v32
        v35 (294) = SelectN <int> [0] v32 (fmt.n[int], fmt..autotmp 0[int])
        v36 (294) = SelectN <error> [1] v32 (fmt.err[error], fmt..autotmp 1[error])
Plain \rightarrow b4 (+6)
b3: ← b1
        v21 (6) = Copy < mem > v17
        v22 (6) = PanicBounds <mem> [6] v14 v15 v21
Exit v22 (6)
b4: ← b2
        v38 (7) = Copy < mem > v33
        v37 (7) = MakeResult < mem > v38
Ret v37 (7)
name fmt.a[[]any]: v24
name fmt.n[int]: v14 v35
name fmt.err[error]: v25 v36
name fmt..autotmp 0[int]: v14 v35
name fmt..autotmp 1[error]: v25 v36
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        v9 (6) = LocalAddr <*[1]any> {.autotmp 8} v2 v8
        v10 (?) = Addr <*uint8> {type.string} v3
        v11 (?) = Addr <*string> {main..stmp 0} v3
        v12 (6) = IMake < any > v10 v11
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        v38 (7) = Copy < mem > v33
        v37 (7) = MakeResult < mem > v38
Ret v37 (7)
name fmt.a[[]any]: v24
name fmt.n[int]: v14 v35
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        v3 (?) = SB < uintptr>
        v4 (?) = ConstInterface <any>
        v5 (?) = ArrayMake1 <[1]any> v4
        v6 (6) = VarDef < mem > {.autotmp 8} v1
        v7 (6) = LocalAddr <*[1]any> {.autotmp 8} v2 v6
        v8 (6) = Store < mem > {[1]any} v7 v5 v6
        v9 (6) = LocalAddr < [1] any {.autotmp 8} v2 v8
        v10 (?) = Addr <*uint8> {type.string} v3
        v11 (?) = Addr <*string> \{main..stmp 0\} v3
        v12 (6) = IMake < any > v10 v11
        v13 (6) = NilCheck <void> v9 v8
        v14 (?) = Const64 < int > [0] (fmt.n[int], fmt..autotmp 0[int])
        v15 (?) = Const64 < int > [1]
        v16 (6) = PtrIndex <*any> v9 v14
        v17 (6) = Store <mem> {any} v16 v12 v8
        v18 (6) = NilCheck < void > v9 v17
        v19 (6) = Copy <*any> v9
        v20 (6) = IsSliceInBounds <bool> v14 v15
        v25 (?) = ConstInterface <error> (fmt.err[error], fmt..autotmp 1[error])
        v28 (?) = Addr <*uint8> {go.itab.*os.File,io.Writer} v3
        v29 (?) = Addr <**os.File> {os.Stdout} v3
If v20 \rightarrow b2 \ b3 \ (likely) \ (6)
b2: ← b1
        v23 (6) = Sub64 < int > v15 v14
        v24 (6) = SliceMake <[]any> v19 v23 v23 (fmt.a[[]any])
        v26 (6) = Copy < mem > v17
        v27 (+6) = InlMark < void > [0] v26
        v30 (294) = Load <*os.File> v29 v26
        v31 (294) = IMake <io.Writer> v28 v30
        v32 (294) = StaticLECall <int, error, mem> {AuxCall{fmt.Fprintln}} [40] v31 v24 v26
        v33 (294) = SelectN < mem > [2] v32
        v34 (294) = SelectN < int > [0] v32
        v35 (294) = SelectN <int> [0] v32 (fmt.n[int], fmt..autotmp 0[int])
        v36 (294) = SelectN <error> [1] v32 (fmt.err[error], fmt..autotmp 1[error])
Plain \rightarrow b4 (+6)
b3: ← b1
        v21 (6) = Copy < mem > v17
        v22 (6) = PanicBounds <mem> [6] v14 v15 v21
Exit v22 (6)
b4: ← b2
        v38 (7) = Copy < mem > v33
        v37 (7) = MakeResult < mem > v38
Ret v37 (7)
name fmt.a[[]any]: v24
name fmt.n[int]: v14 v35
name fmt.err[error]: v25 v36
name fmt..autotmp 0[int]: v14 v35
name fmt..autotmp 1[error]: v25 v36
```

```
package main

import "fmt"

func main() {
    fmt.Println("hello world!")
}
```



```
# Name: hello.init
# Package: hello
# Synthetic: package initializer
func init():
0:
                                                                     entry: PS:2
    t0 = *init$quard
                                                                          bool
    if t0 goto 2 else 1
1:
                                                               inits.tart P:1 S:1
    *init$quard = true:bool
    t1 = fmt.init()
                                                                          ()
    jump 2
                                                                initdone P:2 S:0
    return
# Name: hello.main
# Package: hello
# Location: hello.go:8:6
func main():
                                                                     entry: PS:0
0:
    t0 = new [1]any (varargs)
                                                                     *[1]any
    t1 = &t0[0:int]
                                                                        *any
    t2 = make any <- string ("hello world!":string)
                                                                         any
    *t1 = t2
    t3 = slice t0[:]
                                                                       []any
    t4 = fmt.Println(t3...)
                                                          (n int, err error)
    return
```

```
// stmtList converts the statement list n to SSA and adds it to s.
func (s *state) stmtList(l ir.Nodes) {
       for , n := range 1 {
              s.stmt(n)
// stmt converts the statement n to SSA and adds it to s.
func (s *state) stmt(n ir.Node) {
case ir.ODCL:
      n := n.(*ir.Decl)
       if v := n.X; v.Esc() == ir.EscHeap {
              s.newHeapaddr (v)
case ir.OVARDEF:
      n := n.(*ir.UnaryExpr)
      if !s.canSSA(n.X) {
              s.vars[memVar] = s.newValue1Apos(ssa.OpVarDef, types.TypeMem, n.X.(*ir.Name), s.mem(), false)
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case ir.OIF:
       n := n.(*ir.IfStmt)
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              if ir.BoolVal(n.Cond) {
                      s.stmtList (n.Body)
                      s.stmtList(n.Else)
              break
       bEnd := s.f.NewBlock(ssa.BlockPlain)
       var likely int8
       if n.Likely {
              likelv = 1
       var bThen *ssa.Block
       if len(n.Body) != 0 {
              bThen = s.f.NewBlock(ssa.BlockPlain)
       } else {
              bThen = bEnd
       var bElse *ssa.Block
       if len(n.Else) != 0 {
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       } else {
              bElse = bEnd
       s.condBranch (n.Cond, bThen, bElse, likely)
       if len(n.Body) != 0 {
              s.startBlock (bThen)
              s.stmtList(n.Body)
              if b := s.endBlock(); b != nil {
                      b.AddEdgeTo (bEnd)
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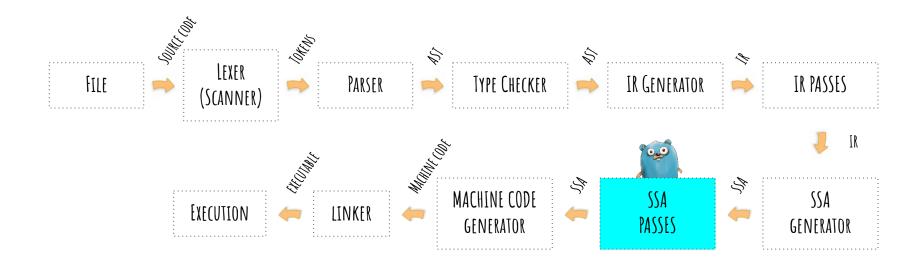
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case ir.OIF:
       n := n.(*ir.IfStmt)
       if ir.IsConst(n.Cond, constant.Bool) {
              s.stmtList(n.Cond.Init())
              if ir.BoolVal(n.Cond) {
                      s.stmtList (n.Body)
                      s.stmtList(n.Else)
              break
       bEnd := s.f.NewBlock(ssa.BlockPlain)
       var likely int8
       if n.Likely {
              likelv = 1
       var bThen *ssa.Block
       if len(n.Body) != 0 {
              bThen = s.f.NewBlock(ssa.BlockPlain)
       } else {
              bThen = bEnd
       var bElse *ssa.Block
       if len(n.Else) != 0 {
              bElse = s.f.NewBlock(ssa.BlockPlain)
       } else {
              bElse = bEnd
       s.condBranch (n.Cond, bThen, bElse, likely)
       if len(n.Body) != 0 {
              s.startBlock (bThen)
              s.stmtList(n.Body)
              if b := s.endBlock(); b != nil {
                      b.AddEdgeTo (bEnd)
       if len(n.Else) != 0 {
              s.startBlock (bElse)
              s.stmtList(n.Else)
              if b := s.endBlock(); b != nil {
                     b.AddEdgeTo (bEnd)
       s.startBlock (bEnd)
```

SSA PASSES



SSA PASSES

- deadcode
- shortcircuit
- cse
- lower
- and a lot more

SSA (BEFORE THE PASSES)

```
package main

import "fmt"

func main() {
    fmt.Println("hello world!")
}
```



```
b1:
        v1 (?) = InitMem <mem>
        v2 (?) = SP <uintptr>
        v3 (?) = SB < uintptr>
        v4 (?) = ConstInterface <any>
        v5 (?) = ArrayMake1 < [1] any> v4
        v6 (6) = VarDef < mem > {.autotmp 8} v1
        v7 (6) = LocalAddr <*[1]any> {.autotmp 8} v2 v6
        v8 (6) = Store < mem > {[1]any} v7 v5 v6
        v9 (6) = LocalAddr <*[1]any> {.autotmp 8} v2 v8
        v10 (?) = Addr <*uint8> {type.string} v3
        v11 (?) = Addr <*string> {main..stmp 0} v3
        v12 (6) = IMake < any > v10 v11
        v13 (6) = NilCheck <void> v9 v8
        v14 (?) = Const64 < int > [0] (fmt.n[int], fmt..autotmp 0[int])
        v15 (?) = Const64 < int > [1]
        v16 (6) = PtrIndex <*any> v9 v14
        v17 (6) = Store < mem > {any} v16 v12 v8
        v18 (6) = NilCheck < void > v9 v17
        v19 (6) = Copy <*any> v9
        v20 (6) = IsSliceInBounds <bool> v14 v15
        v25 (?) = ConstInterface <error> (fmt.err[error], fmt..autotmp 1[error])
        v28 (?) = Addr <*uint8> {go.itab.*os.File,io.Writer} v3
        v29 (?) = Addr <**os.File> {os.Stdout} v3
If v20 \rightarrow b2 \ b3 \ (likely) \ (6)
b2: ← b1
        v23 (6) = Sub64 < int > v15 v14
        v24 (6) = SliceMake <[]any> v19 v23 v23 (fmt.a[[]any])
        v26 (6) = Copy < mem > v17
        v27 (+6) = InlMark < void > [0] v26
        v30 (294) = Load <*os.File> v29 v26
        v31 (294) = IMake <io.Writer> v28 v30
        v32 (294) = StaticLECall <int,error,mem> {AuxCall{fmt.Fprintln}} [40] v31 v24 v26
        v33 (294) = SelectN < mem > [2] v32
        v34 (294) = SelectN < int > [0] v32
        v35 (294) = SelectN <int> [0] v32 (fmt.n[int], fmt..autotmp 0[int])
        v36 (294) = SelectN <error> [1] v32 (fmt.err[error], fmt..autotmp 1[error])
Plain \rightarrow b4 (+6)
b3: ← b1
        v21 (6) = Copy < mem > v17
        v22 (6) = PanicBounds <mem> [6] v14 v15 v21
Exit v22 (6)
b4: ← b2
        v38 (7) = Copy < mem > v33
        v37 (7) = MakeResult < mem > v38
Ret v37 (7)
name fmt.a[[]any]: v24
name fmt.n[int]: v14 v35
name fmt.err[error]: v25 v36
name fmt..autotmp 0[int]: v14 v35
name fmt..autotmp 1[error]: v25 v36
```

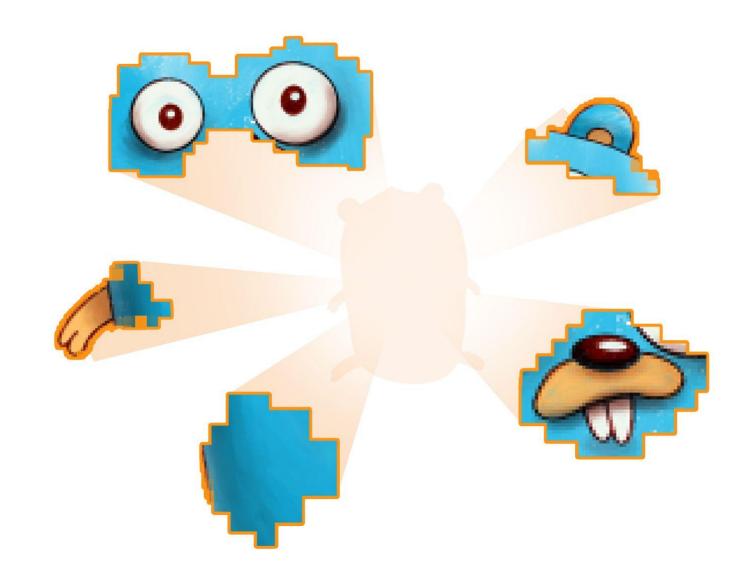
SSA (AFTER THE PASSES)

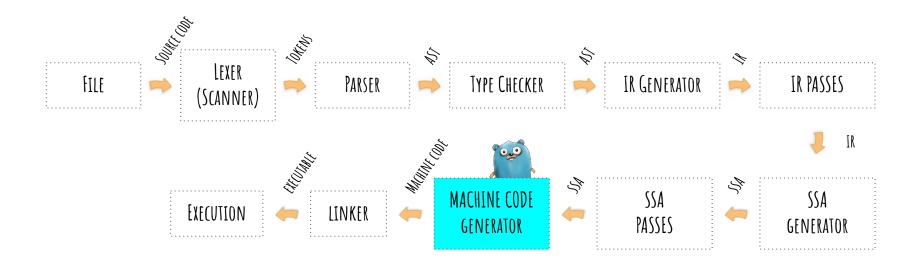
```
package main

import "fmt"

func main() {
    fmt.Println("hello world!")
}
```

```
b4:
        v1 (?) = InitMem < mem >
        v6 (6) = VarDef < mem > {.autotmp 8} v1
        v2 (?) = SP <uintptr> : SP
        v35 (6) = MOVOstoreconst <mem> {.autotmp 8} [val=0,off=0] v2 v6
        v3 (?) = SB <uintptr> : SB
        v14 (6) = LEAQ <*uint8> {type.string} v3 : DX
        v5 (+6) = MOVQstore < mem > {.autotmp 8} v2 v14 v35
        v20 (6) = LEAQ <*string> {main..stmp 0} v3 : DX
        v36 (+6) = MOVQstore < mem > {.autotmp 8} [8] v2 v20 v5
        v27 (+6) = InlMark < void > [0] v36
        v30 (+294) = MOVOload <*os.File> {os.Stdout} v3 v36 : BX
        v22 (294) = LEAQ <*uint8> {qo.itab.*os.File,io.Writer} v3 : AX
        v13 (294) = LEAQ < [1] any {.autotmp 8} v2 : CX
        v16 (294) = MOVQconst < int > [1] : DI
       v7 (294) = Copy < int > v16 : SI
        v32 (294) = CALLstatic <int, unsafe.Pointer, unsafe.Pointer, mem> {AuxCall{fmt.Fprintln}} [40]...
        v33 (294) = SelectN < mem > [3] v32
        v37 (+7) = MakeResult < mem > v33
Ret v37 (7)
name a.ptr[*any]: v9
name a.len[int]: v15
name a.cap[int]: v15
```





```
b4:
        v1 (?) = TnitMem <mem>
        v6 (6) = VarDef < mem > \{.autotmp 8\} v1
        v2 (?) = SP < uintptr > : SP
        v35 (6) = MOVOstoreconst < mem > \{.autotmp 8\} [val=0, off=0] v2 v6
        v3 (?) = SB < uintptr > : SB
        v14 (6) = LEAQ < uint8 > \{type.string\} v3 : DX
        v5 (+6) = MOVQstore < mem > {.autotmp 8} v2 v14 v35
        v20 (6) = LEAQ <*string> {main..stmp 0} v3 : DX
        v36 (+6) = MOVQstore < mem > {.autotmp 8} [8] v2 v20 v5
        v27 (+6) = InlMark < void > [0] v36
        v30 (+294) = MOVQload <*os.File> {os.Stdout} v3 v36 : BX
        v22 (294) = LEAQ <*uint8> {go.itab.*os.File,io.Writer} v3 : Ax
        v13 (294) = LEAQ < *[1] any > {.autotmp 8} v2 : CX
        v16 (294) = MOVQconst < int > [1] : DI
        v7 (294) = Copy < int > v16 : SI
        v32 (294) = CALLstatic <int, unsafe.Pointer, unsafe.Pointer, mem>
{AuxCall{fmt.Fprintln}} [40]...
        v33 (294) = SelectN < mem > [3] v32
        v37 (+7) = MakeResult < mem > v33
Ret v37 (7)
name a.ptr[*any]: v9
name a.len[int]: v15
name a.cap[int]: v15
```

```
# /hello.go
       00000 (5) TEXT main.main(SB), ABIInternal
       00001 (5) FUNCDATA $0, qclocals \cdot q2BeySu+wFnoycqXfElmcq==(SB)
       00002 (5) FUNCDATA $1, qclocals EaPwxsZ75yY1hHMVZLmk6q=(SB)
       00003 (5) FUNCDATA $2, main.main.stkobj(SB)
       00004 (+6) MOVUPS X15, main..autotmp 8-16(SP)
v35
v14
       00005 (6) LEAQ type.string(SB), DX
v_5
       00006 (6) MOVQ DX, main..autotmp 8-16(SP)
       00007 (6) LEAQ main..stmp 0(SB), DX
v20
v36
       00008 (6) MOVQ DX, main..autotmp 8-8(SP)
v27
       00009 (?) NOP
       # $GOROOT/src/fmt/print.go
v30
       00010 (+294) MOVQ os.Stdout(SB), BX
v22
       00011 (294) LEAQ go.itab.*os.File,io.Writer(SB), AX
       00012 (294) LEAQ main..autotmp 8-16(SP), CX
v13
v16
       00013 (294) MOVL $1, DI
v7
       00014 (294) MOVQ DI, SI
v32
       00015 (294) PCDATA $1, $0
       00016 (294) CALL fmt.Fprintln(SB)
v32
       # /home/jespino/Projects/Github/go/hello.go
b4
       00017 (7) RET
       00018 (?) END
```

```
b4:
        v1 (?) = InitMem <mem>
        v6 (6) = VarDef < mem > \{.autotmp 8\} v1
        v2 (?) = SP < uintptr > : SP
        v35 (6) = MOVOstoreconst < mem > \{.autotmp 8\} [val=0, off=0] v2 v6
        v3 (?) = SB < uintptr > : SB
        v14 (6) = LEAQ < uint8 > \{type.string\} v3 : DX
        v5 (+6) = MOVQstore < mem > {.autotmp 8} v2 v14 v35
        v20 (6) = LEAQ <*string> {main..stmp 0} v3 : DX
        v36 (+6) = MOVQstore < mem > {.autotmp 8} [8] v2 v20 v5
        v27 (+6) = InlMark < void > [0] v36
        v30 (+294) = MOVQload <*os.File> {os.Stdout} v3 v36 : BX
        v22 (294) = LEAQ <*uint8> {go.itab.*os.File,io.Writer} v3 : Ax
        v13 (294) = LEAQ < *[1] any > {.autotmp 8} v2 : CX
        v16 (294) = MOVQconst < int > [1] : DI
        v7 (294) = Copy < int > v16 : SI
        v32 (294) = CALLstatic <int, unsafe.Pointer, unsafe.Pointer, mem>
{AuxCall{fmt.Fprintln}} [40]...
        v33 (294) = SelectN < mem > [3] v32
        v37 (+7) = MakeResult < mem > v33
Ret v37 (7)
name a.ptr[*any]: v9
name a.len[int]: v15
name a.cap[int]: v15
```

```
# /hello.go
       00000 (5) TEXT main.main(SB), ABIInternal
       00001 (5) FUNCDATA $0, qclocals \cdot q2BeySu+wFnoycqXfElmcq==(SB)
       00002 (5) FUNCDATA $1, qclocals EaPwxsZ75yY1hHMVZLmk6&=(SB)
       00003 (5) FUNCDATA $2, main.main.stkobj(SB)
       00004 (+6) MOVUPS X15, main..autotmp 8-16(SP)
v35
v14
       00005 (6) LEAQ type.string(SB), DX
v_5
       00006 (6) MOVQ DX, main..autotmp 8-16(SP)
       00007 (6) LEAQ main..stmp 0(SB), DX
v20
       00008 (6) MOVQ DX, main..autotmp 8-8(SP)
v36
v27
       00009 (?) NOP
       # $GOROOT/src/fmt/print.go
v30
       00010 (+294) MOVQ os.Stdout(SB), BX
v22
       00011 (294) LEAQ go.itab.*os.File,io.Writer(SB), AX
       00012 (294) LEAQ main..autotmp 8-16(SP), CX
v13
v16
       00013 (294) MOVL $1, DI
v7
       00014 (294) MOVQ DI, SI
v32
       00015 (294) PCDATA $1, $0
       00016 (294) CALL fmt.Fprintln(SB)
v32
       # /home/jespino/Projects/Github/go/hello.go
b4
       00017 (7) RET
       00018 (?) END
```

```
case ssa.OpAMD64MOVQload, ssa.OpAMD64MOVLload, ssa.OpAMD64MOVWload, ssa.OpAMD64MOVBload, ssa.OpAMD64MOVOload,
     ssa.OpAMD64MOVSSload, ssa.OpAMD64MOVSDload, ssa.OpAMD64MOVBQSXload, ssa.OpAMD64MOVWQSXload, ssa.OpAMD64MOVLQSXload,
     ssa.OpAMD64MOVBEQload, ssa.OpAMD64MOVBELload:
     p := s.Prog(v.Op.Asm())
    p.From.Type = obj.TYPE MEM
     p.From.Reg = v.Args[0].Reg()
                                                                                  // Call returns a new CALL instruction for the SSA value v.
     ssagen.AddAux(&p.From, v)
                                                                                  // It uses PrepareCall to prepare the call.
     p.To.Type = obj.TYPE REG
                                                                                  func (s *State) Call(v *ssa.Value) *obj.Prog {
     p.To.Req = v.Req()
                                                                                      pPosIsStmt := s.pp.Pos.IsStmt() // The statement-ness fo the call comes from ssaGenState
                                                                                      s.PrepareCall(v)
case ssa.OpAMD64CALLstatic, ssa.OpAMD64CALLtail:
                                                                                      p := s.Prog(obj.ACALL)
     if s.ABI == obj.ABI0 && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIInternal {
                                                                                      if pPosIsStmt == src.PosIsStmt {
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          p.Pos = v.Pos.WithIsStmt()
         if buildcfq.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                      } else {
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          p.Pos = v.Pos.WithNotStmt()
         // set G register from TLS
                                                                                      if sym, ok := v.Aux.(*ssa.AuxCall); ok && sym.Fn != nil {
        getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Type = obj.TYPE MEM
                                                                                          p.To.Name = obj.NAME EXTERN
     if v.Op == ssa.OpAMD64CALLtail {
                                                                                          p.To.Sym = sym.Fn
         s.TailCall(v)
                                                                                      } else {
         break
                                                                                          // TODO (mdempsky): Can these differences be eliminated?
                                                                                          switch Arch.LinkArch.Family {
     s.Call(v)
                                                                                          case sys.AMD64, sys.I386, sys.PPC64, sys.RISCV64, sys.S390X, sys.Wasm:
     if s.ABI == obj.ABIInternal && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIO {
                                                                                              p.To.Type = obj.TYPE REG
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          case sys.ARM, sys.ARM64, sys.Loong64, sys.MIPS, sys.MIPS64:
        if buildcfg.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                              p.To.Type = obj.TYPE MEM
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          default:
                                                                                              base.Fatalf("unknown indirect call family")
        // set G register from TLS
         getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Reg = v.Args[0].Reg()
                                                                                      return p
```

```
case ssa.OpAMD64MOVQload, ssa.OpAMD64MOVLload, ssa.OpAMD64MOVWload, ssa.OpAMD64MOVBload, ssa.OpAMD64MOVOload,
     ssa.OpAMD64MOVSSload, ssa.OpAMD64MOVSDload, ssa.OpAMD64MOVBQSXload, ssa.OpAMD64MOVWQSXload, ssa.OpAMD64MOVLQSXload,
     ssa.OpAMD64MOVBEQload, ssa.OpAMD64MOVBELload:
     p := s.Prog(v.Op.Asm())
    p.From.Type = obj.TYPE MEM
     p.From.Reg = v.Args[0].Reg()
                                                                                  // Call returns a new CALL instruction for the SSA value v.
     ssagen.AddAux(&p.From, v)
                                                                                  // It uses PrepareCall to prepare the call.
     p.To.Type = obj.TYPE REG
                                                                                  func (s *State) Call(v *ssa.Value) *obj.Prog {
     p.To.Req = v.Req()
                                                                                      pPosIsStmt := s.pp.Pos.IsStmt() // The statement-ness fo the call comes from ssaGenState
                                                                                      s.PrepareCall(v)
case ssa.OpAMD64CALLstatic, ssa.OpAMD64CALLtail:
                                                                                      p := s.Prog(obj.ACALL)
     if s.ABI == obj.ABI0 && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIInternal {
                                                                                      if pPosIsStmt == src.PosIsStmt {
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          p.Pos = v.Pos.WithIsStmt()
         if buildcfq.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                      } else {
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          p.Pos = v.Pos.WithNotStmt()
         // set G register from TLS
                                                                                      if sym, ok := v.Aux.(*ssa.AuxCall); ok && sym.Fn != nil {
        getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Type = obj.TYPE MEM
                                                                                          p.To.Name = obj.NAME EXTERN
     if v.Op == ssa.OpAMD64CALLtail {
                                                                                          p.To.Sym = sym.Fn
         s.TailCall(v)
                                                                                      } else {
         break
                                                                                          // TODO (mdempsky): Can these differences be eliminated?
                                                                                          switch Arch.LinkArch.Family {
     s.Call(v)
                                                                                          case sys.AMD64, sys.I386, sys.PPC64, sys.RISCV64, sys.S390X, sys.Wasm:
     if s.ABI == obj.ABIInternal && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIO {
                                                                                              p.To.Type = obj.TYPE REG
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          case sys.ARM, sys.ARM64, sys.Loong64, sys.MIPS, sys.MIPS64:
        if buildcfg.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                              p.To.Type = obj.TYPE MEM
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          default:
                                                                                              base.Fatalf("unknown indirect call family")
        // set G register from TLS
         getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Reg = v.Args[0].Reg()
                                                                                      return p
```

```
case ssa.OpAMD64MOVQload, ssa.OpAMD64MOVLload, ssa.OpAMD64MOVWload, ssa.OpAMD64MOVBload, ssa.OpAMD64MOVOload,
     ssa.OpAMD64MOVSSload, ssa.OpAMD64MOVSDload, ssa.OpAMD64MOVBQSXload, ssa.OpAMD64MOVWQSXload, ssa.OpAMD64MOVLQSXload,
     ssa.OpAMD64MOVBEQload, ssa.OpAMD64MOVBELload:
     p := s.Prog(v.Op.Asm())
     p.From.Type = obj.TYPE MEM
     p.From.Reg = v.Args[0].Reg()
                                                                                  // Call returns a new CALL instruction for the SSA value v.
     ssagen.AddAux(&p.From, v)
                                                                                  // It uses PrepareCall to prepare the call.
     p.To.Type = obj.TYPE REG
                                                                                  func (s *State) Call(v *ssa.Value) *obj.Prog {
     p.To.Reg = v.Reg()
                                                                                      pPosIsStmt := s.pp.Pos.IsStmt() // The statement-ness fo the call comes from ssaGenState
                                                                                      s.PrepareCall(v)
case ssa.OpAMD64CALLstatic, ssa.OpAMD64CALLtail:
                                                                                      p := s.Prog(obj.ACALL)
     if s.ABI == obj.ABIO && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIInternal {
                                                                                      if pPosIsStmt == src.PosIsStmt {
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          p.Pos = v.Pos.WithIsStmt()
         if buildcfq.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                      } else {
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          p.Pos = v.Pos.WithNotStmt()
         // set G register from TLS
                                                                                      if sym, ok := v.Aux.(*ssa.AuxCall); ok && sym.Fn != nil {
        getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Type = obj.TYPE MEM
                                                                                          p.To.Name = obj.NAME EXTERN
     if v.Op == ssa.OpAMD64CALLtail {
                                                                                          p.To.Sym = sym.Fn
         s.TailCall(v)
                                                                                      } else {
         break
                                                                                          // TODO (mdempsky): Can these differences be eliminated?
                                                                                          switch Arch.LinkArch.Family {
     s.Call(v)
                                                                                          case sys.AMD64, sys.I386, sys.PPC64, sys.RISCV64, sys.S390X, sys.Wasm:
     if s.ABI == obj.ABIInternal && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIO {
                                                                                              p.To.Type = obj.TYPE REG
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          case sys.ARM, sys.ARM64, sys.Loong64, sys.MIPS, sys.MIPS64:
        if buildcfg.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                              p.To.Type = obj.TYPE MEM
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          default:
                                                                                              base.Fatalf("unknown indirect call family")
        // set G register from TLS
         getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Reg = v.Args[0].Reg()
                                                                                      return p
```

```
case ssa.OpAMD64MOVQload, ssa.OpAMD64MOVLload, ssa.OpAMD64MOVWload, ssa.OpAMD64MOVBload, ssa.OpAMD64MOVOload,
     ssa.OpAMD64MOVSSload, ssa.OpAMD64MOVSDload, ssa.OpAMD64MOVBQSXload, ssa.OpAMD64MOVWQSXload, ssa.OpAMD64MOVLQSXload,
     ssa.OpAMD64MOVBEQload, ssa.OpAMD64MOVBELload:
     p := s.Prog(v.Op.Asm())
    p.From.Type = obj.TYPE MEM
     p.From.Reg = v.Args[0].Reg()
                                                                                  // Call returns a new CALL instruction for the SSA value v.
     ssagen.AddAux(&p.From, v)
                                                                                  // It uses PrepareCall to prepare the call.
     p.To.Type = obj.TYPE REG
                                                                                  func (s *State) Call(v *ssa.Value) *obj.Prog {
     p.To.Req = v.Req()
                                                                                      pPosIsStmt := s.pp.Pos.IsStmt() // The statement-ness fo the call comes from ssaGenState
                                                                                      s.PrepareCall(v)
case ssa.OpAMD64CALLstatic, ssa.OpAMD64CALLtail:
                                                                                      p := s.Prog(obj.ACALL)
     if s.ABI == obj.ABI0 && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIInternal {
                                                                                      if pPosIsStmt == src.PosIsStmt {
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          p.Pos = v.Pos.WithIsStmt()
         if buildcfq.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                      } else {
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          p.Pos = v.Pos.WithNotStmt()
         // set G register from TLS
                                                                                      if sym, ok := v.Aux.(*ssa.AuxCall); ok && sym.Fn != nil {
        getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Type = obj.TYPE MEM
                                                                                          p.To.Name = obj.NAME EXTERN
     if v.Op == ssa.OpAMD64CALLtail {
                                                                                          p.To.Sym = sym.Fn
         s.TailCall(v)
                                                                                      } else {
         break
                                                                                          // TODO (mdempsky): Can these differences be eliminated?
                                                                                          switch Arch.LinkArch.Family {
     s.Call(v)
                                                                                          case sys.AMD64, sys.I386, sys.PPC64, sys.RISCV64, sys.S390X, sys.Wasm:
     if s.ABI == obj.ABIInternal && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIO {
                                                                                              p.To.Type = obj.TYPE REG
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          case sys.ARM, sys.ARM64, sys.Loong64, sys.MIPS, sys.MIPS64:
        if buildcfg.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                              p.To.Type = obj.TYPE MEM
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          default:
                                                                                              base.Fatalf("unknown indirect call family")
        // set G register from TLS
         getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Reg = v.Args[0].Reg()
                                                                                      return p
```

```
case ssa.OpAMD64MOVQload, ssa.OpAMD64MOVLload, ssa.OpAMD64MOVWload, ssa.OpAMD64MOVBload, ssa.OpAMD64MOVOload,
     ssa.OpAMD64MOVSSload, ssa.OpAMD64MOVSDload, ssa.OpAMD64MOVBQSXload, ssa.OpAMD64MOVWQSXload, ssa.OpAMD64MOVLQSXload,
     ssa.OpAMD64MOVBEQload, ssa.OpAMD64MOVBELload:
     p := s.Prog(v.Op.Asm())
    p.From.Type = obj.TYPE MEM
     p.From.Reg = v.Args[0].Reg()
                                                                                  // Call returns a new CALL instruction for the SSA value v.
     ssagen.AddAux(&p.From, v)
                                                                                  // It uses PrepareCall to prepare the call.
     p.To.Type = obj.TYPE REG
                                                                                  func (s *State) Call(v *ssa.Value) *obj.Prog {
     p.To.Req = v.Req()
                                                                                      pPosIsStmt := s.pp.Pos.IsStmt() // The statement-ness fo the call comes from ssaGenState
                                                                                      s.PrepareCall(v)
case ssa.OpAMD64CALLstatic, ssa.OpAMD64CALLtail:
                                                                                      p := s.Prog(obj.ACALL)
     if s.ABI == obj.ABIO && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIInternal {
                                                                                      if pPosIsStmt == src.PosIsStmt {
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          p.Pos = v.Pos.WithIsStmt()
         if buildcfq.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                      } else {
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          p.Pos = v.Pos.WithNotStmt()
         // set G register from TLS
                                                                                      if sym, ok := v.Aux.(*ssa.AuxCall); ok && sym.Fn != nil {
        getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Type = obj.TYPE MEM
      f v.Op == ssa.OpAMD64CALLtail {
                                                                                          p.To.Name = obj.NAME EXTERN
                                                                                          p.To.Sym = sym.Fn
        s.TailCall(v)
                                                                                      } else {
         break
                                                                                          // TODO (mdempsky): Can these differences be eliminated?
                                                                                          switch Arch.LinkArch.Family {
     s.Call(v)
                                                                                          case sys.AMD64, sys.I386, sys.PPC64, sys.RISCV64, sys.S390X, sys.Wasm:
     if s.ABI == obj.ABIInternal && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIO {
                                                                                              p.To.Type = obj.TYPE REG
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          case sys.ARM, sys.ARM64, sys.Loong64, sys.MIPS, sys.MIPS64:
        if buildcfg.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                              p.To.Type = obj.TYPE MEM
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          default:
                                                                                              base.Fatalf("unknown indirect call family")
        // set G register from TLS
         getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Reg = v.Args[0].Reg()
                                                                                      return p
```

```
case ssa.OpAMD64MOVQload, ssa.OpAMD64MOVLload, ssa.OpAMD64MOVWload, ssa.OpAMD64MOVBload, ssa.OpAMD64MOVOload,
     ssa.OpAMD64MOVSSload, ssa.OpAMD64MOVSDload, ssa.OpAMD64MOVBQSXload, ssa.OpAMD64MOVWQSXload, ssa.OpAMD64MOVLQSXload,
     ssa.OpAMD64MOVBEQload, ssa.OpAMD64MOVBELload:
     p := s.Prog(v.Op.Asm())
    p.From.Type = obj.TYPE MEM
     p.From.Reg = v.Args[0].Reg()
                                                                                  // Call returns a new CALL instruction for the SSA value v.
     ssagen.AddAux(&p.From, v)
                                                                                  // It uses PrepareCall to prepare the call.
     p.To.Type = obj.TYPE REG
                                                                                  func (s *State) Call(v *ssa.Value) *obj.Prog {
     p.To.Req = v.Req()
                                                                                      pPosIsStmt := s.pp.Pos.IsStmt() // The statement-ness fo the call comes from ssaGenState
                                                                                      s.PrepareCall(v)
case ssa.OpAMD64CALLstatic, ssa.OpAMD64CALLtail:
                                                                                      p := s.Prog(obj.ACALL)
     if s.ABI == obj.ABI0 && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIInternal {
                                                                                      if pPosIsStmt == src.PosIsStmt {
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          p.Pos = v.Pos.WithIsStmt()
         if buildcfq.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                      } else {
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          p.Pos = v.Pos.WithNotStmt()
         // set G register from TLS
                                                                                      if sym, ok := v.Aux.(*ssa.AuxCall); ok && sym.Fn != nil {
        getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Type = obj.TYPE MEM
                                                                                          p.To.Name = obj.NAME EXTERN
     if v.Op == ssa.OpAMD64CALLtail {
                                                                                          p.To.Sym = sym.Fn
         s.TailCall(v)
                                                                                      } else {
         break
                                                                                          // TODO (mdempsky): Can these differences be eliminated?
                                                                                          switch Arch.LinkArch.Family {
     s.Call(v)
                                                                                          case sys.AMD64, sys.I386, sys.PPC64, sys.RISCV64, sys.S390X, sys.Wasm:
     if s.ABI == obj.ABIInternal && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIO {
                                                                                              p.To.Type = obj.TYPE REG
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          case sys.ARM, sys.ARM64, sys.Loong64, sys.MIPS, sys.MIPS64:
        if buildcfg.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                              p.To.Type = obj.TYPE MEM
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          default:
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        // set G register from TLS
         getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Reg = v.Args[0].Reg()
                                                                                      return p
```

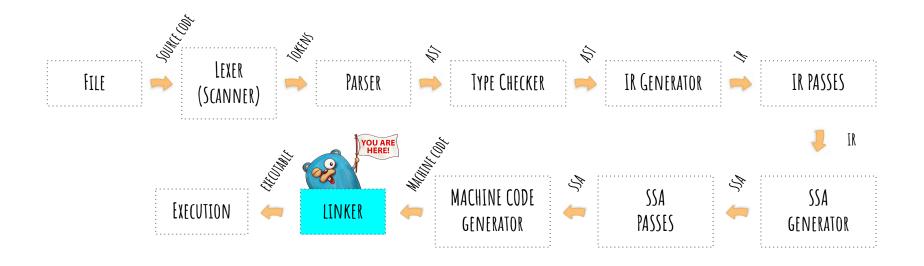
```
case ssa.OpAMD64MOVQload, ssa.OpAMD64MOVLload, ssa.OpAMD64MOVWload, ssa.OpAMD64MOVBload, ssa.OpAMD64MOVOload,
     ssa.OpAMD64MOVSSload, ssa.OpAMD64MOVSDload, ssa.OpAMD64MOVBQSXload, ssa.OpAMD64MOVWQSXload, ssa.OpAMD64MOVLQSXload,
     ssa.OpAMD64MOVBEQload, ssa.OpAMD64MOVBELload:
     p := s.Prog(v.Op.Asm())
    p.From.Type = obj.TYPE MEM
     p.From.Reg = v.Args[0].Reg()
                                                                                  // Call returns a new CALL instruction for the SSA value v.
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                                                                                  func (s *State) Call(v *ssa.Value) *obj.Prog {
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                                                                                      pPosIsStmt := s.pp.Pos.IsStmt() // The statement-ness fo the call comes from ssaGenState
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case ssa.OpAMD64CALLstatic, ssa.OpAMD64CALLtail:
                                                                                      p := s.Prog(obj.ACALL)
     if s.ABI == obj.ABIO && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIInternal {
                                                                                      if pPosIsStmt == src.PosIsStmt {
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          p.Pos = v.Pos.WithIsStmt()
         if buildcfq.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                      } else {
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          p.Pos = v.Pos.WithNotStmt()
         // set G register from TLS
                                                                                      if sym, ok := v.Aux.(*ssa.AuxCall); ok && sym.Fn != nil {
        getgFromTLS(s, x86.REG R14)
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                                                                                          p.To.Name = obj.NAME EXTERN
     if v.Op == ssa.OpAMD64CALLtail {
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         s.TailCall(v)
                                                                                      } else {
         break
                                                                                          // TODO (mdempsky): Can these differences be eliminated?
                                                                                          switch Arch.LinkArch.Family {
     s.Call(v)
                                                                                          case sys.AMD64, sys.I386, sys.PPC64, sys.RISCV64, sys.S390X, sys.Wasm:
     if s.ABI == obj.ABIInternal && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIO {
                                                                                              p.To.Type = obj.TYPE REG
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          case sys.ARM, sys.ARM64, sys.Loong64, sys.MIPS, sys.MIPS64:
        if buildcfg.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                              p.To.Type = obj.TYPE MEM
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
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        // set G register from TLS
         getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Reg = v.Args[0].Reg()
                                                                                      return p
```

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     ssa.OpAMD64MOVSSload, ssa.OpAMD64MOVSDload, ssa.OpAMD64MOVBQSXload, ssa.OpAMD64MOVWQSXload, ssa.OpAMD64MOVLQSXload,
     ssa.OpAMD64MOVBEQload, ssa.OpAMD64MOVBELload:
     p := s.Prog(v.Op.Asm())
    p.From.Type = obj.TYPE MEM
     p.From.Reg = v.Args[0].Reg()
                                                                                  // Call returns a new CALL instruction for the SSA value v.
     ssagen.AddAux(&p.From, v)
                                                                                  // It uses PrepareCall to prepare the call.
     p.To.Type = obj.TYPE REG
                                                                                  func (s *State) Call(v *ssa.Value) *obj.Prog {
     p.To.Req = v.Req()
                                                                                      pPosIsStmt := s.pp.Pos.IsStmt() // The statement-ness fo the call comes from ssaGenState
                                                                                      s.PrepareCall(v)
case ssa.OpAMD64CALLstatic, ssa.OpAMD64CALLtail:
                                                                                      p := s.Prog(obj.ACALL)
     if s.ABI == obj.ABI0 && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABIInternal {
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         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          p.Pos = v.Pos.WithIsStmt()
         if buildcfq.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                      } else {
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                          p.Pos = v.Pos.WithNotStmt()
         // set G register from TLS
                                                                                      if sym, ok := v.Aux.(*ssa.AuxCall); ok && sym.Fn != nil {
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                                                                                          p.To.Type = obj.TYPE MEM
                                                                                          p.To.Name = obj.NAME EXTERN
     if v.Op == ssa.OpAMD64CALLtail {
                                                                                          p.To.Sym = sym.Fn
         s.TailCall(v)
                                                                                      } else {
         break
                                                                                          // TODO (mdempsky): Can these differences be eliminated?
                                                                                          switch Arch.LinkArch.Family {
     s.Call(v)
                                                                                          case sys.AMD64, sys.I386, sys.PPC64, sys.RISCV64, sys.S390X, sys.Wasm:
     if s.ABI == obj.ABIInternal && v.Aux.(*ssa.AuxCall).Fn.ABI() == obj.ABI0 {
                                                                                              p.To.Type = obj.TYPE REG
         // zeroing X15 when entering ABIInternal from ABIO
                                                                                          case sys.ARM, sys.ARM64, sys.Loong64, sys.MIPS, sys.MIPS64:
        if buildcfg.GOOS != "plan9" { // do not use SSE on Plan 9
                                                                                              p.To.Type = obj.TYPE MEM
             opregreg(s, x86.AXORPS, x86.REG X15, x86.REG X15)
                                                                                           default:
                                                                                              base.Fatalf("unknown indirect call family")
        // set G register from TLS
         getgFromTLS(s, x86.REG R14)
                                                                                          p.To.Reg = v.Args[0].Reg()
                                                                                      return p
```

LINKING



LINKING



THE RUNTIME



- Maps, slices, channels, goroutines...
- Memory management
- The scheduler
- The startup



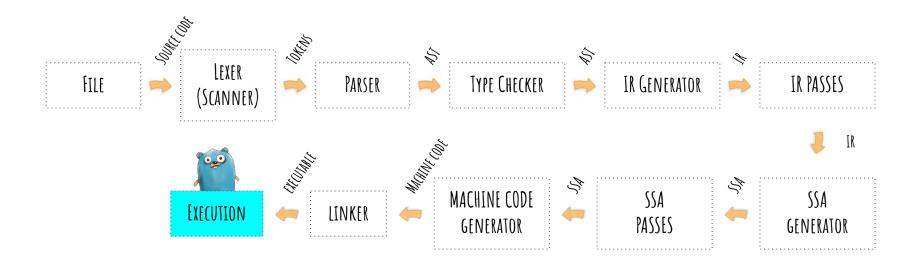
THE KERNEL AND THE STDOUT







THE KERNEL AND THE STDOUT

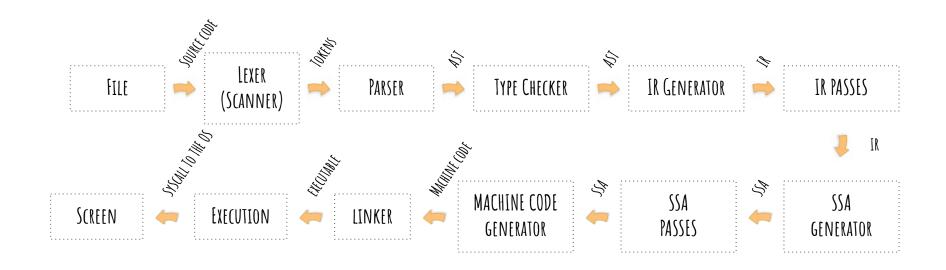


PRINTING TO THE SCREEN

PRINTING TO THE SCREEN

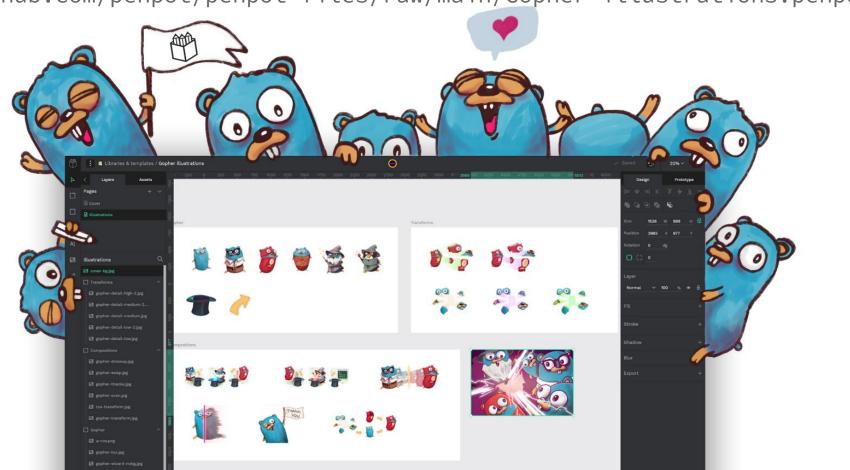


SUMMARY



THE ILLUSTRATIONS OF THE TALK

- Made by Juan de la Cruz for this talk
- Creative Commons 0 (Use it however you want)
- Downloadable in Penpot (Open Source Design tool) format
- https://github.com/penpot/penpot-files/raw/main/Gopher-illustrations.penpot





REFERENCES

- The compile command README: https://go.dev/src/cmd/compile/README
- The SSA README: https://go.dev/src/cmd/compile/internal/ssa/README
- https://go.dev/doc/asm
- https://pkg.go.dev/runtime
- SSA Talks:
 - o https://www.youtube.com/watch?v=D2-gaMvWfQY
 - o https://www.youtube.com/watch?v=uTMvKVma5ms
- Go Assembler: https://www.youtube.com/watch?v=KINIAgRpkDA



