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Performance Summary Table

	Design metric	Performance	Specification
Input Matching	Worst case S11 in the specified band	-10.26 dB	< −10dB
	Band over which S11 < −10dB	1.90 GHz - 2.1 GHz	1.9GHz to 2.1GHz
	Band over which S11 < −15dB	1.94 Ghz - 2.05 GHz	-
Voltage Gain	Minimum Gain in the specified band	29.255 dB	> 20dB
	Maximum Gain in the specified band	32.59 dB	> 20dB
	Gain flatness in specified band [Max-Min Gain]	3.31 dB	< 3dB
	3dB Bandwidth	253.3 MHz	> 200M Hz
	Load Capacitance [Differential]	1 pF	1p F
Noise Figure	Maximum Noise Figure in the specified band	1.259 dB	< 1.5dB
	Minimum Noise Figure in the specified band	1.296 dB	-
	Band over which N F < 1.5dB	1.9 Ghz – 2.1 GHz	1.9GHz to 2.1GHz
Linearity	IIP3 Tones used	1.9995 – 2.0005 GHz	-
	Input power used for extrapolation	-90 dBm	-
	Power of Fundamental Tone at output (at chosen input power)	-69.19 dBm	-
	Power of IM3 Tone at output (at chosen input power)	-235.66 dBm	-
	Extrapolated IIP3	-6.67 dBm	> −10dBm
Power	LNA DC power consumption [Excluding Bias]	4.8331 mW	Minimize
	Bias circuit power consumption	97 uW	Minimize
Other	Sum of all on-chip inductances	7.12 nH	-
	Sum of all off-chip inductances	50.9 nH	-
	Sum of all resistances [Including bias]	21.2 k ohms	-
	Sum of all capacitances [Including AC coupling, excluding load]	0.2nF	-
	Simulator Used	Eldo	-