Table 1: Power Amplifier Performance Summary Table

K.R.Srinivas EE18B136

	Design Metric	Performance	Specification
Output P1dB	fo = 1.9 GHz	10.193 dBm	≥ 10 dBm
	fo = 2.0 GHz	10.324 dBm	≥ 10 dBm
	fo = 2.1 GHz	10.605 dBm	≥ 10 dBm
AM-PM deviation at P1dB	fo = 1.9 GHz	0.606°	≤ 5 degrees
	fo = 2.0 GHz	0.507°	≤ 5 degrees
	fo = 2.1 GHz	0.264	≤ 5 degrees
Voltage-gain from gate to drain	fo = 1.9 GHz	2.50	≥ 2
	fo = 2.0 GHz	2.53	≥ 2
	fo = 2.1 GHz	2.56	≥ 2
Power	PA average power consumption [Excluding Bias]	35.78 mW	-
	Bias circuit power consumption	0.60 mW	Minimize
Other	Sum of all capacitances [Including AC coupling]	20 nF	-
	Inductance used	5 nH	-
	Simulator Used	Eldo	-