

Table 1: Power Amplifier Performance Summary Table
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	Design Metric	Performance	Specification
Output P1dB	$f_o = 1.9$ GHz	10.193 dBm	≥ 10 dBm
	$f_o = 2.0$ GHz	10.324 dBm	≥ 10 dBm
	$f_o = 2.1$ GHz	10.605 dBm	≥ 10 dBm
AM-PM deviation at P1dB	$f_o = 1.9$ GHz	5.4°	≤ 5 degrees
	$f_o = 2.0$ GHz	5.3°	≤ 5 degrees
	$f_o = 2.1$ GHz	5.25°	≤ 5 degrees
Voltage-gain from gate to drain	$f_o = 1.9$ GHz	2.50	≥ 2
	$f_o = 2.0$ GHz	2.53	≥ 2
	$f_o = 2.1$ GHz	2.56	≥ 2
Power	PA average power consumption [Excluding Bias]	35.78 mW	-
	Bias circuit power consumption	0.60 mW	Minimize
Other	Sum of all capacitances [Including AC coupling]	20 nF	-
	Inductance used	5 nH	-
	Simulator Used	Eldo	-