Alarm Claock Using FPGA

Shreeshan S
EE19MTECH01007
Srinivasulu N
EE18MTECH11003

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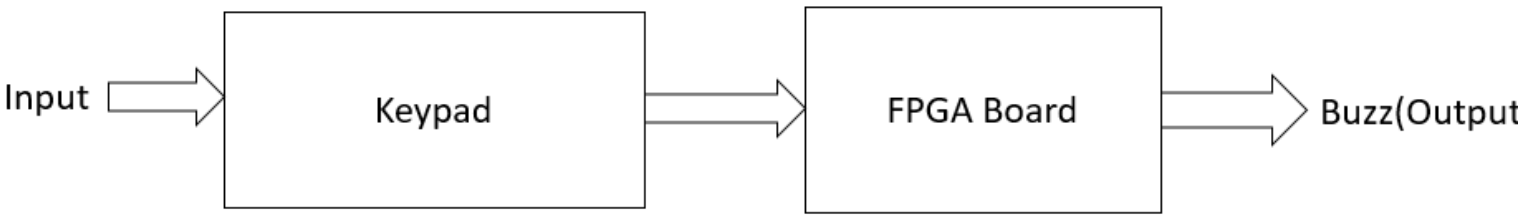
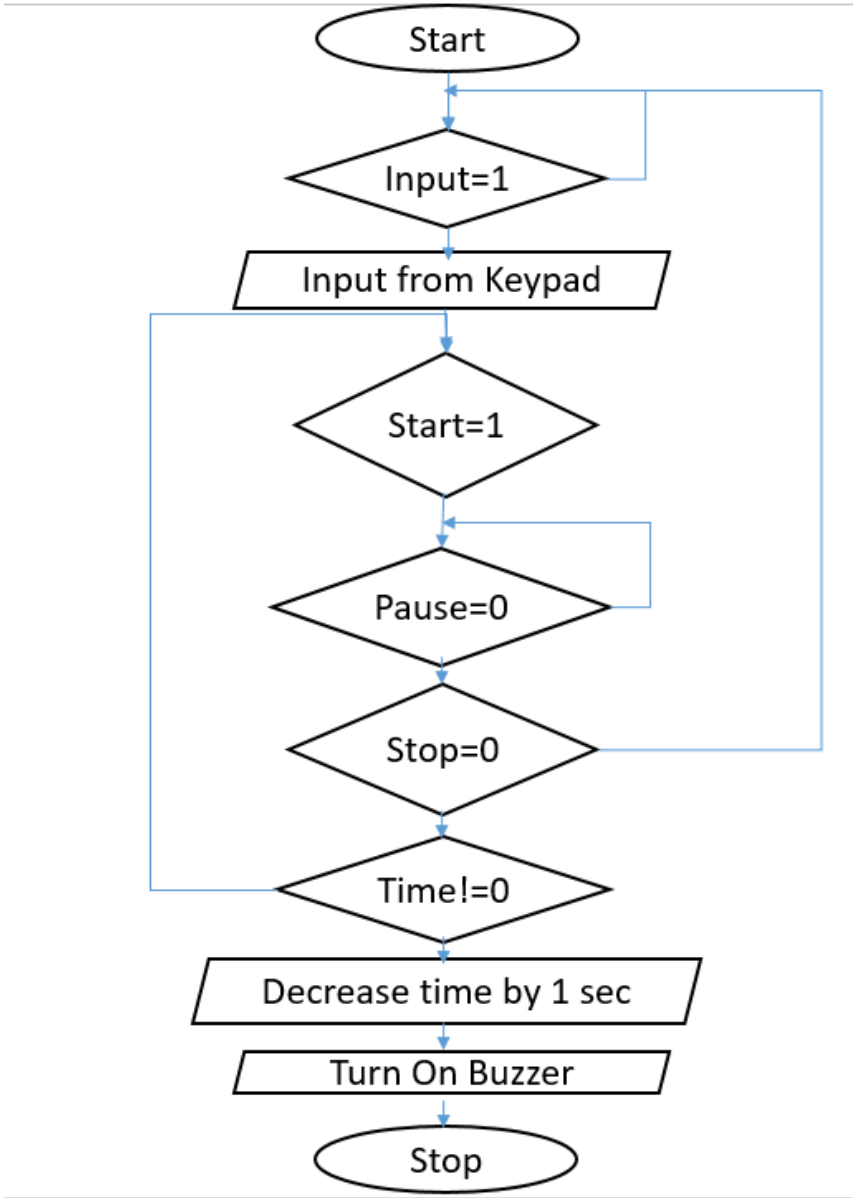
Overview


Input
Keypad
FPGA Board
Buzz(Output

1
2
3
F1
4
5
6
F2
7
8
9
F3
START
0
STOP
F4

Flowchart


Start
Input=1
Input from Keypad
Start=1
Pause=0
Stop=0
Time!=0
Decrease time by 1 sec
Turn On Buzzer
Stop