GENERAL POSSIBLE OPTIMIZATIONS IN C:

1. Loop unrolling
2. Avoid calculations in loop - **Most compilers are good** **at identifying** loop invariant computations. //makes it a must-implement case in an automated source code optimization tool then?
3. Avoid pointer dereference in loop - Better assign it to some temporary variable and use the temporary variable in the loop.
4. Use Register variables as counters of inner loops - Variables stored in registers can be accessed much faster than variables stored in memory.
5. Loop jamming - combine adjacent loops which loop over the same range of the same variable
6. Loop inversion - Assuming the loop is insensitive to direction. Some machines have a special instruction for decrement and compare with 0.
7. Mathematical optimizations
   1. Avoid integer division where possible because it is slow
   2. Use shift operators instead of \* and / for powers of 2
   3. Strength reduction - Use an alternative cheaper operator than a costly one where possible
8. Switch instead of if-else when possible, especially when case labels can be contiguous because that creates a jump table which is fast.
9. Prefer int to char or short - C performs all operations of char with an integer. In all operations like passing char to a function or an arithmetic operation, the first char will be converted into integer and after compilation of operation, it will again be converted into char. For a single char, this may not affect the efficiency but suppose the same operation is performed 100 times in a loop then it can decrease the efficiency of the program. //is this useful info to us?
10. Short-circuit evaluation
11. Dead code evaluation

<https://www.geeksforgeeks.org/basic-code-optimizations-in-c/>

<http://icps.u-strasbg.fr/~bastoul/local_copies/lee.html>

REFERENCE PAPERS:

[1:](https://www.researchgate.net/publication/2394127_Combining_Analyses_Combining_Optimizations)

* Global analysis: One obvious method for improving the translated code is to look for code fragments with common patterns and replace them with more efficient code fragments. These are called peephole optimizations. However, peephole optimizations are limited by their local view of the code. A stronger method involves global analysis, in which the compiler first inspects the entire program before making changes.
* Data-flow analysis: A common global analysis is called data-flow analysis, in which the compiler studies the flow of data around a program.
* Top-down and bottom-up: Central to most data-flow frameworks is the concept of a lattice. Analysis can be from top to bottom in the lattice or bottom to top.
* Multi-pass: Modern optimizing compilers make several passes over a program's intermediate representation in order to generate good code. Many of these optimizations exhibit a phase ordering problem. The compiler discovers different facts (and generates different code) depending on the order in which it executes the optimizations. Getting the best code requires iterating several optimizations until reaching a fixed point. This thesis shows that by combining optimization passes, the compiler can discover more facts about the program, providing more opportunities for optimization.
* Most constants and common subexpressions are easy to find; they can be found using a simple peephole analysis technique. This peephole analysis was done at parse-time lowering total compilation time by over 6%. To do strong peephole analysis at parse-time requires SSA form and use-def chains. We implemented a parser that builds SSA form and use-def chains incrementally, while parsing.

[2:](https://tel.archives-ouvertes.fr/tel-02443231/document)

* Common compiler-optimization techniques:
* Data-flow analysis: gathers information about the possible set of values calculated at various points in a computer program. A program control flow graph (CFG) is used to determine those parts of a program to which a particular value assigned to a variable might propagate.
* Partial evaluation, dead code elimination and common sub-expression elimination, to reduce code size.
* Inline expansion
* Instruction scheduling
* Common loop optimizations include interchange, splitting, unrolling, etc.
* Automatic parallelization by converting sequential code into multi-threaded or vectorized code in order to utilize multiple processors simultaneously
* Strength reduction
* A lot of existing tools allow to perform source-to-source transformation, most of them focus on optimizing loops using the polyhedral model. The polyhedral method treats each loop iteration within nested loops as lattice points inside mathematical objects called the polyhedral. It performs affine transformations or more general non-affine transformations such as tiling on the polytopes, and then converts the transformed polytopes into equivalent, but optimized, loop nests through polyhedral scanning. The polyhedral model allows for good performance on loops that can be handled and is especially used for the parallelism, but it can be applied on a small set of loops.
* Pg 29-34 //check where we stand

SIMILAR EXISTING TOOLS:

* Compiler Explorer is an open source software/website that can compile and show the assembly created by a wide variety of compilers, platforms and settings.

[Someone asked for the exact tool we are building and it doesn't exist](https://softwareengineering.stackexchange.com/questions/380328/c-c-code-optimization-without-compilation)