30. a.	Explain the various structure of page tables with a neaf sketch.	12	3	3	4
b.	(OR) Experiment the operation of segmentation with a neat sketch and suitable example.	12	4	3	4
31. a.	Explain the concept of demand paging with neat sketch and examine how the page faults are handled.	12	4	4	5
b.	(OR) Implement the concept of first in first out, optimal page replacement and least recently used page replacement for the string = 4, 7, 6, 1, 7, 6, 1, 2, 7, 2. Size of frame is 3.	12	3	4	5
32. a.	Experiment the concept of disk scheduling algorithms with a suitable example.	12	3	5	3
	(OR)				
b.	Examine the various implementation of file systems with a neat sketch.	12	4	5	3

Reg. No.			

MINOR CERTIFICATION EXAMINATION, JUNE 2023 Second Semester

18CSE001J – OPERATING SYSTEMS

		(For the candidates admitted during	ng the	academic year 2021-2022 & 2022-202	23)			
Note:	_							
(i)	ove	r to hall invigilator at the end of 40th	minut		et shou	ld be	han	ded
(ii)	Par	t - B & Part - C should be answered	in an	swer booklet.				-
Time: 3	hour	S			Max. 1	Marl	ks: 1	00
			73		Marks	BL	СО	PΩ
		$PART - A (20 \times 1)$,	Marks	DL	CO	PU
		Answer ALL C	-			ij		
1.	proc	is responsible for manage cessing and control.	ing r	esource for movement storage,	1	1	1	1
	(A)	Visual programming	(B)	Operating systems				
	(C)	High performance computing	(D)	Hypervisor				
2.	In s	imple batch system, the program	n inst	truction are given to the monitor	1	1	1	1
	usin	g special language called						
	(A)	Job control language	(B)	Serial processing program				
	(C)	Scheduler program	(D)	Object oriented program				
3.		is passive entity stored on c	lisk as	s executable file.	1	2	1	1
	(A)	Process	(B)	Program				
	(C)	Неар	(D)	Stack				
4.		scheduler can be added if	degre	e of multiple programming needs	1	2	1	1
		ecrease						
	(A)	Short term	(B)	Medium term				
	(C)	JOB	(D).	CPU				
5.			e the	internal variables only accessible	1	2	. 2	1
	by c	code within the procedure					(6)	
	(A)	Semaphore	(B)	Binary semaphore				
	(C)	Counting semaphore	(D)	Monitors				
6.			_	cular process from the time of	1	2	2	3
		mission through the time of comp	pletion	n is called as				
	(A)	Throughput	(B)	Response time				
	(C)	Waiting time	(D)	Turnaround time			0_	۲.
7.	In		•	wer priority process has to be	1	1	2	3
		eased after a fixed interval of tim						
		Aging technique	. ,	Starvation				
	(C)	Deadlock	(D)	Long-term scheduler				

The rate monotonic scheduling al stack priority policy with	gorithn	n schedules periodic tasks using	1	1	2	3	
(A) Non preemption	(B)	Preemption					
(C) Medium term scheduler		_				_ // =	
The address generated by the centr to as a	al proc	essing unit is commonly referred	1	1	3	3	
(A) Physical address	(B)	Memory address register					
(C) Logical address							
	1 to ph	sysical addresses is done by the	1	1	3	2	
	(B)	Mani memory					
(C) Secondary memory	(D)	Cache memory					
		empty space required to store the	1	2	3	2	
					ě -		
If a page is needed that was not of generated.	original	ly loaded up, then a is	1	2	3	2	
(A) Page fault trap	(B)	Page error trap					
(C) Page mistake trap	(D)	Page correctness trap					
as they are needed is called as		26	1	1	4	2	
(C) Fragmentation	(D)	Compaction					
Which page replacement algorithm	underg	o Belady's anomaly?	1	2	4	4	
(A) Optimal page replacement algorithm	nt (B)	First in first out					
(C) Least recently used	(D)	Most recently used					
is amenable to full statistic	cal anal	vsis	1	1	4	1	
(A) First in first out							-5
(C) Most recently used	. ,	•					
activity is called	g pages	in and out. This high paging	1	1	- 4	3 .	
	(B)	Spoofing					
(C) Non paging	(D)	Non segmentation					
is the time taken to locate the data is to be read or write.	the dis	k arm to a specified track where	1	2	5	3	
(A) Rotational latency	(B)	Transfer time					
(C) Seek time	(D)	Disk access time		4			
	stack priority policy with (A) Non preemption (C) Medium term scheduler The address generated by the centre to as a	stack priority policy with	(A) Non preemption (C) Medium term scheduler (D) Process allocation The address generated by the central processing unit is commonly referred to as a	stack priority policy with	stack priority policy with	stack priority policy with (A) Non preemption (B) Preemption (C) Medium term scheduler (D) Process allocation The address generated by the central processing unit is commonly referred to as a (A) Physical address (B) Memory address register (C) Logical address (D) Addressing modes The runtime mapping from virtual to physical addresses is done by the hardware device called (A) Memory management unit (B) Mani memory (C) Secondary memory (D) Cache memory — occurs when the total amount of empty space required to store the process is available in the main memory. (A) Internal fragmentation (B) External fragmentation (C) Peterson's solution (D) Paging If a page is needed that was not originally loaded up, then a is generated. (A) Page fault trap (B) Page error trap (C) Page mistake trap (D) Page correctness trap An alternate approach to solve the paging problem which loads pages only as they are needed is called as (A) Demand paging (B) Swapping (C) Fragmentation (D) Compaction Which page replacement algorithm undergo Belady's anomaly? (A) Optimal page replacement (B) First in first out algorithm (C) Least recently used (D) Most recently used (D) Most recently used A process is busy with swapping pages in and out. This high paging activity is called (A) First in first out (B) Least recently used (C) Most recently used (D) Most frequently used A process is busy with swapping pages in and out. This high paging activity is called (A) Thrashing (B) Spoofing (C) Non paging (C) Non paging (D) Non segmentation (E) Least technique where the data is to be read or write. (A) Rotational latency (B) Transfer time	stack priority policy with (A) Non preemption (B) Preemption (C) Medium term scheduler (D) Process allocation The address generated by the central processing unit is commonly referred to as a (A) Physical address (B) Memory address register (C) Logical address (C) Logical address (D) Addressing modes The runtime mapping from virtual to physical addresses is done by the hardware device called (A) Memory management unit (B) Mani memory (C) Secondary memory (D) Cache memory ———————————————————————————————————

18.	Dividing a disk into sectors that the disk called as	controller can read and write is	1	2	5	1
		High level formatting Virtual formatting				
19.	is also called as relative access me	1	1	5	1	
	(A) Indirect access method (B)	Indexed access method Sequential access method				
20.	represent an important criterion for supports file sharing.	Linui .	1	2	5	4
		Consistency syntax Inconsistency syntax				
2	(C) inconsistency semantics (D)	meonsistency syntax				
	$PART - B (5 \times 4 = 2)$ Answer ANY FIVE		Marks	BL	co	PO
21.	Examine the working of simple batch system point of view and processor point of view.	ms and differentiate the monitor	4	4	1	1
22.	How the coordinating process can resolve fi	rom race condition?	4	4	1	1
23.	Justify Peterson's solving in solving the crit	4	4	2	2	
24.	Implement the strategy to solve the readers	4	3	2	2	
25.	Demonstrate the memory partition alloc examples.	ation algorithm with suitable	5	3	3	3
26.	Implement the counting algorithm-least free for the reference string = 0, 2, 0, 1, 0, 3, 2, 1		5	3	4	3
27.	Illustrate the continuous allocation of disk s	pace with a suitable example.	5	3	5	5
	$PART - C (5 \times 12 = 60 \text{ M})$ Angular ALL Quantier		Marks	BL	CO	PO ·
•	Answer ALL Question	18				
28: a.	Examine the steps involved in the process	creation and termination with a	12	4	1	1
9 .	proper example.	100				
	(OR)					
b.	Describe the operation and significance of detail.	inter process communication in	12	4	1	1
29. a.	Implement the concept of monitors in so problem with a neat pseudo code.	olving the dinning philosopher	12	3	2	2
	(OR)					
Ъ.	List out the reasons for deadlock occurrence handle deadlocks.	ces and explain the methods to.	12	3	2	2