Reg. No.															
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B.Tech. / M.Tech. (Integrated) DEGREE EXAMINATION, DECEMBER 2023

First and Second Semester

21EES101T - ELECTRICAL AND ELECTRONICS ENGINEERING

(For the candidates admitted from the academic year 2022-2023)

(i)	Part - A should be answered in OMR s over to hall invigilator at the end of 40 th Part - B and Part - C should be answered	minute	.	et shoul	d be	hano	ded
(ii)		zu III a	nswer bookiet.	λ.(N (-	1	75
Time: 3	Hours			Max.	ivia	rks:	13
	$PART - A (20 \times 1)$	= 20N	Aarks)	Marks	BL	CO	PO
	Answer ALL Q						
1.	The expression for rms voltage of an			1	1	1	1
		(B)					
	(A) $\frac{V_m}{2}$		$\frac{V_m}{\sqrt{2}}$				
	(C) V_{dc}	(D)					
	(C) $\frac{V_{dc}}{\sqrt{2}}$		$\frac{3}{\sqrt{3}}$				
			*				
2.	The law proposed by ohm is relevant			1	1	1	1
	(A) Temperature constant		Pressure constant				
	(C) Volume constant	(D)	Constant pressure and				
-			temperature				
3.	In a pure inductive circuit (L), the cur	rent	by a voltage of 90 degree.	1	1	1	1
	(A) Leads		Lags				
	(C) In phase		Greater than and equal				
4.	According to superposition theorem,	while	considering an individual source,	1	1	1	1
	all other voltage sources can be	(D)	Short circuited				
	(A) Replaced by current sources(C) Open circuited	` '	Replaced by external resistor				
	(C) Open encured	(D)	Replaced by external resistor				
5.	The diode is used in			1	1	2	1
	(A) Uncontrolled rectifiers	(B)	Stepping down AC voltage				
			circuits				
	(C) Stepping up AC voltage circuits	(D)	Controlled rectifiers				
6	The violage controlled comicandusts	r dav	ice is	1	1	2	1
0.	The voltage controlled semiconductor (A) Zener diode		BJT	-	-	-	
	(C) PN diode		MOSFET				
	(-,:	(~)					

7. The logical expression Y=AB+AC is known as

(A) Standard sum of product form(B) Sum of product form(C) Standard product of sum form(D) Product of sum form

Note:

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8.	The	input X-NOR gate gives high our	tput		1	1	2	1
	(A)	When one input is high and other is low	(B)	Only when both inputs are low				
	(C)	When both inputs are same	(D)	Only when both the inputs are high				
9.	9. The material used for the yoke of a DC machine is to provide a						3	1
		Current path	` '	Flux path	40			
	(C)	Mechanical support	(D)	Conversion from AC to DC				
10.	Roto	or windings are not present in			1	1	3	1
	` '	DC shunt motor	` /	DC series motor				
	(C)	Squirrel cage induction motor	(D)	Slip ring induction motor				
11.	Whi	ch of the following motor is more	e suit	able for EV applications?	1	1	3	1
	(A)	BLDC motor	(B)	Stepper motor				
	(C)	DC shunt motor	(D)	Induction motor				
12.	Late	st E-vehicle launched by Tata mo	otors	is	1	1	3	1
	(A)	Tigor EV	(B)	Kona				
	(C)	Chetak	(D)	Nexon EV				
13.		LVDT, when the iron core is enducer is	xactl	y at centre, then net output of	1	1	4	1
	(A)	Zero	(B)	Maximum				
	(C)	Average	(D)	Unpredictable				
14.	bear	ing limitations?		y used in testing of structural load	1	1	4	1
	` ′	Thermocouple	(B)	Thermistor				
	(C)	LDR	(D)	Strain gauge				
15.	The	current flow in photodiode when	light	fall on its junction is called	1	1	4	1
	` '	Knee current	(B)	Saturation current				
	(C)	Photo current	(D)	Pinchoff current				
16.		ch transducer is used for light spe			1	1	4	1
	` '	Laser	` '	Photo transistor				
	(C)	LED	(D)	LVDT				
17.	Eart	h wire is made up of			1	1	5	1
	(A)	Copper	(B)	Iron				
	(C)	Aluminium	(D)	Galvanized steel				
18.	18. Solar efficiency will be commonly in the range of							1
	(A)	3% to 5%	(B)	90% to 95%				
	(C)	40% to 50%	(D)	10% to 20%				
19.	The	most widely used material for so	lar ce	ell fabrication is	1	1	5	1
	(A)	Germanium	(B)	Silicon				
	(C)	Aluminium	(D)	Silver				

	(A) Silicon (B) Titanium (C) Arsenide (D) Gallium		6		
	PART – B ($5 \times 8 = 40$ Marks) Answer ALL Questions	Marks	BL	со	РО
21. a.	Using Mesh analysis, find mesh current in first loop (I ₁).	8	2	1	2
	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$				
b.	(OR) A resistor of 10Ω, an inductance of 100 mH and a capacitance of 150 mF are connected in series and supplied by 200V, 50 Hz supply. Calculate, (i) Inductive reactance (ii) Capacitive reactance (iii) Impedance (iv) Current	8	2	²¹ 1	2
22. a.i.	For the Boolean function given below, obtain the canonical SOP form and canonical POS form $Y(A,B,C) = A + B'C$.	4	2	2	2
ii.	Draw the output characteristics of common emitter configuration and name three regions.		1	2	1
b.	(OR) Explain the operating characteristics of SCR with relevant diagrams.	_8	1	2	1
23. a.	Explain in detail about the construction and principle of operation of a DC generator.	8	1	3	1
b.	(OR) With a neat diagram, describe the working of BLDC motor. List out the advantages, disadvantages and applications of it.	8	1	3	1
24. a.	Explain the principle, construction and working of permanent magnet moving coil instrument.	8	1	4	1
_	(OR)		1	4	1
b.	Write short notes on (i) Piezoelectric transducer (ii) Inductive proximity sensor	4 4	1	4	1
25. a.	Explain in detail about hybrid electric vehicle and plug in hybrid electric vehicle with neat sketches.	8	1	5	1
	(OR)				

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20. The predominant material used for PV cell manufacturing is

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- b.i. What is the necessity of earthing? Explain pipe earthing with a neat diagram. 4 1 5 1
- ii. Draw the single line diagram of a typical generation, transmission and 4 1 5 distribution of AC power system.

$$PART - C (1 \times 15 = 15 Marks)$$

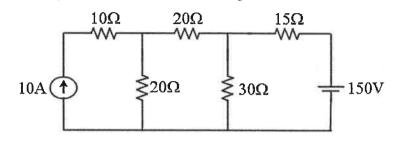
Marks BL CO PO for 15 2 2 2

15

- 26. Simplify the following expression using k-map and draw logic diagrams for the simplified expressions.
 - (i) $Y(A,B,C,D) = \sum m(0,1,2,4,5,8,9,10,12,13)$
 - (ii) $F(A,B,C,D) = \Pi M(0,2,4,14,15)$

Obtain the logic diagram for the above expressions.

27. Using nodal analysis find all the node voltages.



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