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B.Tech DEGREE EXAMINATION, NOVEMBER 2023

Fifth and Seventh Semester

18ECO102J - ELECTRONIC CIRCUITS AND SYSTEMS

(For the candidates admitted during the academic year 2020 - 2021 & 2021 - 2022)

Note:

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i. Part - A should be answered in OMR sheet within first 40 minutes and OMR sheet should be handed over to hall invigilator at the end of 40th minute.
ii. Part - B and Part - C should be answered in answer booklet.

Time: 3 Hours PART - A (20 × 1 = 20 Marks) Answer all Questions			Max. Marks: 100				
			Mar	ks BL	CO		
	1.	What is the cut-in voltage for the silicon s (A) 0.3 (C) 0.4	emiconductor? (B) 0.7 (D) 0.1	1	Î .	1	
	2.	which material has poor electrical conduct (A) Metal (C) Semiconductor	tivity? (B) Insulator (D) Conductor	1	1	1	
	3.	An reverse biased PN junction will act as (A) amplifier (C) attenuator	a/an (B) open switch (D) closed switch	1	1	1	
	4.	Identify the device that can be used to reg (A) p-n junction diode (C) snubber circuit	ulate voltage in an electrical system. (B) zener diode (D) JFET	1	1	1	
	5.	The extremely high input impedance of a (A) absence of its channel (C) depletion of current carriers	MOSFET is primarily due to the (B) negative gate-source voltage (D) extremely small leakage current of its gate capacitor	1	1	2	
	6.	FET have similar properties to (A) PNP transistors (C) NPN transistors	(B) Power transistors(D) Unijunction transistors	1	1	2	
	7.	For small values of drain-to-source voltag (A) constant-current source (C) negative resistance	e, JFET behaves like a (B) constant-voltage source (D) resistor	1	1	2	
	8.	The voltage gain of a given common-sour (A) amplification factor (C) drain load resistance	ce JFET amplifier depends on its (B) dynamic drain resistance (D) Input impedance	1	1	2	
	9.	An OP-AMP can be classified as(A) low input resistance (C) RC-Coupled	amplifier (B) positive feedback (D) linear	1	. 1	3	
	10.	An ideal OP-AMP has (A) finite voltage gain (C) low bandwidth	(B) Infinite input resistance(D) zero total gain	1	1	3	
	11.	The two input terminals of an OP-AMP art (A) positive and negative voltage	(B) differential and non-differential amplifier	11	1	3	
		(C) inverting and non-inverting voltage	(D) high and low voltage				

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12.	Which of the following op-amp configurations is commonly used as a voltage follower?				3
	(A) Inverting amplifier (C) Integrator	(B) Non-Inverting amplifier(D) Differentiator			
13.	Which type of waveform is typically ger oscillator?	nerated by an IC741-based Wien Bridge	1	1	4
	(A) Square wave(C) Triangle wave	(B) Sine wave(D) Sawtooth wave			
14.	Which parameter of an IC741 operational oscillators?	amplifier is critical for designing stable	1	1	4
	(A) Input bias current(C) Slew rate	(B) Open-loop voltage gain(D) Output voltage swing		(5)	
15.	Which of the following is not a type of mul (A) Astable (C) Bistable	tivibrator? (B) Monostable (D) Bipolar	1	1	4
16.	In an astable mode configuration, what is the (A) Constant high voltage (C) Alternating between high and low	ne output of the 555 timer? (B) Constant low voltage (D) Pulsating voltage	1	1	4
17.	In AM modulation, which part of the carrier signal varies with the amplitude of the modulating signal?			1	5
	(A) Frequency (C) Wavelength	(B) Phase (D) Amplitude			
18.	Which of the following statements is true about FM modulation compared to AM modulation?			1	5
	(A) FM is less immune to noise.(C) FM is less affected by atmospheric conditions.	(B) FM has a narrower bandwidth.(D) FM requires less transmitter power.			
19.	19. In Pulse Amplitude Modulation (PAM), what does the amplitude of each pulse represent?				5
	(A) Phase information (C) Digital data	(B) Frequency information (D) Analog data			
20.	What is the primary purpose of an antenna (A) To amplify the received signal (C) To convert electrical signals into electromagnetic waves and vice versa	in a wireless communication system? (B) To filter out noise from the signal (D) To increase the data rate of the transmission	1	1	5
	$PART - B (5 \times 4 = 2)$		Marl	ks BL	СО
	Answer any 5 Qu	estions			
21.	The following current values are obtained values are I_E =2mA , I_B =10 μ A.Compute th current I_c .		4	1	1
22.	Classify solids based on energy band diagram.		4	1	1
23.	Define Transconductance and Amplification factor of FET.			1	2
24.	List out the difference between FET and BI	T	4	1	2
25.	5. Draw circuit diagram of op-amp in Non-inverting amplifier and give the expression for its output voltage & gain.			1	3
26.	Mention some applications of IC555 timer.		4	1	4

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27	Illustrate the purpose of modulation in communication.	4	1	5
21.6	PART - C (5 × 12 = 60 Marks) Answer all Questions	Mark	s BL	СО
28.	(a) Describe the circuit operation of Half wave and Full-wave rectifier using pn- junction diode with suitable circuit diagram and its waveform (OR)	12	1	1
	(b) Explain the input and output characteristics of common Emitter-BJT amplifier with neat diagram.			
29.	(a) Explain the Voltage-Divider Biasing of JFET using D.C analysis with the circuit diagram	12	1	2
	(OR) (b) Sketch the circuit diagram for a depletion type common source MOSFET amplifier operated in voltage divider bias configuration and determine the expressions for its input impedance, output impedance and voltage gain.			
30.	(a) Describe the basic operation of a comparator using the IC741 with suitable circuit diagram. (OR) (I) Describe the functioning of an OPAMP log amplifier circuit, derive the	12	1	3
	output voltage formula and illustrate the circuit diagram of an Office inverting amplifier, also find its voltage gain.			
31.	(a) Explain the operation of an RC phase shift oscillator, detailing the key components and the principles behind its oscillation mechanism. (OR)	12	1	4
	(b) Explain the operation of Hartley oscillator circuit using IC741 with a neat diagram.			
32.	communication, and how does it play a crucial role in the transmission of information?	12	1	5
	(OR)			
	(b) (i) Define the term amplitude modulation with its waveform. (4 Marks) (ii) Describe the types of amplitude modulation with its waveform. (8 Marks)			
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