

## Interview Questions: (eInfoChips)

1. What is your company, Who is your client?
2. You have mentioned 4 projects can you explain what your chip and how you have done the flow?
3. What are the issues you have faced in block?
4. How do you place the macros? On what basis you have placed the macros?
5. How do you reduced the congestion?
6. What is your block size?
7. What macros do u have in your block?
8. How many FP's you have done for your block
9. What is your power structure. Have you done IR drop analysis?
10. Have you performed PV?
11. Have you done SI analysis?
12. What is crosstalk? What are 2 effects of crosstalk in CTS?
13. What your CT structure for the blocks you have worked
14. What are the violations u faced at each stage?
15. How many modes does your block have?
16. Can u explain various approaches to fix the congestion
17. How you will fix the timing violations
18. Have fixed any timing violations manually
19. If you have a setup time violation of 50 ps. How you will solve this?
20. After changing to higher drive strengths still you have 10 ps setup violation and there is no changing datapath cell to higher drives . how will you solve this violation
21. Have you worked on MCMM analysis
22. Which cells u used in CT building? And why?
23. Reg-Reg path has both setup time and hold violations. How you will fix?
24. Datapath has violation of 10ps and there is no possibility of alter the datapath delay. In that how you will fix setup the issue.
25. In that case if you delay the capture flop. This capture flop will be the launch for next flop. Launch the data slow. How will you solve the problem.

These are the core questions that remembered. Still few questions they asked which are related to flow only.

Guys in-case if you have any doubts in questions please feel free to call me.

