JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486196

PROVISIONAL CERTIFICATE

College: 24 - G R R I E T, HYDERABAD

PC No: 31101045894

HT No: 10241A0457

21101023785

This is to certify that Mr. SAPIREDDY SRINIVAS RAHUL

son of Mr. SAPIREDDY BHASKARA SATYANARAYANA

passed B. Tech. ELECTRONICS & COMMUNICATION ENGINEERING degree examination of this University, held in April, 2014 and that he was placed in FIRST CLASS WITH DISTINCTION.

He has satisfied all the requirements for the award of the degree.

Controller of Examinations **REGISTRAR**

Issue Date: April 19, 2016

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486186

CONSOLIDATED MARKS MEMO

Sl NO 21101023785 Branch : ELECTRONICS & COMMUNICATION ENGINEERING

Name SAPIREDDY SRINIVAS RAHUL College : 24-G R R I E T,HYDERABAD

HTNO 10241A0457 Year of Admission : 2010-2011 ExamDate : April,2014

Class Awarded : FIRST CLASS WITH DISTINCTION

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
	I YEAR										
1	ENGLISH	23	32	55	4	2	ENGLISH LANGUAGE COMMUNICATION SKIL	23	47	70	4
3	IT WORKSHOP & ENGINEERING WORKSHOP	22	50	72	4	4	MATHEMATICS - I	21	29	50	6
5	MATHEMATICAL METHODS	16	51	67	6	6	ENGINEERING PHYSICS	22	58	80	4
7	ENGINEERING CHEMISTRY	18	50	68	4	8	COMPUTER PROGRAMMING & DATA STRUC	15	42	57	6
9	ENGINEERING DRAWING	19	69	88	4	10	COMPUTER PROGRAMMING LAB	24	48	72	4
11	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	22	45	67	4						
	I SEMESTER			**II YEAR**			**II SEMESTER**				
1	MATHEMATICS-III	12	55	67	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	20	65	85	3
2	PROBABILITY THEORY & STOCHASTIC PROCES	21	63	84	3	2	ELECTRONIC CIRCUIT ANALYSIS	22	55	77	4
3	ENVIRONMENTAL STUDIES	22	49	71	3	3	PULSE & DIGITAL CIRCUITS	19	41	60	4
4	ELECTRIC CIRCUITS	25	29	54	4	4	SWITCHING THEORY & LOGIC DESIGN	25	40	65	4
5	ELECTRONIC DEVICES & CIRCUITS	20	45	65	4	5	ELECTROMAGNETIC THEORY & TRANSMISS	25	54	79	4
6	SIGNALS & SYSTEMS	19	49	68	4	6	ELECTRICAL ENGINEERING LAB	25	48	73	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	25	50	75	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	20	26	46	2
8	BASIC SIMULATION LAB	23	47	70	2	8	PULSE & DIGITAL CIRCUITS LAB	25	27	52	2
	I SEMESTER			**III YEAR**			**II SEMESTER**				
1	CONTROL SYSTEMS	18	41	59	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANA	22	30	52	4
2	COMPUTER ORGANIZATION	17	35	52*	4	2	ADVANCED ENGLISH COMMUNICATION SKIL	22	47	69	2
3	ANTENNAS & WAVE PROPAGATION	21	51	72	3	3	OPERATING SYSTEMS	24	41	65	4
4	ELECTRONIC MEASUREMENTS & INSTRUMENT	25	36	61	4	4	DIGITAL COMMUNICATIONS	24	63	87	3
5	ANALOG COMMUNICATIONS	18	32	50*	3	5	MICROPROCESSORS & MICROCONTROLLER	22	47	69	4
6	IC APPLICATIONS	23	48	71	3	6	DIGITAL SIGNAL PROCESSING	25	39	64	4
7	ANALOG COMMUNICATIONS (LAB)	23	44	67	2	7	MICROPROCESSORS & MICROCONTROLLER	24	47	71	2
8	IC APPLICATIONS (LAB)	23	49	72	2	8	DIGITAL SIGNAL PROCESSING (LAB)	24	48	72	2
	I SEMESTER			**IV YEAR**			**II SEMESTER**				
1	MANAGEMENT SCIENCE	20	52	72	3	1	CELLULAR & MOBILE COMMUNICATIONS	25	48	73	3
2	E-CAD AND VLSI LAB	24	44	68	2	2	WIRELESS COMMUNICATIONS & NETWORKS	20	40	60	3
3	MICROWAVE ENGG. AND DIGITAL COMMNS. L/	25	45	70	2	3	INDUSTRY ORIENTED MINI PROJECT	-	43	43	2
4	VLSI DESIGN	23	49	72	4	4	SEMINAR	48	-	48	2
5	MICROWAVE ENGINEERING	22	67	89	3	5	MAJOR PROJECT	46	130	176	10
6	COMPUTER NETWORKS	20	39	59	4	6	COMPREHENSIVE VIVA	-	98	98	2
7	DIGITAL IMAGE PROCESSING	21	39	60	3	7	RADAR SYSTEMS	23	46	69	3
8	OPTICAL COMMUNICATIONS	22	55	77	4						
							(# Project Internal= 50 , External= 150)				

3922 OUT OF 5200 (75.42%)

Number of Credits registered for : 200 Aggregate Marks Secured for best: 193

IssueDate : April 19, 2016

CONTROLLER OF EXAM REGISTRAR

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486119

MEMORANDUM OF MARKS

SERIAL NO : 11033003121

EXAMINATION : I Year B.Tech. (R09) Regular

BRANCH : ELECTRONICS & COMMUNICATION ENGINEERING

NAME : SAPIREDDY SRINIVAS RAHUL

HALL TICKET NO : 10241A0457

MONTH & YEAR OF EXAM : June,2011

INSTITUTION : G R R I E T,HYDERABAD

S.No.	SUBJECT CODE	S U B J E C T T I T L E	Internal Marks	End Exam	Total Marks	Result	Credits
1	51001	ENGLISH	23	32	55	P	4
2	51002	MATHEMATICS - I	21	29	50	P	6
3	51008	MATHEMATICAL METHODS	16	51	67	P	6
4	51004	ENGINEERING PHYSICS	22	58	80	P	4
5	51005	ENGINEERING CHEMISTRY	18	50	68	P	4
6	51006	COMPUTER PROGRAMMING & DATA STRUCTURES	15	42	57	P	6
7	51013	ENGINEERING DRAWING	19	69	88	P	4
8	51612	COMPUTER PROGRAMMING LAB	24	48	72	P	4
9	51613	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	22	45	67	P	4
10	51614	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	23	47	70	P	4
11	51615	IT WORKSHOP & ENGINEERING WORKSHOP	22	50	72	P	4
SUBJECTS REGISTERED: 11	APPEARED: 11	PASSED: 11	TOTAL:	225	521	746		50

AGGREGATE(IN WORDS) : **** Seven Four Six ****

	MAXIMUM MARKS			MINIMUM FOR PASS	
	Internal	External	Total	External	Total of Int&Ext
THEORY/DRAWING/DESIGN SUBJECTS	25	75	100	26	40
PRACTICAL SUBJECTS	25	50	75	18	30

RESULT (P-PASS,F-FAIL)

IssueDate : April 19, 2016

CONTROLLER OF EXAMINATION

REGISTRAR

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486126

MEMORANDUM OF MARKS

SERIAL NO : 11042003400

EXAMINATION : II B.Tech. I Semester (R09) Regular

BRANCH : ELECTRONICS & COMMUNICATION ENGINEERING

NAME : SAPIREDDY SRINIVAS RAHUL

HALL TICKET NO : 10241A0457

MONTH & YEAR OF EXAM : November,2011

INSTITUTION : G R R I E T,HYDERABAD

S.No.	SUBJECT CODE	SUBJECT TITLE	Internal Marks	End Exam	Total Marks	Result	Credits
1	53007	MATHEMATICS-III	12	55	67	P	3
2	53019	PROBABILITY THEORY & STOCHASTIC PROCESSES	21	63	84	P	3
3	53013	ENVIRONMENTAL STUDIES	22	49	71	P	3
4	53020	ELECTRIC CIRCUITS	25	29	54	P	4
5	53009	ELECTRONIC DEVICES & CIRCUITS	20	45	65	P	4
6	53021	SIGNALS & SYSTEMS	19	49	68	P	4
7	53606	ELECTRONIC DEVICES & CIRCUITS LAB	25	50	75	P	2
8	53607	BASIC SIMULATION LAB	23	47	70	P	2
SUBJECTS REGISTERED: 8	APPEARED: 8	PASSED: 8	TOTAL:	167	387	554		25

AGGREGATE(IN WORDS) : **** Five Five Four ****

	MAXIMUM MARKS			MINIMUM FOR PASS	
	Internal	External	Total	External	Total of Int&Ext
THEORY/DRAWING/DESIGN SUBJECTS	25	75	100	26	40
PRACTICAL SUBJECTS	25	50	75	18	30

RESULT (P-PASS,F-FAIL)

IssueDate : April 19, 2016

CONTROLLER OF EXAMINATION

REGISTRAR

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486136

MEMORANDUM OF MARKS

SERIAL NO : 11052003199

HALL TICKET NO : 10241A0457

EXAMINATION : II B.Tech. II Semester (R09) Regular

MONTH & YEAR OF EXAM : April,2012

BRANCH : ELECTRONICS & COMMUNICATION ENGINEERING

INSTITUTION : G R R I E T,HYDERABAD

NAME : SAPIREDDY SRINIVAS RAHUL

S.No.	SUBJECT CODE	S U B J E C T T I T L E	Internal Marks	End Exam	Total Marks	Result	Credits
1	54019	PRINCIPLES OF ELECTRICAL ENGINEERING	20	65	85	P	3
2	54020	ELECTRONIC CIRCUIT ANALYSIS	22	55	77	P	4
3	54021	PULSE & DIGITAL CIRCUITS	19	41	60	P	4
4	54010	SWITCHING THEORY & LOGIC DESIGN	25	40	65	P	4
5	54011	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	25	54	79	P	4
6	54606	ELECTRICAL ENGINEERING LAB	25	48	73	P	2
7	54607	ELECTRONIC CIRCUIT ANALYSIS LAB	20	26	46	P	2
8	54608	PULSE & DIGITAL CIRCUITS LAB	25	27	52	P	2
SUBJECTS REGISTERED: 8	APPEARED: 8	PASSED: 8	TOTAL:	181	356	537		25

AGGREGATE(IN WORDS) : **** Five Three Seven ****

	MAXIMUM MARKS			MINIMUM FOR PASS	
	Internal	External	Total	External	Total of Int&Ext
THEORY/DRAWING/DESIGN SUBJECTS	25	75	100	26	40
PRACTICAL SUBJECTS	25	50	75	18	30

RESULT (P-PASS,F-FAIL)

IssueDate : April 19, 2016

CONTROLLER OF EXAMINATION

REGISTRAR

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486146

MEMORANDUM OF MARKS

SERIAL NO : 11063003091

EXAMINATION : III B.Tech. I Semester (R09) Regular

BRANCH : ELECTRONICS & COMMUNICATION ENGINEERING

NAME : SAPIREDDY SRINIVAS RAHUL

HALL TICKET NO : 10241A0457

MONTH & YEAR OF EXAM : November,2012

INSTITUTION : G R R I E T,HYDERABAD

S.No.	SUBJECT CODE	SUBJECT TITLE	Internal Marks	End Exam	Total Marks	Result	Credits
1	55012	CONTROL SYSTEMS	18	41	59	P	4
2	55021	COMPUTER ORGANIZATION	17	35	52	P	4
3	55022	ANTENNAS & WAVE PROPAGATION	21	51	72	P	3
4	55023	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	25	36	61	P	4
5	55024	ANALOG COMMUNICATIONS	18	32	50	P	3
6	55009	IC APPLICATIONS	23	48	71	P	3
7	55606	ANALOG COMMUNICATIONS (LAB)	23	44	67	P	2
8	55607	IC APPLICATIONS (LAB)	23	49	72	P	2
SUBJECTS REGISTERED: 8	APPEARED: 8	PASSED: 8	TOTAL:	168	336	504		25

AGGREGATE(IN WORDS) : **** Five Zero Four ****

	MAXIMUM MARKS			MINIMUM FOR PASS	
	Internal	External	Total	External	Total of Int&Ext
THEORY/DRAWING/DESIGN SUBJECTS	25	75	100	26	40
PRACTICAL SUBJECTS	25	50	75	18	30

RESULT (P-PASS,F-FAIL)

IssueDate : April 19, 2016

CONTROLLER OF EXAMINATION

REGISTRAR

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486156

MEMORANDUM OF MARKS

SERIAL NO : 11075003055

HALL TICKET NO : 10241A0457

EXAMINATION : III B.Tech. II Semester (R09) Regular

MONTH & YEAR OF EXAM : May,2013

BRANCH : ELECTRONICS & COMMUNICATION ENGINEERING

INSTITUTION : G R R I E T,HYDERABAD

NAME : SAPIREDDY SRINIVAS RAHUL

S.No.	SUBJECT CODE	S U B J E C T T I T L E	Internal Marks	End Exam	Total Marks	Result	Credits
1	56023	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	22	30	52	P	4
2	56024	OPERATING SYSTEMS	24	41	65	P	4
3	56026	DIGITAL COMMUNICATIONS	24	63	87	P	3
4	56012	MICROPROCESSORS & MICROCONTROLLERS	22	47	69	P	4
5	56027	DIGITAL SIGNAL PROCESSING	25	39	64	P	4
6	56606	MICROPROCESSORS & MICROCONTROLLERS (LAB)	24	47	71	P	2
7	56607	DIGITAL SIGNAL PROCESSING (LAB)	24	48	72	P	2
8	56608	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	22	47	69	P	2
SUBJECTS REGISTERED: 8	APPEARED: 8	PASSED: 8	TOTAL:	187	362	549		25

AGGREGATE(IN WORDS) : **** Five Four Nine ****

	MAXIMUM MARKS			MINIMUM FOR PASS	
	Internal	External	Total	External	Total of Int&Ext
THEORY/DRAWING/DESIGN SUBJECTS	25	75	100	26	40
PRACTICAL SUBJECTS	25	50	75	18	30

RESULT (P-PASS,F-FAIL)

IssueDate : April 19, 2016

CONTROLLER OF EXAMINATION

REGISTRAR

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486166

MEMORANDUM OF MARKS

SERIAL NO : 11089002892

EXAMINATION : IV B.Tech. I Semester (R09) Regular

BRANCH : ELECTRONICS & COMMUNICATION ENGINEERING

NAME : SAPIREDDY SRINIVAS RAHUL

HALL TICKET NO : 10241A0457

MONTH & YEAR OF EXAM : November,2013

INSTITUTION : G R R I E T,HYDERABAD

S.No.	SUBJECT CODE	SUBJECT TITLE	Internal Marks	End Exam	Total Marks	Result	Credits
1	57034	MANAGEMENT SCIENCE	20	52	72	P	3
2	57035	VLSI DESIGN	23	49	72	P	4
3	57036	MICROWAVE ENGINEERING	22	67	89	P	3
4	57037	COMPUTER NETWORKS	20	39	59	P	4
5	57041	DIGITAL IMAGE PROCESSING	21	39	60	P	3
6	57042	OPTICAL COMMUNICATIONS	22	55	77	P	4
7	57607	E-CAD AND VLSI LAB	24	44	68	P	2
8	57608	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	25	45	70	P	2
SUBJECTS REGISTERED: 8	APPEARED: 8	PASSED: 8	TOTAL:		177	390	567		25

AGGREGATE(IN WORDS) : **** Five Six Seven ****

	MAXIMUM MARKS			MINIMUM FOR PASS	
	Internal	External	Total	External	Total of Int&Ext
THEORY/DRAWING/DESIGN SUBJECTS	25	75	100	26	40
PRACTICAL SUBJECTS	25	50	75	18	30

RESULT (P-PASS,F-FAIL)

IssueDate : April 19, 2016

CONTROLLER OF EXAMINATION

REGISTRAR

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

TRANSCRIPT

Sl. No. : CC 6486176

MEMORANDUM OF MARKS

SERIAL NO : 11101002818

EXAMINATION : IV B.Tech. II Semester (R09) Regular

BRANCH : ELECTRONICS & COMMUNICATION ENGINEERING

NAME : SAPIREDDY SRINIVAS RAHUL

HALL TICKET NO : 10241A0457

MONTH & YEAR OF EXAM : April,2014

INSTITUTION : G R R I E T,HYDERABAD

S.No.	SUBJECT CODE	S U B J E C T T I T L E	Internal Marks	End Exam	Total Marks	Result	Credits
1	58024	CELLULAR & MOBILE COMMUNICATIONS	25	48	73	P	3
2	58028	RADAR SYSTEMS	23	46	69	P	3
3	58032	WIRELESS COMMUNICATIONS & NETWORKS	20	40	60	P	3
4	58613	INDUSTRY ORIENTED MINI PROJECT	-	43	43	P	2
5	58614	SEMINAR	48	-	48	P	2
6	58615	MAJOR PROJECT	46	130	176	P	10
7	58616	COMPREHENSIVE VIVA	-	98	98	P	2
SUBJECTS REGISTERED: 7	APPEARED: 7	PASSED: 7	TOTAL:	162	405	567		25

AGGREGATE(IN WORDS) : **** Five Six Seven ****

	MAXIMUM MARKS			MINIMUM FOR PASS	
	Internal	External	Total	External	Total of Int&Ext
THEORY/DRAWING/DESIGN SUBJECTS	25	75	100	26	40
PRACTICAL SUBJECTS	-	-	-	-	-
SEMINAR	50	-	50	-	20
MINI PROJECT	-	50	50	20	20
COMPREHENSIVE VIVA-VOCE	-	100	100	40	40

RESULT (P-PASS,F-FAIL)

IssueDate : April 19, 2016

CONTROLLER OF EXAMINATION

REGISTRAR