Note Title 07-09-2012 Floating Point Addition, Subtraction
Mulliplication, Division Addition:  $\chi_1 + \chi_2 \qquad (\chi_1 > \chi_2)$ En:1 1.11 + 1.00 ± 1.00 Ex1.3 1.11 + 1.10 x 2-2 Eq: 2 1.11 + 1.00 x 2-2 1.11 + 0.011 10.001

 $\frac{4}{72} \frac{1.11}{0.00} = 1.000 \times 2^{1}$   $\frac{1.0001}{2000} \times 2$ 

Steps  $\chi_1 + \chi_2 \qquad (\chi_1 > \chi_2)$  (e,) (e<sub>2</sub>)

- 1). Right shift  $x_2$  (e, -e2) times
- 2) Perform the addition
- 3) It is possible that there is a carry-out

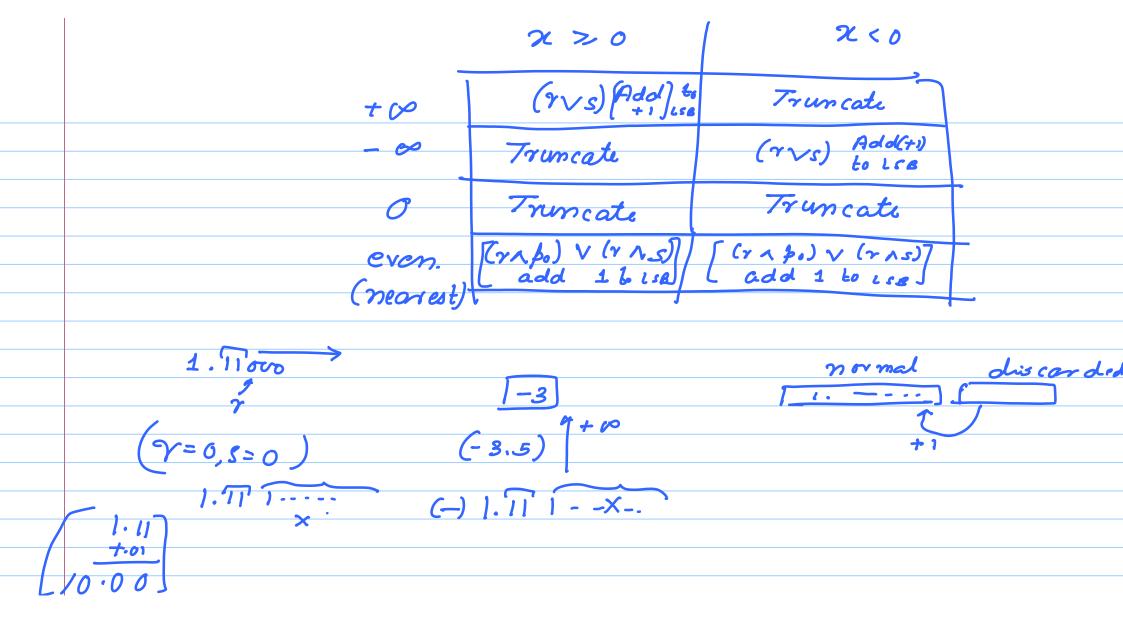
  If there is a carry out

  a) Right shift result
  - b) Increment exponent

4) Round the result

5) Possible: Step 4 might have a carry out If it is so, a) Shift (right), increment enfonent Example (3) once again Intermediate result: 1.0001 IEEE 754 format has four rounding modes. 3.5 4 3 3 4

[precision e2] 1.0001 Example po = 1 1. 11 1 oove these bits po = 1 5 = 0 po = 0 7=0 5 = 1 Given the intermediate result: compute [po, r,s]



Rounding towards [-0] Rounding towards [+0] Round towards O Round Lowards even

binary number

normal

1.11 0001 If, the LSB (fo) = 1) consider number to be odd If LSB [(po)=0] consider number to be evan When are you going to add 1 to the LSB (y NS): discarded bits (D)

3>0.5 x 2<sup>-t</sup>

Add 1 to 158

50-50 case D = 0.5x2-+ (rhpo) add 1 to the USB Big Picture It is fossible that the intermedials result has more 2-han & bits after
the decimal foint.

Normal Discarded

IR = 11. Option 1:

Truncate discorded bits,

option 2:

"" add " add 1 to the

LSB of IR 210 ペフロ [D > 0] add 1 (2>0] -00 add 1 (D > 0.5x2-4)

add 1

or even (D = 0.5x2") 9 (po=1) add 1 1.111 +0.01 1.0.00

Subtraction. 1) Align the numbers. 2) Take a 2s complement of x2 (difference: right shift of a (-) ve number (shift in 1s)) Mulliplication 1. 2 × 2°1 × 1. y × 2°2 (1.2 × 1.y) × 2°1+°2 1) Compute the sign of the result 2) Add the exponents

2) Perform multification.
4) Adjust and round.
5) Adjust again