

# SRISHTI RESEARCH GROUP

COMPUTER SCIENCE & ENGG, IIT DELHI

<http://www.cse.iitd.ac.in/~srsarangi/openings.html>

## Project Position

Topic: A parallel computer architecture simulator

### POST

#### Senior Research Fellow

### QUALIFICATION

- M.Tech in Computer Science, preferably with a good GATE or UGC-Net score, and grades.
- Outstanding B.Tech students can also apply.
- Skills Required: C, Java, Knowledge of basic computer architecture and software engineering

### JOB DESCRIPTION

The candidate will work on the development of an advanced, cycle accurate, and parallel architecture simulator. In specific, we want the candidate to develop the transactional memory subsystem, create a detailed model of an on-chip network, and parallelize the simulator such that it can scale upto hundreds of cores. We wish to release this simulator as an open source project, such that it can be used by research groups all over the world. The candidate needs to be very proficient in programming, especially in C and Java, and have a good aptitude for writing parallel programs using the correct software engineering techniques. There is a possibility of pursuing an advanced degree like M.S/ Ph.D. Please note that these programs have their separate and independent selection procedures, and short-listing criteria.

### COMPENSATION

- Rs. 25,000 p.m. + 30% HRA (if applicable)
- All other benefits that are given to project employees as per IIT rules.

These are only approximate figures. The final amount will be decided after the formal sanction letter is received.

### CONTACT

### PRINCIPAL INVESTIGATOR

Dr. Smruti Ranjan Sarangi



[srsarangi@cse.iitd.ac.in](mailto:srsarangi@cse.iitd.ac.in)



011 - 2659 - 7065, 9650 622 884



<http://www.cse.iitd.ac.in/~srsarangi>