Nov-16-Delhi
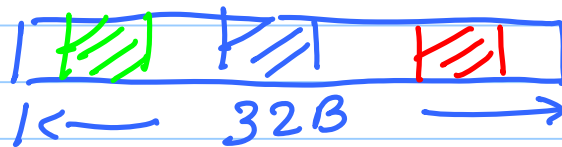
1) To write for the first time

         ↳ You need to fetch the
                 entire block first

|←—　32B　—→|

Two axioms of coherence

1) Writes to one location are seen in the same order by all processors

2) A write operation ultimately succeeds.

3) At any point of time:
   For a memory block.

   1 writer

   OR

   MULTIPLE Readers.

event: action

local read: place read miss on bus

evict: 😊

local read: 😊

remote write:
supply blk.

remote read:
Supply block

local write:
send inv.
fetch data.

local write: send
inv. message

remote read: ① supply block
② write-back

remote write:
supply blk.

evict: write-
back.

Local read/write: 😊

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