MOUNISH MANALA

Intern

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Flat No.302,Madhava Homes, Bank Colony Road No.5,Karimnagar

ABOUT ME

I am interested in VLSI Design and Digital System Design domains. I am familiar with basic programming skills. My strengths include Analytical resolution, Problem solving skills and Critical thinking. I am passionate about consumer hardware products and low power applications/high efficiency computing solutions.

EDUCATION

Bachelor of Technology
Mahatma Gandhi Institute of Technology
2021-2025

WORK EXPERIENCE

Oct - Nov 2021
Academor Learning

VLSI Design Internship

The internship offered a detailed and fundamental knowledge about CMOS Technology production, application and utilization. The software counterparts of logical components consisting of MOS transistors are ventured. The industrial approach of simulating gate level synthesis of such digital circuits is explained.

EXPERTISE

PERL, TCL, Ruby & elementary Python&C

Basic Assembly language and Bash Scripts

Application based programs such as Matlab/Octave, Autocad, Xilinx project Navigator & Keil Uvision

PROJECTS

VLSI DESIGN
Academor Learning

Low Power Noise Amplifier using CMOS

A LNA(Low Noise Amplifier) of given specifications is simulated using circuit design software such as Tina software and Mulitsim. The required LNA is operated at 1.5754 Ghz frequency range, Linear and operates at 0.7V supply with 23.72mW of power consumption. The constituent elements in the LNA are calculated and simulated to result in operation at above specified criterion. The operation and output stability are simulated and recorded until a low power consumption amplifier is designed that was practically viable to be manufactured without off-chip components.

LANGUAGE

English

Telugu

Hindi