xv6 is a re-implementation of Dennis Ritchie's and Ken Thompson's Unix Version 6 (v6). xv6 loosely follows the structure and style of v6, but is implemented for a modern x86-based multiprocessor using ANSI C.

ACKNOWLEDGMENTS

xv6 is inspired by John Lions's Commentary on UNIX 6th Edition (Peer to Peer Communications; ISBN: 1-57398-013-7; 1st edition (June 14, 2000)). See also http://pdos.csail.mit.edu/6.828/2014/xv6.html, which provides pointers to on-line resources for v6.

xv6 borrows code from the following sources:
 JOS (asm.h, elf.h, mmu.h, bootasm.S, ide.c, console.c, and others)
 Plan 9 (entryother.S, mp.h, mp.c, lapic.c)
 FreeBSD (ioapic.c)
 NetBSD (console.c)

The following people have made contributions:
 Russ Cox (context switching, locking)
 Cliff Frey (MP)
 Xiao Yu (MP)
 Nickolai Zeldovich
 Austin Clements

In addition, we are grateful for the bug reports and patches contributed by Silas Boyd-Wickizer, Peter Froehlich, Shivam Handa, Anders Kaseorg, Eddie Kohler, Yandong Mao, Hitoshi Mitake, Carmi Merimovich, Joel Nider, Greg Price, Eldar Sehayek, Yongming Shen, Stephen Tu, and Zouchangwei.

The code in the files that constitute xv6 is Copyright 2006-2014 Frans Kaashoek, Robert Morris, and Russ Cox.

ERROR REPORTS

If you spot errors or have suggestions for improvement, please send email to Frans Kaashoek and Robert Morris (kaashoek.rtm@csail.mit.edu).

BUILDING AND RUNNING XV6

To build xv6 on an x86 ELF machine (like Linux or FreeBSD), run "make". On non-x86 or non-ELF machines (like OS X, even on x86), you will need to install a cross-compiler gcc suite capable of producing x86 ELF binaries. See http://pdos.csail.mit.edu/6.828/2014/tools.html. Then run "make TOOLPREFIX=i386-jos-elf-".

To run xv6, install the OEMU PC simulators. To run in OEMU, run "make gemu".

To create a typeset version of the code, run "make xv6.pdf". This requires the "mpage" utility. See http://www.mesa.nl/pub/mpage/.

The numbers to the left of the file names in the table are sheet numbers. The source code has been printed in a double column format with fifty lines per column, giving one hundred lines per sheet (or page). Thus there is a convenient relationship between line numbers and sheet numbers.

<pre># basic headers 01 types.h 01 param.h 02 memlayout.h 02 defs.h</pre>	36 vectors.pl 36 trapasm.S 37 trap.c 38 syscall.h 39 syscall.c	74 mp.h 75 mp.c 77 lapic.c 80 ioapic.c 81 picirg.c
02 ders.n 04 x86.h	41 sysproc.c	82 kbd.h
06 asm.h	43 halt.c	84 kbd.c
07 mmu.h	# file system	84 console.c
09 elf.h	44 buf.h	88 timer.c
10 ps.h	44 fcntl.h	88 uart.c
-	45 stat.h	
<pre># entering xv6</pre>	45 fs.h	# user-level
10 entry.S	46 file.h	89 initcode.S
11 entryother.S	47 ide.c	90 usys.S
12 main.c	49 bio.c	90 init.c
	51 log.c	91 sh.c
# locks	54 fs.c	
15 spinlock.h	62 file.c	# bootloader
16 spinlock.c	64 sysfile.c	98 bootasm.S
	69 exec.c	99 bootmain.c
# processes		
17 vm.c	# pipes	# add student files her
23 proc.h	71 pipe.c	100 date.c
24 proc.c		100 time.c
33 swtch.S	# string operations	101 ps.c
34 kalloc.c	72 string.c	101 testids.c 102 testpriority.c
# system calls	<pre># low-level hardware</pre>	102 testSched.c
35 traps.h		

The source listing is preceded by a cross-reference that lists every defined constant, struct, global variable, and function in xv6. Each entry gives, on the same line as the name, the line number (or, in a few cases, numbers) where the name is defined. Successive lines in an entry list the line numbers where the name is used. For example, this entry:

```
swtch 2658
0374 2428 2466 2657 2658
```

indicates that swtch is defined on line 2658 and is mentioned on five lines on sheets 03, 24, and 26.

acquire 1624	5454 5474 5817 5825 5829	5257 5263 5264 5338 5339	9728 9737 9738 9744 9745
0380 1624 1628 2591 2620	BBLOCK 4610	5372 5419 5430 5441 5457	9751 9752 9761 9764 9766
2736 2760 2792 2823 2890	4610 5461 5485	5481 5606 5628 5705 5813	9772 9773 9778 9784 9790
2914 2961 2966 3054 3100	B_BUSY 4409	5859 5905 5955 8479 8490	9791 9794
3116 3168 3181 3257 3304	4409 4908 5026 5027 5040	8494 8497 8668 8689 8703	CMOS_PORT 7935
3476 3493 3766 4222 4242	5043 5067 5078 5090	8737 8758 8765 9237 9240	7935 7949 7950 7988
4857 4915 5020 5081 5280	B DIRTY 4411	9241 9242 9355 9367 9369	CMOS RETURN 7936
5307 5324 5381 5658 5691	4411 4843 4866 4871 4910	9372 9373 9374 9377 9378	
5711 5740 5760 5770 6279	4928 5040 5069 5389	9382	CMOS STATA 7975
6304 6318 7163 7184 7205	begin op 5278	B VALID 4410	7975 8023
8510 8681 8727 8763	0336 2787 5278 6333 6424	4410 4870 4910 4928 5057	CMOS STATB 7976
addtofree 2579	6571 6661 6761 6806 6824	bwrite 5065	7976 8016
2579 2621 2663 2737 2848	6856 6970	0266 5065 5068 5230 5263	CMOS TITP 7977
addtoready 2530	bfree 5479	5341	7977 8023
2530 2690 2762 2876 2923	5479 5864 5874 5877	bzero 5439	COM1 8863
2932 3056 3160 3188	haet 5016	5439 5468	8863 8873 8876 8877 8878
allocproc 2586	5016 5048 5056	C 8281 8674	8879 8880 8881 8884 8890
2586 2666 2728	hinit 4989	8281 8329 8354 8355 8356	8891 8907 8909 8917 8919
allocuvm 2003	0263 1281 4989	8357 8358 8360 8674 8684	commit 5351
0428 2003 2017 2709 6996	hman 5810	8687 8694 8705 8738	5203 5323 5351
7008	5572 5810 5836 5010 5060	CADSIOCK 8363	CONSOLE 4687
alltraps 3654	hootmain 0017	8262 820E 8426	1607 0777 0770
2600 2617 2620 2625 2652	0868 0017	0202 0293 0430	gongoleinit 9773
3654	DDD 4607	860E 8663	0260 1277 8773
ALT 8260	1607 1610 E160 E162 E186	alearnteu 2070	gongoleintr 9677
8360 8388 8380	hroad 5052	0/27 2079 2085 7010	0271 8448 8677 8025
argfd 6469	0264 5052	0137 2079 2003 7010	dongolerend 8720
6/60 6E06 6E01 6E00 6E//	E2EE E220 E220 E422 E442	0557 0550 1176 1710 0560	0720 0770
6556	5250 5550 5559 5452 5445 EAG1 EAGE EG10 EG21 E710	0557 0559 1170 1710 0500	0/20 0//0 gongolowyita 0750
argint 3945	5401 5403 5010 5031 5710 E006 E070 E010 E060	and 0116	07E0 0777
0401 2045 2050 2074 4102	5020 5070 5919 5909 brolan E076	0116 0120 0127 0120 0142	0/30 0/// concept a 0651
4206 4220 4206 4204 4206	0265 5076 5070 5221 5222	01/4 0150 0157 0161 0170	0466 0407 0E10 0E26 0E20
4200 4220 4200 4294 4300	0200 00/0 00/9 0201 0202	9144 9152 9157 9101 9170	0400 0497 0310 0330 0339
431/ 4329 4331 04/4 0321	524/ 5204 5342 5343 5434	91/3 91/0 9100 9192 9190	0704 0765
0000 0/00 0020 002/ 0001	5440 540/ 54/2 5492 5010	9204 9220 9230 9319 9331	0/04 0/05
argptr 3954	5019 5040 5720 5832 5870	9335 9336 9452 9455 9457	CONTEXT 2393
0402 3934 4201 4300 0321	0944 0973	0466 0460 0460 0470 0471	0626 0627 0620 2620 2012
0000 0000	P2T7F 4222	9400 9400 9409 9470 9471	2030 2037 2030 2039 3013
argstr 3971	440/ 4555 45/3 4601 460/	94/2 94/3 94/4 94/5 94/6	3041 3228
0403 39/1 6568 6658 6/58	4831 4845 4867 5208 5229	9479 9480 9482 9484 9485	CONV 8032
080/ 0825 085/ 0881	5340 5444 5919 5920 5921	9486 9487 9488 9489 9500	8032 8033 8034 8035 8036
attribute 1360	5965 5969 5970 5971	9501 9503 9505 9506 9507	8037 8038 8039
0272 0368 1259 1360	bui 4400	9508 9509 9510 9513 9514	copyout 2168
BACK 9112	0250 0264 0265 0266 0308	9516 9518 9519 9520 9521	0436 2168 7018 7029
9112 9227 9520 9789	0335 2170 2173 2182 2184	9522 9612 9613 9614 9615	copyuvm 2103
backcmd 9150 9514	4400 4404 4405 4406 4762	9617 9621 9624 9630 9631	0433 2103 2114 2116 2732
9150 9164 9228 9514 9516	47/8 4781 4825 4854 4904	9634 9637 9639 9642 9646	countForever 10258
9642 9755 9790	4906 4909 4977 4981 4985	9648 9650 9653 9655 9658	10258 10286 10290
BACKSPACE 8600	4991 5003 5015 5018 5051	9660 9663 9664 9675 9678	cprintf 8502
8600 8617 8659 8691 8697	5054 5065 5076 5155 5227	9681 9685 9700 9703 9708	0270 1274 1314 2017 2520
balloc 5454	5228 5240 5241 5247 5256	5257 5263 5264 5338 5339 5372 5419 5430 5441 5457 5481 5606 5628 5705 5813 5859 5905 5955 8479 8490 8494 8497 8668 8689 8703 8737 8758 8765 9237 9240 9241 9242 9355 9367 9369 9372 9373 9374 9377 9378 9382 B_VALID 4410 4410 4870 4910 4928 5057 bwrite 5065 0266 5065 5068 5230 5263 5341 bzero 5439 5439 5468 C 8281 8674 8281 8329 8354 8355 8356 8357 8358 8360 8674 8684 8687 8694 8705 8738 CAPSLOCK 8262 8262 8295 8436 cgaputc 8605 8605 8663 clearpteu 2079 0437 2079 2085 7010 cli 0557 0557 0559 1176 1710 8560 8654 9812 cmd 9116 9116 9128 9137 9138 9143 9144 9152 9157 9161 9170 9173 9178 9186 9192 9196 9204 9228 9230 9319 9331 9335 9336 9452 9455 9457 9458 9459 9460 9463 9464 9466 9468 9469 9470 9471 9472 9473 9474 9475 9479 9480 9482 9484 9485 9486 9487 9484 9485 9486 9487 9488 9489 9500 9501 9503 9505 9506 9507 9508 9509 9510 9513 9514 9516 9518 9519 9520 9521 9522 9612 9613 9614 9615 9617 9621 9624 9630 9642 9646 9648 9659 9659 9658 9660 9663 9664 9675 9678 9681 9685 9700 9703 9708 9712 9713 9716 9721 9722	2521 2575 2880 3226 3230

2222 2700 2002 2000 4055	E622 E706 E710	EVEC 0100	0072 0074 0405 0407 10066
1059 4061 4064 4067 4070	dirent 1615	0100 0177 0450 0765	10000
1072 1076 1079 1097 1095	4615 6014 6055 6616 6654	execumd 0120 0452	fork1 0401
4088 4091 4094 4097 4100	dirlink 6052	9120 9133	0155 0107 0207 0214 0220
4103 4106 4109 4112 4115	0288 6021 6052 6067 6075	9720 9103 9170 9433 9433	9133 9197 9207 9214 9229
1103 1100 1103 1112 1113	6501 6730 6743 6744	ovit 2771	forbrot 3064
5572 7660 7690 7011 9112	dirlockup 6011	0250 2771 2012 2755 2750	2471 2620 2064
9502 9562 9563 9564 9567	0280 6011 6017 6050 6175	3910 3939 4059 4169 9066	fragrance 2/51
cpu 2354	6672 6717	0060 0011 0075 0000 0171	2/11 2/2/ 2//0 2/51
0211 1274 1214 1216 1220	00/3 0/1/ DIDCI7 4613	0100 0100 0222 0205 0202	froum 2060
1556 1616 1627 1650 1606	1612 4617 600E 6072 6120	1000 2190 2233 2300 2322	0420 2060 2065 2120 2027
1711 1712 1720 1722 1760	6120 6102 6565 6655 6711	10023 10020 10073 10093	7045 7052
1701 1707 1006 1007 1000	0129 0192 0303 0033 0711	TOT3/ TOT/I TOZI3 TOZ9I	7040 700Z
1/81 1/8/ 1920 192/ 1928	0221 0270	EXTMEM UZUZ	FSSIZE UI0Z
1929 2354 2364 2368 2379	9331 9378	0202 0208 1879	0102 4829
3013 3034 3040 3041 3042	DPL_USER 0//9	Idalloc 6488	gatedesc U9UI
3765 3790 3791 3803 3804	0779 1777 1778 2673 2674	6488 6508 6782 6912	0523 0526 0901 3711
3808 3810 7563 7564 7911	3723 3818 3827	fetchint 3917	getbuiltin 9301
8562	dspri 2885	0404 3917 3947 6888	9301 9326
cpunum 7901	0364 2885 4333	fetchstr 3929	getcallerpcs 1676
0326 1338 1774 7901 8123	dspri2 2865	0405 3929 3976 6894	0381 1638 1676 3228 8565
8132	2865 2892	file 4650	getcmd 9237
CR0_PE 0727	E0ESC 8266	0252 0278 0279 0280 0282	9237 9367
0727 1185 1221 9843	8266 8420 8424 8425 8427	0283 0284 0351 2414 4650	gettoken 9556
CR0_PG 0737	8430	5420 6258 6264 6274 6277	9556 9641 9645 9657 9670
0737 1100 1221	elfhdr 0955	6280 6301 6302 6314 6316	9671 9707 9711 9733
CR0_WP 0733	0955 6965 9919 9924	6352 6365 6402 6463 6469	growproc 2703
0733 1100 1221	ELF_MAGIC 0952	6472 6488 6503 6517 6529	0361 2703 4209
CR4_PSE 0739	0952 6981 9930	6542 6553 6755 6904 7106	havedisk1 4780
0739 1093 1214	ELF_PROG_LOAD 0986	7121 8460 8858 9129 9188	4780 4814 4912
create 6707	0986 6992	9189 9464 9472 9672	holding 1694
6707 6727 6740 6744 6764	end_op 5303	filealloc 6275	0382 1627 1654 1694 3032
6807 6828	0337 2789 5303 6335 6429	0278 6275 6782 7127	HOURS 7981
CRTPORT 8601	6573 6580 6598 6607 6663	fileclose 6314	7981 8004
8601 8610 8611 8612 8613	6697 6702 6766 6771 6777	0279 2782 6314 6320 6547	ialloc 5603
8631 8632 8633 8634	6786 6790 6808 6812 6829	6784 6915 6916 7154 7156	0290 5603 5621 6726 6727
CTL 8259	6833 6858 6864 6869 6972	filedup 6302	IBLOCK 4604
8259 8285 8289 8435	7002 7055	0280 2752 6302 6306 6510	4604 5610 5631 5718
DAY 7982	entry 1090	fileinit 6268	I BUSY 4675
7982 8005	0961 1086 1089 1090 3602	0281 1282 6268	4675 5712 5714 5737 5741
deallocuvm 2032	3603 7042 7421 9921 9945	fileread 6365	5763 5765
0429 2018 2032 2066 2712	9946	0282 6365 6380 6523	TCRHT 7779
DEVSPACE 0204	EOT 7765	filestat 6352	7779 7887 7957 7969
0204 1882 1895	7765 7884 7925	0283 6352 6558	TCRIO 7769
devsw 4680	ERROR 7786	filewrite 6402	7769 7888 7889 7958 7960
4680 4685 5908 5910 5958	4121 7786 7877	0284 6402 6434 6439 6535	7970
5960 6261 8777 8778	FSR 7768	FI. TF 0710	TD 7762
dgpri 3301	7768 7880 7881	0710 1712 1718 2677 2029	2417 2418 2419 7762 7709
0362 3301 4310	7700 7000 7001 evec 6960	EXEC 9108 9108 9177 9459 9765 execcmd 9120 9453 9120 9165 9178 9453 9455 9721 9727 9728 9756 9766 exit 2771 0359 2771 2813 3755 3759 3819 3828 4058 4168 8966 8969 9011 9075 9080 9171 9180 9190 9233 9385 9392 10023 10026 10073 10095 10137 10171 10215 10291 EXTMEM 0202 0202 0208 1879 fdalloc 6488 6488 6508 6782 6912 fetchint 3917 0404 3917 3947 6888 fetchstr 3929 0405 3929 3976 6894 file 4650 0252 0278 0279 0280 0282 0283 0284 0351 2414 4650 5420 6258 6264 6274 6277 6280 6301 6302 6314 6316 6352 6365 6402 6463 6469 6472 6488 6503 6517 6529 6542 6553 6755 6904 7106 7121 8460 8858 9129 9188 9189 9464 9472 9672 filealloc 6275 0278 6275 6782 7127 fileclose 6314 0279 2782 6314 6320 6547 6784 6915 6916 7154 7156 filedup 6302 0280 2752 6302 6306 6510 fileinit 6268 0281 1282 6268 fileread 6365 0282 6365 6380 6523 filestat 6352 0283 6352 6558 filewrite 6402 0284 6402 6434 6439 6535 FL_IF 0710 0710 1712 1718 2677 3038 7908 fork 2723 0360 2723 4055 4162 9010	7016
dinode 4577	0275 4073 6897 6960 9019	fork 2723	TDF BSV 4765
4577 4601 5607 5611 5620	02/3 40/3 009/ 0900 9010 0070 0070 0101 0102 10072	0360 3732 NOEE N163 0010	4765 4789
4071 4001 0007 0011 0029	2010 2012 2101 2107 10017	0300 2723 4033 4102 9010	4/03 4/09

TDD (ND DELD 4550	0224 2056 5006 5000	TDD 4601	0200 2001 0446
IDE_CMD_READ 4770	0334 3076 5206 5209	IPB 4601	0322 3781 8446
4//0 484/	inituvm 1953	4601 4604 5611 5632 5719	KBS_DIB 8253
IDE_CMD_WRITE 4771	0431 1953 1958 2670	iput 5758	8253 8415
47/1 4844	inode 4662	0294 2788 5758 5764 5783	KBSTATP 8252
IDE_DF 4767	0253 0288 0289 0290 0291	6060 6183 6334 6596 6868	8252 8414
4767 4791	0293 0294 0295 0296 0297	IRQ_COM1 3583	KERNBASE 0207
IDE_DRDY 4766	0299 0300 0301 0302 0303	3583 3784 8892 8893	0207 0208 0212 0213 0217
4766 4789	0432 1968 2415 4656 4662	IRQ_ERROR 3585	0218 0220 0221 1365 1683
IDE_ERR 4768	4681 4682 5423 5564 5576	3585 7877	1879 2008 2066
4768 4791	5602 5626 5653 5656 5662	IRQ_IDE 3584	KERNLINK 0208
ideinit 4801	5688 5689 5703 5735 5758	3584 3773 3777 4806 4807	0208 1880
0306 1283 4801	5780 5810 5856 5887 5902	IRQ_KBD 3582	KEY_DEL 8278
ideintr 4852	5952 6010 6011 6052 6056	3582 3780 8781 8782	8278 8319 8341 8365
0307 3774 4852	6154 6157 6189 6200 6566	IRQ_SLAVE 8160	KEY_DN 8272
idelock 4777	6613 6653 6706 6710 6756	8160 8164 8202 8217	8272 8315 8337 8361
4777 4805 4857 4859 4878	6804 6819 6854 6966 8720	IRQ_SPURIOUS 3586	KEY_END 8270
4915 4929 4932	8758	3586 3789 7857	8270 8318 8340 8364
iderw 4904	INPUT_BUF 8666	IRQ_TIMER 3581	KEY_HOME 8269
0308 4904 4909 4911 4913	8666 8668 8689 8701 8703	3581 3764 3823 7864 8830	8269 8318 8340 8364
5058 5070	8705 8737	isdirempty 6613	KEY_INS 8277
idestart 4825	insl 0462	6613 6620 6679	8277 8319 8341 8365
4781 4825 4828 4834 4876	0462 0464 4867 9973	1smp 7565	KEY_LF 8273
4925	install_trans 5222	0340 1284 7565 7662 7670	8273 8317 8339 8363
idewait 4785	5222 5271 5356	7690 7693 8105 8125	KEY_PGDN 8276
4785 4808 4836 4866	INT_DISABLED 8069	1trunc 5856	8276 8316 8338 8362
idtinit 3729	8069 8117	5423 5767 5856	KEY_PGUP 8275
0412 1315 3729	10apic 8077	iunlock 5/35	8275 8316 8338 8362
1dup 5689	7657 7679 7680 8074 8077	0295 5735 5738 5782 6172	KEY_RT 8274
0291 2753 5689 6162	8086 8087 8093 8094 8108	6357 6377 6428 6586 6789	8274 8317 8339 8363
1get 5654	TOAPIC 8058	6867 8725 8762	KEY_UP 8271
55/6 561/ 5654 56/4 60/29	8058 8108	1un1ockput 5/80	82/1 8315 833/ 8361
6160	loapicenable 8123	0296 5780 6167 6176 6179	Kiree 3465
11111 5508	0311 4807 8123 8782 8893	05/9 0592 0595 0000 0080	031/ 2048 2050 20/0 20/3
0292 3075 5568	10apicia /56/	6691 6695 6701 6718 6722	2/33 2835 3456 3465 3470
110CK 57U3	0312 7567 7680 7697 8111	6746 6776 6785 6811 6832	7152 7173
0293 5/03 5/09 5/29 6165	8112	0803 /001 /054	K111 31//
6355 6374 6425 6577 6590	10apicinit 8101	1update 5626	0362 31// 3809 40/0 4185
6003 6067 6675 6715 6719	0313 12/6 8101 8112	029/ 5020 5/69 5882 59/8	9017
6729 6774 6861 6975 8732	10apicread 8084	6585 6605 6689 6694 6733	Kiniti 3430
8/52/8/6/	8084 8109 8110	0/3/	0318 1269 3430
1nb 0453	loapicwrite 8091	I_VALID 46/6	Kinit2 3438
0453 4789 4813 7704 7991	8091 8117 8118 8131 8132	4676 5717 5727 5761	U319 1287 3438
8414 841/ 8611 8613 8884	10_PICI 815/	Kalloc 3488	KSTACKSIZE UI51
8890 8891 8907 8917 8919	8157 8170 8185 8194 8197	0316 1344 1813 1892 1959	0151 1104 1113 1345 1929
9823 9831 9954	82U2 8212 8226 8227	2015 2119 2618 3488 7129	2625
1111110CK 1012	IU_PIUZ 8158	NBUATAP 8254	KVIIIALLOC 1907
U383 1612 2479 3432 3725	8158 8171 8186 8215 8216	8254 8417	0424 1270 1907
4805 4993 5212 5570 6270	821/822U8229823U	KDageta 8406	Tapiceol /922
/135 8//5	TO_LIMEKI 880A	8406 8448	0328 3771 3775 3782 3786
11111109 3200	0009 0010 0020 0829	IPB 4601	3134 1344

lapicinit 7851	0538 0540 1930	1302 1346	2470 2610
0329 1272 1306 7851	makeint 9263	mpinit 7651	NFILE 0154
lapicstartap 7941	9263 9284 9290	0342 1271 7651 7669 7689	0154 6264 6280
0330 1349 7941	mappages 1829	mpioapic 7439	NINDIRECT 4573
lapicw 7795	1829 1898 1961 2022 2122	7439 7657 7679 7681	4573 4574 5822 5872
7795 7857 7863 7864 7865	MAXARG 0158	MPIOAPIC 7453	NINODE 0155
7868 7869 7874 7877 7880	0158 6877 6964 7015	7453 7678	0155 5564 5662
7881 7884 7887 7888 7893	MAXARGS 9114	MPIOINTR 7454	NO 8256
7925 7957 7958 7960 7969	9114 9122 9123 9740	7454 7684	8256 8302 8305 8307 8308
7970	MAXFILE 4574	MPLINTR 7455	8309 8310 8312 8324 8327
lcr3 0590	4574 5965	7455 7685	8329 8330 8331 8332 8334
0590 1918 1933 ladt 0512	MAXOPBLOCKS 0159	mpmain 1312	8352 8353 8355 8356 8357
lgdt 0512	0159 0160 0161 5284	1259 1290 1307 1312	8358
0512 0520 1183 1783 9841	MaxTimeToReset 0163	mpproc 7428	NOFILE 0153
lidt 0526	0163 2915 2954	7428 7656 7667 7676	0153 2414 2750 2780 6476
0526 0534 3731	memcmp 7265	MPPROC 7451	6492
LINTO 7784	0389 7265 7595 7638 8026	7451 7666	NPDENTRIES 0821
7784 7868	memmove 7281	mpsearch 7606	0821 1361 2067
LINT1 7785	0390 1335 1962 2121 2182	7606 7635	NPROC 0150
7785 7869	5229 5340 5433 5638 5725	mpsearch1 7588	0150 2462 2600 2661 2801
LIST 9111	5229 5340 5433 5638 5725 5921 5971 6129 6131 7281 7304 8626	7588 7614 7618 7621	2827 2868 2916 3157 3182
9111 9195 9507 9783 listcmd 9141 9501	7304 8626	multiboot_header 1075	3219 3258 3260 3305
		1074 1075	NPTENTRIES 0822
9141 9166 9196 9501 9503	0391 1816 1894 1960 2021	namecmp 6003	0822 2044
9646 9757 9784	2638 2672 3473 5444 5613	0298 6003 6024 6670	NSEGS 2351
loadgs 0551	6684 6884 7254 8628 9240	0298 6003 6024 6670 namei 6190	1761 2351 2358
0551 1784	9458 9469 9485 9506 9519	0299 2681 6190 6572 6770	nulterminate 9752
loaduvm 1968	microdelay 7931	6857 6971	9615 9630 9752 9773 9779
0432 1968 1974 1977 6998	0331 7931 7959 7961 7971	nameiparent 6201	9780 9785 9786 9791
log 5187 5200	7989 8908	0300 6155 6170 6182 6201	numChildren 10255
5187 5200 5212 5214 5215	min 5422	6588 6662 6713	10255 10283
5216 5226 5227 5228 5240	5422 5920 5970	namex 6155	NUMLOCK 8263
5243 5244 5245 5256 5259	MINS 7980	6155 6193 6203	8263 8296
5260 5261 5272 5280 5282	7980 8003	NBUF 0161	O_CREATE 4453
5283 5284 5286 5288 5289	MONTH 7983	0161 4981 5003	4453 6763 9678 9681
5307 5308 5309 5310 5311	7983 8006	ncpu 7566	O_RDONLY 4450
5313 5316 5318 5324 5325	mp 7402	ncpu 7566 1274 1337 2369 4807 7566 7668 7669 7673 7674 7675	4450 6775 9675
5326 5327 5337 5338 5339	7402 7558 7587 7594 7595	7668 7669 7673 7674 7675	O_RDWR 4452
5353 5357 5376 5378 5381	7596 7605 7610 7614 7615	7695	4452 6796 9063 9065 9359
5382 5383 5386 5387 5388	7618 7619 7630 7633 7635	NCPU 0152	outb 0471
5390	7637 7644 7654 7660 7700	0152 2368 7563	0471 4811 4820 4837 4838
logheader 5182	mpbcpu 7570	NDEV 0156	4839 4840 4841 4842 4844
5182 5194 5208 5209 5241	0341 7570	0156 5908 5958 6261	4847 7703 7704 7949 7950
5257	MPBUS 7452	NDIRECT 4572	7988 8170 8171 8185 8186
LOGSIZE 0160	7452 7683	4572 4574 4583 4673 5815	8194 8197 8202 8212 8215
	mpconf 7413	5820 5824 5825 5862 5869	8216 8217 8220 8226 8227
log_write 5372	7413 7629 7632 7637 7655	5870 5877 5878	8229 8230 8610 8612 8631
0335 5372 5379 5445 5466	mpconfig 7630	NELEM 0440	8632 8633 8634 8827 8828
5491 5615 5639 5830 5972	7630 7660	0440 1897 3222 4130 6886	8829 8873 8876 8877 8878
ltr 0538	mpenter 1302	nextpid 2470	8879 8880 8881 8909 9828

9836 9964 9965 9966 9967	0437 1260 1320 1361 1760	9658 9758 9778	proghdr 0974
9968 9969	1804 1806 1829 1886 1889	piperead 7201	proghdr 0974 0974 6967 9920 9934 PTE_ADDR 0844 0844 1811 1978 2046 2069
outsl 0483	1892 1953 1968 2003 2032	0353 6372 7201	PTE_ADDR 0844
0483 0485 4845	0437 1260 1320 1361 1760 1804 1806 1829 1886 1889 1892 1953 1968 2003 2032 2060 2079 2102 2103 2105 2152 2168 2405 6968 PDX 0812 0812 1809 PDXSHIFT 0827 0812 0818 0827 1365 peek 9601 9601 9625 9640 9644 9656 9669 9705 9709 9724 9732 PGROUNDDOWN 0830 0830 1834 1835 2175 PGROUNDUP 0829 0829 2013 2040 3454 7007 PGSIZE 0823 0823 0829 0830 1360 1816 1844 1845 1894 1957 1960 1961 1973 1975 1979 1982 2014 2021 2022 2041 2044 2112 2121 2122 2179 2185 2671 2678 3455 3469 3473 7008 7010 PHYSTOP 0203 0203 1287 1881 1895 1896 3469 picenable 8175 0346 4806 8175 8781 8830 8892 picinit 8182 0347 1275 8182 picsetmask 8167 8167 8177 8233 pinit 2477	PIPESIZE 7109	0844 1811 1978 2046 2069
outw 0477	2152 2168 2405 6968	7109 7113 7186 7194 7216 pipewrite 7180 0354 6409 7180 popcli 1716	2117 2161
0477 1231 1233 4253 9874	PDX 0812	pipewrite 7180	PTE_FLAGS 0845
9876	0812 1809	0354 6409 7180	0845 2118
O_WRONLY 4451	PDXSHIFT 0827	popcli 1716	PTE_P 0833
4451 6795 6796 9678 9681	0812 0818 0827 1365	0386 1671 1716 1719 1721	
P2V 0218	peek 9601	1934	1839 1841 2045 2068 2115 2157 PTE_PS 0840 0840 1363 1365 pte_t 0848 0848 1803 1807 1811 1813 1832 1971 2034 2081 2106 2154
0218 1269 1287 7612 7951	9601 9625 9640 9644 9656	printdate 10005	2157
8602	9669 9705 9709 9724 9732	10005 10025	PTE_PS 0840
panic 8555 9389	PGROUNDDOWN 0830	printint 8476	0840 1363 1365
0272 1628 1655 1719 1721	0830 1834 1835 2175	8476 8526 8530	pte_t 0848
1840 1896 1932 1958 1974	PGROUNDUP 0829	PrioCount 10254	0848 1803 1807 1811 1813
1977 2048 2065 2085 2114	0829 2013 2040 3454 7007	10254 10264 10271	1832 1971 2034 2081 2106
2116 2533 2557 2669 2777	PGSIZE 0823	proc 2403	2154
2813 2988 2992 2994 2996	0823 0829 0830 1360 1816	0255 0358 0434 1255 1608	PTE_U 0835
2998 3000 3033 3035 3037	1844 1845 1894 1957 1960	1756 1788 1923 1929 2365	0835 1820 1961 2022 2086
3039 3088 3091 3470 3805	1961 1973 1975 1979 1982	2380 2403 2409 2421 2456	2159
4828 4830 4834 4909 4911	2014 2021 2022 2041 2044	2462 2463 2464 2468 2507	PTE_W 0834
4913 5048 5068 5079 5209	2112 2121 2122 2179 2185	2508 2530 2536 2551 2553	0834 1363 1365 1820 1879
5310 5377 5379 5474 5489	2671 2678 3455 3469 3473	2554 2579 2585 2588 2600	1881 1882 1961 2022
5621 5674 5709 5729 5738	7008 7010	2654 2662 2707 2709 2712	PTX 0815
5764 5836 6017 6021 6067	PHYSTOP 0203	2715 2716 2726 2732 2741	0815 1822
6075 6306 6320 6380 6434	0203 1287 1881 1895 1896	2742 2743 2744 2745 2746	PTXSHIFT 0826
6439 6620 6678 6686 6727	3469	2751 2752 2753 2755 2773	0815 0818 0826
6740 6744 8513 8555 8562	nicenable 8175	2776 2781 2782 2783 2788	pushcli 1705
8623 9156 9175 9206 9389	0346 4806 8175 8781 8830	2790 2795 2801 2802 2810	0385 1626 1705 1925
9407 9628 9672 9706 9710	picenable 8175	2821 2827 2828 2855 2861	rcr2 0582
9736 9741 10007	nicinit 8182	2867 2868 2912 2916 2953	0582 3804 3811
panicked 8468	0247 1275 9192	2955 3007 3013 3019 3036	readeflags 0544
8468 8268 8623	niggotmagk 8167	3041 3055 3056 3087 3105	0544 1709 1718 3038 7908
parseblock 9701	9167 9177 9222	3106 3107 3111 3155 3157	read_head 5238
0701 0706 070E	010/ 01// 0233	3179 3182 3215 3219 3253	-
9/01 9/00 9/25	PINIC 2477	3260 3303 3305 3705 3754	5238 5270
parsecmd 9618	0300 12/9 24//	3756 3758 3801 3809 3810	readi 5902 0301 1983 5902 6020 6066
915/ 9302 9010	bibe /III	3/30 3/30 3001 3009 3010	
parseexec 9717	0254 0352 0353 0354 4064 4655 6331 6333 6400 7111	3812 3818 3823 3827 3905	6375 6619 6620 6979 6990
9014 9055 9/1/	4000 0331 0372 0409 7111 7100 7100 7100 7140	3919 3933 3936 3947 3960	readsb 5428
parseline 9635	/123 /129 /135 /139 /143	4129 4132 4133 4138 4139	0287 5213 5428 5484 5571
9612 9624 9635 9646 9708	7161 7180 7201 9013 9205	4157 4191 4208 4225 4269	readsect 9960
parsepipe 9651	9206	4274 4279 4287 4296 4757	9960 9995
9613 9639 9651 9658	PIPE 9110	5416 6162 6461 6476 6493	readseg 9979
parseredirs 9664	9110 9203 9486 9777 pipealloc 7121	6494 6546 6868 6870 6914	9914 9927 9938 9979
9664 9712 9731 9742	pipealloc 7121	6954 7036 7039 7040 7041	recover_from_log 5268
PCINT 7783	0351 6909 7121	7042 7043 7044 7104 7187	5202 5217 5268
	7161 7180 7201 9013 9205 9206 PIPE 9110 9110 9203 9486 9777 pipealloc 7121 0351 6909 7121 pipeclose 7161 0352 6331 7161 pipecmd 9135 9480 9135 9167 9204 9480 9482	7207 7561 7656 7667 7668	REDIR 9109
pde_t 0103	0352 6331 7161	7669 7672 8463 8730 8860	9109 9185 9470 9771
	pipecmd 9135 9480	procdump 3204	redircmd 9126 9464
0430 0431 0432 0433 0436	9135 9167 9204 9480 9482	0367 3204 8715	9126 9168 9186 9464 9466

		sleep 3085 0370 2861 3085 3088 3091 3209 4091 4229 4929 5031 5283 5286 5713 7192 7211 8735 9029 spinlock 1551 0257 0370 0380 0382 0383 0384 0415 1551 1609 1612 1624 1652 1694 2457 2461 3085 3409 3419 3708 3713 4760 4777 4975 4980 5153 5188 5417 5563 6259 6263 7107 7112 8458 8471 8856 STA_R 0669 0786 0669 0786 0669 0786 1240 1775 1777 9884 start 1175 8958 9811 1174 1175 1217 1225 1227 5189 5214 5227 5240 5256 5338 5572 8957 8958 9810 9811 9867 10266 startothers 1324 1258 1286 1324 stat 4504 0258 0283 0302 4504 5414 5887 6352 6459 6554 9053 stati 5887 0302 5887 6356 STA_W 0668 0785 0668 0785 0668 0785 1241 1776 1778 1781 9885 STA_X 0665 0782 0665 0782 0665 0782 1240 1775 1777 9884 sti 0563 0563 0565 1723 2959 stosb 0492 0492 0494 7260 9940 stosl 0501 0501 0503 7258 strlen 7351 0393 7017 7018 7351 9279 9282 9288 9303 9335 9372 9623 strncmp 7308 9253 0394 6005 7308 9253 9280 9281 9283 9287 9289 9304 9305 9309 9335 strncpy 7318 0395 6072 7318	
9675 9678 9681 9759 9772	scheduler 2951	sleep 3085	STS_IG32 0800
REG_ID 8060	0368 1317 2356 2951 3013	0370 2861 3085 3088 3091	0800 0927
8060 8110	3041	3209 4091 4229 4929 5031	STS_T32A 0797
REG_TABLE 8062	SCROLLLOCK 8264	5283 5286 5713 7192 7211	0797 1926
8062 8117 8118 8131 8132	8264 8297	8735 9029	STS_TG32 0801
REG_VER 8061	SECS 7979	spinlock 1551	0801 0927
8061 8109	7979 8002	0257 0370 0380 0382 0383	sum 7576
release 1652	SECTOR_SIZE 4764	0384 0415 1551 1609 1612	7576 7578 7580 7582 7583
0384 1652 1655 2594 2603	4764 4831	1624 1652 1694 2457 2461	7595 7642
2608 2622 2738 2763 2849	SECTSIZE 9912	3085 3409 3419 3708 3713	superblock 4562
2856 2894 2936 2963 2984	9912 9973 9986 9989 9994	4760 4777 4975 4980 5153	0259 0287 4562 5211 5424
3020 3058 3068 3101 3115	SEG 0769	5188 5417 5563 6259 6263	5428
3170 3190 3194 3288 3307	0769 1775 1776 1777 1778	7107 7112 8458 8471 8856	SVR 7766
3311 3481 3498 3769 4226	1781	STA_R 0669 0786	7766 7857
4231 4244 4859 4878 4932	SEG16 0773	0669 0786 1240 1775 1777	switchkvm 1916
5028 5044 5093 5289 5318	0773 1926	9884	0435 1304 1910 1916 3014
5327 5390 5665 5681 5693	SEG_ASM 0660	start 1175 8958 9811	switchuvm 1923
5715 5743 5766 5775 6283	0660 1240 1241 9884 9885	1174 1175 1217 1225 1227	0434 1923 1932 2716 3009
6287 6308 6322 6328 7172	segdesc 0752	5189 5214 5227 5240 5256	7044
7175 7188 7197 7208 7219	0509 0512 0752 0769 0773	5338 5572 8957 8958 9810	swtch 3358
8551 8713 8731 8751 8766	1761 2358	9811 9867 10266	0377 3013 3041 3357 3358
removefromready 2551	seginit 1766	startothers 1324	syscall 4126
2551 2874 2921 2930 3010	0423 1273 1305 1766	1258 1286 1324	0406 3757 3907 4052 4126
resetpriority 2909	SEG KCODE 0741	stat 4504	SYSCALL 9003 9010 9011 9012 9013 90
2909 2965	0741 1200 1775 3722 3723	0258 0283 0302 4504 5414	9010 9011 9012 9013 9014
ROOTDEV 0157	9853	5887 6352 6459 6554 9053	9015 9016 9017 9018 9019
0157 3075 3076 6160	SEG KCPU 0743	stati 5887	9020 9021 9022 9023 9024
ROOTINO 4554	0743 1781 1784 3666	0302 5887 6356	9025 9026 9027 9028 9029
4554 6160	SEG KDATA 0742	STA W 0668 0785	9030 9031 9032 9033 9034
run 3414	0742 1204 1776 1928 3663	0668 0785 1241 1776 1778	9035 9036 9037 9038 9039
3211 3285 3414 3415 3421	9858	1781 9885	9040
3467 3477 3490 10065	SEG NULLASM 0654	STA X 0665 0782	svs chdir 6851
runcmd 9161	0654 1239 9883	0665 0782 1240 1775 1777	3979 4019 6851
9161 9175 9192 9198 9200	SEG TSS 0746	9884	SYS chdir 3859
9212 9219 9230 9382	0746 1926 1927 1930	sti 0563	3859 4019
RUNNING 2400	SEG UCODE 0744	0563 0565 1723 2959	svs close 6539
2400 2870 2999 3011 3036	0744 1777 2673	stosb 0492	3980 4031 6539
3211 3275 3823 10128	SEG IDATA 0745	0492 0494 7260 9940	SYS close 3871
safestropy 7332	0745 1778 2674	stosl 0501	3871 4031
0392 2680 2755 3281 7036	setbuiltin 9275	0501 0503 7258	svs date 4258
7332	9275 9325	strlen 7351	4001 4033 4258
sh 5424	SETGATE 0921	0393 7017 7018 7351 9279	SYS date 3873
0287 4604 4610 5211 5213	0921 3722 3723	9282 9288 9303 9335 9372	3873 4033
5214 5215 5424 5428 5433	setupkym 1887	9623	sys dup 6501
5460 5461 5462 5484 5485	0426 1887 1909 2110 2668	strncmp 7308 9253	3981 4020 6501
5571 5572 5573 5609 5610	6984	0394 6005 7308 9253 9280	SYS dup 3860
5631 5718 8014 8016 8018	SHIFT 8258	9281 9283 9287 9289 9304	3860 4020
ached 3028	8258 8286 8287 8435	9305 9309 9335	gyg eyec 6875
0369 2812 3028 3033 3035	gkinelem 6115	strncny 7318	3982 4017 6875
3037 3039 3057 3109	6115 6164	0395 6072 7318	SVS eyec 3857
3031 3032 3031 3102	0113 0101	0373 0012 1310	515_CACC 5057

3857 4017 8962	3870 4030	3998 4026 6527	0602 2410 2629 3751
sys_exit 4166	sys_mknod 6817	SYS write 3866	trapret 3677
3983 4012 4166	3990 4027 6817	3866 4026	2472 2634 3676 3677
SYS_exit 3852	SYS_mknod 3867	taskstate 0851	T_SYSCALL 3576
3852 4012 8967	3867 4027	0851 2357	3576 3723 3753 8963 8968
sys fork 4160	sys_open 6751	TDCR 7790	9007
3984 4011 4160	3991 4025 6751	7790 7863	tvinit 3717
SYS_fork 3851	SYS_open 3865	T_DEV 4502	0414 1280 3717
3851 4011	3865 4025	4502 5907 5957 6828	uart 8865
sys fstat 6551	sys_pipe 6901	T DIR 4500	8865 8886 8905 8915
3985 4018 6551	3992 4014 6901	4500 6016 6166 6578 6679	uartgetc 8913
SYS_fstat 3858	SYS_pipe 3854	6687 6735 6775 6807 6862	8913 8925
3858 4018	3854 4014	T FILE 4501	uartinit 8868
sys_getgid 4273	sys_read 6515	4501 6720 6764	0418 1278 8868
4003 4035 4273	3993 4015 6515	ticks 3714	uartintr 8923
			0419 3785 8923
SYS_getgid 3875	SYS_read 3855	0413 3714 3767 3768 4223	
3875 4035	3855 4015	4224 4229 4243	uartputc 8901
sys_getpid 4189	sys_sbrk 4201	tickslock 3713	0420 8660 8662 8897 8901
3986 4021 4189	3994 4022 4201	0415 3713 3725 3766 3769	uproc 1002
SYS_getpid 3861	SYS_sbrk 3862	4222 4226 4229 4231 4242 4244	0260 0363 1002 3251 4303
3861 4021	3862 4022		10107
sys_getppid 4278	sys_setgid 4292	TICR 7788	userinit 2652
4004 4036 4278	4006 4038 4292	7788 7865	0371 1288 2652 2669
SYS_getppid 3876	SYS_setgid 3878	TIMER 7780	uva2ka 2152
3876 4036	3878 4038	7780 7864	0427 2152 2176
sys_getpriority 4315	sys_setpriority 4324	TIMER_16BIT 8821	V2P 0217
4008 4040 4315	4009 4041 4324	8821 8827	0217 1880 1881
SYS_getpriority 3880	SYS_setpriority 3881	TIMER_DIV 8816	V2P_WO 0220
3880 4040	3881 4041	8816 8828 8829	0220 1086 1096
sys_getprocs 4301	sys_setuid 4283	TIMER_FREQ 8815	VER 7763
4007 4039 4301	4005 4037 4283	8815 8816	7763 7873
SYS_getprocs 3879	SYS_setuid 3877	timerinit 8824	wait 2819
3879 4039	3877 4037	0409 1285 8824	0372 2819 4061 4175 9012
sys_getuid 4268	sys_sleep 4215	TIMER_MODE 8818	9082 9199 9223 9224 9383
4002 4034 4268	3995 4023 4215	8818 8827	10070
SYS_getuid 3874	SYS_sleep 3863	TIMER_RATEGEN 8820	waitdisk 9951
3874 4034	3863 4023	8820 8827	9951 9963 9972
SYS_halt 3872	sys_unlink 6651	TIMER_SELO 8819	wakeup 3166
3872 4032	3996 4028 6651	8819 8827	0373 3166 3768 4872 5091
sys_kill 4179	SYS_unlink 3868	T_IRQ0 3579	5316 5326 5742 5772 7166
3987 4016 4179	3868 4028	3579 3764 3773 3777 3780	7169 7191 7196 7218 8707
SYS_kill 3856	sys_uptime 4238	3784 3788 3789 3823 7857	wakeup1 3153
3856 4016	3999 4024 4238	7864 7877 8117 8131 8197	2474 2795 2805 3153 3169
sys_link 6563	SYS_uptime 3864	8216	walkpgdir 1804
3988 4029 6563	3864 4024	TPR 7764	1804 1837 1976 2042 2083
SYS_link 3869	sys_wait 4173	7764 7893	2113 2156
3869 4029	3997 4013 4173	trap 3751	write_head 5254
sys_mkdir 6801	SYS_wait 3853	3602 3604 3672 3751 3803	5254 5273 5355 5358
3989 4030 6801	3853 4013	3805 3808	writei 5952
SYS_mkdir 3870	sys_write 6527	trapframe 0602	0303 5952 6074 6426 6685

May 16 22:03 2016 cross-references Page 15

6686 write_log 5333 5333 5354 xchg 0569 0569 1316 1633 1669 YEAR 7984 7984 8007 yield 3052 0374 3052 3824

0101 0102	typedef typedef	unsigned unsigned unsigned uint pde_	short char	ushort;	
0112 0113 0114 0115 0116					
0117 0118 0119 0120 0121 0122					
0123 0124 0125 0126 0127					
0128 0129 0130 0131 0132					
0133 0134 0135 0136 0137					
0138 0139 0140 0141 0142 0143					
0143 0144 0145 0146 0147 0148 0149					

```
0150 #define NPROC
                         64 // maximum number of processes
0151 #define KSTACKSIZE 4096 // size of per-process kernel stack
0152 #define NCPU
                          8 // maximum number of CPUs
0153 #define NOFILE
                         16 // open files per process
0154 #define NFILE
                        100 // open files per system
0155 #define NINODE
                         50 // maximum number of active i-nodes
0156 #define NDEV
                         10 // maximum major device number
0157 #define ROOTDEV
                          1 // device number of file system root disk
0158 #define MAXARG
                         32 // max exec arguments
0159 #define MAXOPBLOCKS 10 // max # of blocks any FS op writes
0160 #define LOGSIZE
                         (MAXOPBLOCKS*3) // max data blocks in on-disk log
0161 #define NBUF
                         (MAXOPBLOCKS*3) // size of disk block cache
0162 #define FSSIZE
                         1000 // size of file system in blocks
0163 #define MaxTimeToReset 99999999
0164
0165
0166
0167
0168
0169
0170
0171
0172
0173
0174
0175
0176
0177
0178
0179
0180
0181
0182
0183
0184
0185
0186
0187
0188
0189
0190
0191
0192
0193
0194
0195
0196
0197
0198
0199
```

Sheet 01 Sheet 01

```
0250 struct buf;
0200 // Memory layout
0201
                                                                                  0251 struct context;
0202 #define EXTMEM 0x100000
                                         // Start of extended memory
                                                                                  0252 struct file;
0203 #define PHYSTOP 0xE000000
                                                                                  0253 struct inode;
                                         // Top physical memory
0204 #define DEVSPACE 0xFE000000
                                         // Other devices are at high addresses
                                                                                 0254 struct pipe;
0205
                                                                                  0255 struct proc;
0206 // Key addresses for address space layout (see kmap in vm.c for layout)
                                                                                  0256 struct rtcdate;
                                         // First kernel virtual address
0207 #define KERNBASE 0x80000000
                                                                                  0257 struct spinlock;
0208 #define KERNLINK (KERNBASE+EXTMEM) // Address where kernel is linked
                                                                                  0258 struct stat;
                                                                                  0259 struct superblock;
0209
0210 #ifndef __ASSEMBLER__
                                                                                  0260 struct uproc;
0211
                                                                                  0261
0212 static inline uint v2p(void *a) { return ((uint) (a)) - KERNBASE; }
                                                                                  0262 // bio.c
0213 static inline void *p2v(uint a) { return (void *) ((a) + KERNBASE); }
                                                                                  0263 void
                                                                                                       binit(void);
                                                                                  0264 struct buf*
                                                                                                       bread(uint, uint);
0214
0215 #endif
                                                                                  0265 void
                                                                                                       brelse(struct buf*);
0216
                                                                                  0266 void
                                                                                                       bwrite(struct buf*);
0217 #define V2P(a) (((uint) (a)) - KERNBASE)
                                                                                  0267
0218 #define P2V(a) (((void *) (a)) + KERNBASE)
                                                                                  0268 // console.c
                                                                                  0269 void
                                                                                                       consoleinit(void);
                                                                                  0270 void
0220 #define V2P WO(x) ((x) - KERNBASE)
                                          // same as V2P, but without casts
                                                                                                       cprintf(char*, ...);
0221 #define P2V_WO(x) ((x) + KERNBASE)
                                          // same as P2V, but without casts
                                                                                  0271 void
                                                                                                       consoleintr(int(*)(void));
0222
                                                                                  0272 void
                                                                                                       panic(char*) attribute ((noreturn));
0223
                                                                                  0273
0224
                                                                                  0274 // exec.c
0225
                                                                                  0275 int
                                                                                                       exec(char*, char**);
0226
                                                                                  0276
0227
                                                                                  0277 // file.c
0228
                                                                                  0278 struct file*
                                                                                                       filealloc(void);
0229
                                                                                  0279 void
                                                                                                       fileclose(struct file*);
0230
                                                                                  0280 struct file*
                                                                                                       filedup(struct file*);
0231
                                                                                  0281 void
                                                                                                       fileinit(void);
0232
                                                                                  0282 int
                                                                                                       fileread(struct file*, char*, int n);
0233
                                                                                  0283 int.
                                                                                                       filestat(struct file*, struct stat*);
0234
                                                                                  0284 int.
                                                                                                       filewrite(struct file*, char*, int n);
0235
                                                                                  0285
0236
                                                                                  0286 // fs.c
0237
                                                                                  0287 void
                                                                                                       readsb(int dev, struct superblock *sb);
0238
                                                                                  0288 int
                                                                                                       dirlink(struct inode*, char*, uint);
0239
                                                                                  0289 struct inode*
                                                                                                       dirlookup(struct inode*, char*, uint*);
0240
                                                                                  0290 struct inode*
                                                                                                       ialloc(uint, short);
                                                                                  0291 struct inode*
0241
                                                                                                       idup(struct inode*);
0242
                                                                                  0292 void
                                                                                                       iinit(int dev);
0243
                                                                                  0293 void
                                                                                                       ilock(struct inode*);
0244
                                                                                  0294 void
                                                                                                       iput(struct inode*);
0245
                                                                                  0295 void
                                                                                                       iunlock(struct inode*);
0246
                                                                                  0296 void
                                                                                                       iunlockput(struct inode*);
0247
                                                                                 0297 void
                                                                                                       iupdate(struct inode*);
0248
                                                                                  0298 int.
                                                                                                       namecmp(const char*, const char*);
0249
                                                                                  0299 struct inode*
                                                                                                       namei(char*);
```

Sheet 02 Sheet 02

0200		0250 // '	
0300 struct inode*	nameiparent(char*, char*);	0350 // pipe.c	
0301 int	readi(struct inode*, char*, uint, uint);	0351 int	<pre>pipealloc(struct file**, struct file**);</pre>
0302 void	<pre>stati(struct inode*, struct stat*);</pre>	0352 void	<pre>pipeclose(struct pipe*, int);</pre>
0303 int	<pre>writei(struct inode*, char*, uint, uint);</pre>	0353 int	<pre>piperead(struct pipe*, char*, int);</pre>
0304		0354 int	<pre>pipewrite(struct pipe*, char*, int);</pre>
0305 // ide.c		0355	
0306 void	<pre>ideinit(void);</pre>	0356	
0307 void	ideintr(void);	0357 // proc.c	
0308 void	<pre>iderw(struct buf*);</pre>	0358 struct proc*	copyproc(struct proc*);
0309		0359 void	exit(void);
0310 // ioapic.c		0360 int	fork(void);
0311 void	<pre>ioapicenable(int irg, int cpu);</pre>	0361 int	<pre>growproc(int);</pre>
0312 extern uchar	ioapicid;	0362 int	kill(int);
0313 void	ioapicinit(void);	0363 int	<pre>dogetprocs(int max, struct uproc *table);</pre>
0314	Toapicinit (Void) /	0364 int	
			<pre>dspri(int pid, int priority);</pre>
0315 // kalloc.c	1 2.2	0365 int	<pre>dgpri(int pid);</pre>
0316 char*	kalloc(void);	0366 void	pinit(void);
0317 void	kfree(char*);	0367 void	<pre>procdump(void);</pre>
0318 void	kinit1(void*, void*);	0368 void	<pre>scheduler(void)attribute((noreturn));</pre>
0319 void	<pre>kinit2(void*, void*);</pre>	0369 void	sched(void);
0320		0370 void	<pre>sleep(void*, struct spinlock*);</pre>
0321 // kbd.c		0371 void	userinit(void);
0322 void	kbdintr(void);	0372 int	<pre>wait(void);</pre>
0323		0373 void	<pre>wakeup(void*);</pre>
0324 // lapic.c		0374 void	<pre>yield(void);</pre>
0325 void	<pre>cmostime(struct rtcdate *r);</pre>	0375	
0326 int	cpunum(void);	0376 // swtch.S	
0327 extern volatile		0377 void	<pre>swtch(struct context**, struct context*);</pre>
0328 void	lapiceoi(void);	0378	, , , , , , , , , , , , , , , , , , , ,
0329 void	lapicinit(void);	0379 // spinlock.c	
0330 void	lapicstartap(uchar, uint);	0380 void	<pre>acquire(struct spinlock*);</pre>
0331 void	microdelay(int);	0381 void	<pre>qetcallerpcs(void*, uint*);</pre>
0332	microderay (inc) r	0382 int	holding(struct spinlock*);
0333 // log.c		0383 void	<pre>initlock(struct spinlock*, char*);</pre>
0334 void	initles(int down):	0384 void	
	initlog(int dev);		release(struct spinlock*);
0335 void	<pre>log_write(struct buf*);</pre>	0385 void	pushcli(void);
0336 void	begin_op();	0386 void	<pre>popcli(void);</pre>
0337 void	end_op();	0387	
0338		0388 // string.c	
0339 // mp.c		0389 int	<pre>memcmp(const void*, const void*, uint);</pre>
0340 extern int	ismp;	0390 void*	<pre>memmove(void*, const void*, uint);</pre>
0341 int	<pre>mpbcpu(void);</pre>	0391 void*	<pre>memset(void*, int, uint);</pre>
0342 void	<pre>mpinit(void);</pre>	0392 char*	<pre>safestrcpy(char*, const char*, int);</pre>
0343 void	<pre>mpstartthem(void);</pre>	0393 int	strlen(const char*);
0344		0394 int	<pre>strncmp(const char*, const char*, uint);</pre>
0345 // picirq.c		0395 char*	<pre>strncpy(char*, const char*, int);</pre>
0346 void	<pre>picenable(int);</pre>	0396	
0347 void	picinit(void);	0397	
0348		0398	
0349		0399	

Sheet 03 Sheet 03

```
0400 // syscall.c
                                                                                 0450 // Routines to let C code use special x86 instructions.
0401 int
                     argint(int, int*);
                                                                                 0451
0402 int.
                     argptr(int, char**, int);
                                                                                 0452 static inline uchar
0403 int
                     argstr(int, char**);
                                                                                 0453 inb(ushort port)
0404 int
                     fetchint(uint, int*);
                                                                                 0454 {
0405 int
                     fetchstr(uint, char**);
                                                                                 0455 uchar data;
0406 void
                     syscall(void);
                                                                                 0456
0407
                                                                                 0457 asm volatile("in %1,%0": "=a" (data): "d" (port));
0408 // timer.c
                                                                                 0458 return data;
0409 void
                     timerinit(void);
                                                                                 0459 }
0410
                                                                                 0460
0411 // trap.c
                                                                                 0461 static inline void
0412 void
                     idtinit(void);
                                                                                 0462 insl(int port, void *addr, int cnt)
0413 extern uint
                     ticks;
                                                                                 0463 {
0414 void
                     tvinit(void);
                                                                                 0464 asm volatile("cld; rep insl":
                                                                                                     "=D" (addr), "=c" (cnt) :
0415 extern struct spinlock tickslock;
                                                                                 0465
0416
                                                                                 0466
                                                                                                     "d" (port), "0" (addr), "1" (cnt) :
0417 // uart.c
                                                                                 0467
                                                                                                     "memory", "cc");
0418 void
                     uartinit(void);
                                                                                 0468 }
0419 void
                     uartintr(void);
                                                                                 0469
                                                                                 0470 static inline void
0420 void
                     uartputc(int);
                                                                                 0471 outb(ushort port, uchar data)
0421
0422 // vm.c
                                                                                 0472 {
0423 void
                     seginit(void);
                                                                                 0473 asm volatile("out %0,%1" : : "a" (data), "d" (port));
0424 void
                     kvmalloc(void);
                                                                                 0474 }
0425 void
                     vmenable(void);
                                                                                 0475
                                                                                 0476 static inline void
0426 pde_t*
                     setupkvm(void);
0427 char*
                     uva2ka(pde_t*, char*);
                                                                                 0477 outw(ushort port, ushort data)
0428 int.
                     allocuvm(pde t*, uint, uint);
0429 int
                     deallocuvm(pde_t*, uint, uint);
                                                                                 0479 asm volatile("out %0,%1" : : "a" (data), "d" (port));
0430 void
                     freevm(pde_t*);
                                                                                 0480 }
                     inituvm(pde_t*, char*, uint);
0431 void
                                                                                 0481
                     loaduvm(pde_t*, char*, struct inode*, uint, uint);
                                                                                 0482 static inline void
0432 int
0433 pde_t*
                     copyuvm(pde_t*, uint);
                                                                                 0483 outsl(int port, const void *addr, int cnt)
0434 void
                     switchuvm(struct proc*);
                                                                                 0484 {
0435 void
                     switchkvm(void);
                                                                                 0485 asm volatile("cld; rep outsl" :
                                                                                                     "=S" (addr), "=c" (cnt) :
0436 int
                     copyout(pde_t*, uint, void*, uint);
                                                                                 0486
0437 void
                     clearpteu(pde_t *pgdir, char *uva);
                                                                                 0487
                                                                                                      "d" (port), "0" (addr), "1" (cnt) :
0438
                                                                                 0488
                                                                                                      "cc");
0439 // number of elements in fixed-size array
                                                                                 0489 }
0440 #define NELEM(x) (sizeof(x)/sizeof((x)[0]))
                                                                                 0490
                                                                                 0491 static inline void
0441
0442
                                                                                 0492 stosb(void *addr, int data, int cnt)
0443
                                                                                 0493 {
0444
                                                                                 0494 asm volatile("cld; rep stosb" :
0445
                                                                                 0495
                                                                                                     "=D" (addr), "=c" (cnt) :
                                                                                                      "0" (addr), "1" (cnt), "a" (data) :
0446
                                                                                 0496
0447
                                                                                 0497
                                                                                                     "memory", "cc");
0448
                                                                                 0498 }
0449
                                                                                 0499
```

Sheet 04

```
0500 static inline void
0501 stosl(void *addr, int data, int cnt)
0502 {
0503 asm volatile("cld; rep stosl" :
                   "=D" (addr), "=c" (cnt) :
0504
0505
                   "0" (addr), "1" (cnt), "a" (data) :
0506
                   "memory", "cc");
0507 }
0508
0509 struct segdesc;
0510
0511 static inline void
0512 lqdt(struct segdesc *p, int size)
0513 {
0514 volatile ushort pd[3];
0515
0516 pd[0] = size-1;
0517 \text{ pd}[1] = (uint)p;
0518 pd[2] = (uint)p >> 16;
0519
0520 asm volatile("lqdt (%0)" : : "r" (pd));
0521 }
0522
0523 struct gatedesc;
0524
0525 static inline void
0526 lidt(struct gatedesc *p, int size)
0527 {
0528 volatile ushort pd[3];
0529
0530 pd[0] = size-1;
0531 pd[1] = (uint)p;
0532 pd[2] = (uint)p >> 16;
0533
0534 asm volatile("lidt (%0)" : : "r" (pd));
0535 }
0536
0537 static inline void
0538 ltr(ushort sel)
0539 {
0540 asm volatile("ltr %0" : : "r" (sel));
0541 }
0542
0543 static inline uint
0544 readeflags(void)
0545 {
0546 uint eflags;
0547 asm volatile("pushfl; popl %0" : "=r" (eflags));
0548 return eflags;
0549 }
```

```
0550 static inline void
0551 loadqs(ushort v)
0552 {
0553 asm volatile("movw %0, %%qs" : : "r" (v));
0554 }
0555
0556 static inline void
0557 cli(void)
0558 {
0559 asm volatile("cli");
0560 }
0561
0562 static inline void
0563 sti(void)
0564 {
0565 asm volatile("sti");
0566 }
0567
0568 static inline uint
0569 xchg(volatile uint *addr, uint newval)
0570 {
0571 uint result;
0572
0573 // The + in "+m" denotes a read-modify-write operand.
0574 asm volatile("lock; xchql %0, %1":
0575
                  "+m" (*addr), "=a" (result) :
0576
                   "1" (newval) :
0577
                   "cc");
0578 return result;
0579 }
0580
0581 static inline uint
0582 rcr2(void)
0583 {
0584 uint val;
0585 asm volatile("movl %%cr2,%0" : "=r" (val));
0586 return val;
0587 }
0588
0589 static inline void
0590 lcr3(uint val)
0591 {
0592 asm volatile("movl %0,%%cr3" : : "r" (val));
0593 }
0594
0595
0596
0597
0598
0599
```

```
0650 //
0600 // Layout of the trap frame built on the stack by the
0601 // hardware and by trapasm.S, and passed to trap().
                                                                               0651 // assembler macros to create x86 segments
0602 struct trapframe {
                                                                               0652 //
0603 // registers as pushed by pusha
                                                                               0653
0604 uint edi;
                                                                               0654 #define SEG_NULLASM
0605 uint esi;
                                                                               0655
                                                                                            .word 0, 0;
0606 uint ebp;
                                                                               0656
                                                                                            .byte 0, 0, 0, 0
                                                                               0657
0607 uint oesp;
                      // useless & ignored
0608 uint ebx;
                                                                               0658 // The 0xC0 means the limit is in 4096-byte units
0609 uint edx;
                                                                               0659 // and (for executable segments) 32-bit mode.
0610 uint ecx;
                                                                               0660 #define SEG_ASM(type,base,lim)
0611 uint eax;
                                                                                            .word (((lim) >> 12) & 0xffff), ((base) & 0xffff);
                                                                               0661
0612
                                                                               0662
                                                                                            .byte (((base) >> 16) & 0xff), (0x90 \mid (type)),
0613 // rest of trap frame
                                                                               0663
                                                                                                    (0xC0 | (((lim) >> 28) & 0xf)), (((base) >> 24) & 0xff)
0614 ushort qs;
                                                                               0664
0615 ushort padding1;
                                                                               0665 #define STA_X
                                                                                                      0x8
                                                                                                                // Executable segment
0616 ushort fs;
                                                                               0666 #define STA E
                                                                                                      0x4
                                                                                                               // Expand down (non-executable segments)
0617
      ushort padding2;
                                                                               0667 #define STA C
                                                                                                      0x4
                                                                                                               // Conforming code segment (executable only)
0618 ushort es;
                                                                               0668 #define STA_W
                                                                                                      0x2
                                                                                                               // Writeable (non-executable segments)
0619
      ushort padding3;
                                                                               0669 #define STA R
                                                                                                      0x2
                                                                                                               // Readable (executable segments)
                                                                                                      0x1
0620
      ushort ds;
                                                                               0670 #define STA_A
                                                                                                               // Accessed
0621
      ushort padding4;
                                                                               0671
0622 uint trapno;
                                                                               0672
0623
                                                                               0673
0624 // below here defined by x86 hardware
                                                                               0674
0625 uint err;
                                                                               0675
0626 uint eip;
                                                                               0676
0627 ushort cs;
                                                                               0677
0628 ushort padding5;
                                                                               0678
0629 uint eflags;
                                                                               0679
0630
                                                                               0680
0631 // below here only when crossing rings, such as from user to kernel
                                                                               0681
0632 uint esp;
                                                                               0682
0633
      ushort ss;
                                                                               0683
0634
      ushort padding6;
                                                                               0684
0635 };
                                                                               0685
0636
                                                                               0686
0637
                                                                               0687
0638
                                                                               0688
0639
                                                                               0689
0640
                                                                               0690
0641
                                                                               0691
0642
                                                                               0692
0643
                                                                               0693
0644
                                                                               0694
0645
                                                                               0695
0646
                                                                               0696
0647
                                                                               0697
0648
                                                                               0698
0649
                                                                               0699
```

Sheet 06 Sheet 06

```
0750 #ifndef __ASSEMBLER_
0700 // This file contains definitions for the
0701 // x86 memory management unit (MMU).
                                                                                 0751 // Segment Descriptor
0702
                                                                                 0752 struct segdesc {
                                                                                 0753 uint lim_15_0 : 16; // Low bits of segment limit
0703 // Eflags register
0704 #define FL_CF
                             0x0000001
                                            // Carry Flag
                                                                                 0754
                                                                                       uint base_15_0 : 16; // Low bits of segment base address
0705 #define FL PF
                             0x00000004
                                            // Parity Flag
                                                                                 0755 uint base 23 16 : 8; // Middle bits of segment base address
0706 #define FL_AF
                             0x00000010
                                            // Auxiliary carry Flag
                                                                                 0756 uint type : 4;
                                                                                                             // Segment type (see STS_ constants)
0707 #define FL_ZF
                             0x00000040
                                            // Zero Flag
                                                                                 0757 uint s : 1;
                                                                                                             // 0 = system, 1 = application
0708 #define FL SF
                             0x00000080
                                            // Sign Flag
                                                                                 0758 uint dpl : 2;
                                                                                                             // Descriptor Privilege Level
0709 #define FL_TF
                                            // Trap Flag
                                                                                 0759
                                                                                       uint p:1;
                                                                                                             // Present
                             0x00000100
0710 #define FL_IF
                             0x00000200
                                            // Interrupt Enable
                                                                                 0760
                                                                                       uint lim_19_16 : 4; // High bits of segment limit
                                            // Direction Flag
                                                                                      uint avl : 1;
                                                                                                             // Unused (available for software use)
0711 #define FL_DF
                             0 \times 00000400
                                                                                 0761
0712 #define FL_OF
                             0x00000800
                                            // Overflow Flag
                                                                                 0762 uint rsv1 : 1;
                                                                                                             // Reserved
0713 #define FL_IOPL_MASK
                             0x00003000
                                            // I/O Privilege Level bitmask
                                                                                 0763
                                                                                       uint db : 1;
                                                                                                             // 0 = 16-bit segment, 1 = 32-bit segment
0714 #define FL IOPL 0
                                            // IOPL == 0
                                                                                 0764 uint q : 1;
                                                                                                            // Granularity: limit scaled by 4K when set
                             0x00000000
0715 #define FL_IOPL_1
                             0x00001000
                                            // IOPL == 1
                                                                                 0765 uint base_31_24 : 8; // High bits of segment base address
0716 #define FL IOPL 2
                             0x00002000
                                            // IOPL == 2
                                                                                 0766 };
0717 #define FL IOPL 3
                             0x00003000
                                            // IOPL == 3
                                                                                 0767
0718 #define FL_NT
                             0x00004000
                                            // Nested Task
                                                                                 0768 // Normal segment
0719 #define FL RF
                             0x00010000
                                            // Resume Flag
                                                                                 0769 #define SEG(type, base, lim, dpl) (struct segdesc)
                                                                                 0770 { ((lim) >> 12) & 0xffff, (uint)(base) & 0xffff,
0720 #define FL VM
                             0x00020000
                                            // Virtual 8086 mode
0721 #define FL AC
                             0 \times 00040000
                                            // Alignment Check
                                                                                 0771 ((uint)(base) >> 16) & 0xff, type, 1, dpl, 1,
                                                                                 0772 (uint)(lim) >> 28, 0, 0, 1, 1, (uint)(base) >> 24 }
0722 #define FL VIF
                             0x00080000
                                            // Virtual Interrupt Flag
0723 #define FL_VIP
                             0x00100000
                                            // Virtual Interrupt Pending
                                                                                 0773 #define SEG16(type, base, lim, dpl) (struct segdesc)
0724 #define FL ID
                                            // ID flag
                                                                                 0774 { (lim) & 0xffff, (uint)(base) & 0xffff,
                             0 \times 00200000
0725
                                                                                 0775 ((uint)(base) >> 16) & 0xff, type, 1, dpl, 1,
                                                                                 0776 (uint)(lim) >> 16, 0, 0, 1, 0, (uint)(base) >> 24 }
0726 // Control Register flags
0727 #define CRO_PE
                                             // Protection Enable
                                                                                 0777 #endif
                             0x00000001
0728 #define CR0 MP
                             0x00000002
                                             // Monitor coProcessor
                                                                                 0778
                                                                                 0779 #define DPL_USER
0729 #define CRO_EM
                             0x00000004
                                            // Emulation
                                                                                                                  // User DPL
                                                                                                          0x3
0730 #define CRO_TS
                             0x00000008
                                            // Task Switched
                                                                                 0780
0731 #define CR0 ET
                             0x00000010
                                            // Extension Type
                                                                                 0781 // Application segment type bits
                             0x00000020
                                                                                 0782 #define STA_X
0732 #define CRO_NE
                                            // Numeric Errror
                                                                                                          0x8
                                                                                                                  // Executable segment
0733 #define CRO_WP
                             0x00010000
                                            // Write Protect
                                                                                 0783 #define STA_E
                                                                                                          0x4
                                                                                                                  // Expand down (non-executable segments)
0734 #define CR0 AM
                             0x00040000
                                            // Alignment Mask
                                                                                 0784 #define STA C
                                                                                                          0x4
                                                                                                                  // Conforming code segment (executable only)
                                            // Not Writethrough
                                                                                                          0x2
                                                                                                                  // Writeable (non-executable segments)
0735 #define CRO_NW
                             0x20000000
                                                                                 0785 #define STA_W
0736 #define CRO_CD
                             0x40000000
                                            // Cache Disable
                                                                                 0786 #define STA_R
                                                                                                          0x2
                                                                                                                  // Readable (executable segments)
0737 #define CR0 PG
                             0x80000000
                                            // Paging
                                                                                 0787 #define STA A
                                                                                                          0x1
                                                                                                                  // Accessed
0738
                                                                                 0788
0739 #define CR4_PSE
                             0x00000010
                                            // Page size extension
                                                                                 0789 // System segment type bits
0740
                                                                                 0790 #define STS T16A
                                                                                                          0x1
                                                                                                                  // Available 16-bit TSS
0741 #define SEG_KCODE 1 // kernel code
                                                                                 0791 #define STS_LDT
                                                                                                          0x2
                                                                                                                  // Local Descriptor Table
0742 #define SEG KDATA 2 // kernel data+stack
                                                                                 0792 #define STS_T16B
                                                                                                          0x3
                                                                                                                  // Busy 16-bit TSS
0743 #define SEG KCPU 3 // kernel per-cpu data
                                                                                 0793 #define STS CG16
                                                                                                          0x4
                                                                                                                  // 16-bit Call Gate
0744 #define SEG_UCODE 4 // user code
                                                                                 0794 #define STS_TG
                                                                                                          0x5
                                                                                                                  // Task Gate / Coum Transmitions
0745 #define SEG UDATA 5 // user data+stack
                                                                                 0795 #define STS IG16
                                                                                                                  // 16-bit Interrupt Gate
                                                                                                          0x6
0746 #define SEG TSS 6 // this process's task state
                                                                                 0796 #define STS TG16
                                                                                                          0x7
                                                                                                                  // 16-bit Trap Gate
0747
                                                                                 0797 #define STS_T32A
                                                                                                          0x9
                                                                                                                  // Available 32-bit TSS
0748
                                                                                 0798 #define STS T32B
                                                                                                                  // Busy 32-bit TSS
                                                                                                          0xB
0749
                                                                                 0799 #define STS_CG32
                                                                                                          0xC
                                                                                                                 // 32-bit Call Gate
```

Sheet 07 Sheet 07

```
0800 #define STS IG32 0xE // 32-bit Interrupt Gate
                                                                       0850 // Task state segment format
0851 struct taskstate {
0802
                                                                       0852 uint link;
                                                                                             // Old ts selector
0803 // A virtual address 'la' has a three-part structure as follows:
                                                                      0853 uint esp0;
                                                                                             // Stack pointers and segment selectors
                                                                                             // after an increase in privilege level
                                                                       0854 ushort ss0;
0805 // +-----10-----+
                                                                      0855 ushort padding1;
0806 // | Page Directory | Page Table | Offset within Page |
                                                                      0856 uint *esp1;
0807 // | Index | Index
                                                                      0857 ushort ss1;
0808 // +------+
                                                                      0858 ushort padding2;
0809 // \--- PDX(va) --/ \--- PTX(va) --/
                                                                       0859 uint *esp2;
0810
                                                                       0860 ushort ss2;
0811 // page directory index
                                                                       0861 ushort padding3;
0812 #define PDX(va) (((uint)(va) >> PDXSHIFT) & 0x3FF)
                                                                       0862 void *cr3;
                                                                                             // Page directory base
0813
                                                                       0863 uint *eip;
                                                                                             // Saved state from last task switch
0814 // page table index
                                                                       0864 uint eflags;
0815 #define PTX(va)
                       (((uint)(va) >> PTXSHIFT) & 0x3FF)
                                                                       0865 uint eax;
                                                                                            // More saved state (registers)
                                                                       0866 uint ecx;
0817 // construct virtual address from indexes and offset
                                                                       0867 uint edx;
0869 uint *esp;
0820 // Page directory and page table constants.
                                                                       0870 uint *ebp;
0821 #define NPDENTRIES 1024 // # directory entries per page directory 0871 uint esi;
0822 #define NPTENTRIES
                        1024 // # PTEs per page table
                                                                       0872 uint edi;
0823 #define PGSIZE 4096 // bytes mapped by a page
                                                                      0873 ushort es;
                                                                                             // Even more saved state (segment selectors)
0824
                                                                       0874 ushort padding4;
                   12 // log2(PGSIZE)
12 // offset of PTX in a linear address
0825 #define PGSHIFT
                                                                       0875 ushort cs;
0826 #define PTXSHIFT
                                                                      0876 ushort padding5;
0827 #define PDXSHIFT
                    22 // offset of PDX in a linear address
                                                                      0877 ushort ss;
                                                                       0878 ushort padding6;
0829 #define PGROUNDUP(sz) (((sz)+PGSIZE-1) & ~(PGSIZE-1))
                                                                       0879 ushort ds;
                                                                       0880 ushort padding7;
0830 #define PGROUNDDOWN(a) (((a)) & ~(PGSIZE-1))
                                                                      0881 ushort fs;
                                                                       0882 ushort padding8;
0832 // Page table/directory entry flags.
0833 #define PTE P 0x001 // Present
                                                                       0883 ushort gs;
                    0x002 // Writeable
0x004 // User
0834 #define PTE W
                                                                       0884 ushort padding9;
0835 #define PTE_U
                                                                      0885 ushort ldt;
                  0x008 // Write-Through
0x010 // Cache-Disable
0x020 // Accessed
0x040 // Dirty
0x080 // Page Size
0x180 // Bits must be zero
0836 #define PTE_PWT
                                                                      0886 ushort padding10;
0837 #define PTE PCD
                                                                       0887 ushort t;
                                                                                            // Trap on task switch
0838 #define PTE_A
                                                                       0888 ushort iomb;
                                                                                             // I/O map base address
0839 #define PTE_D
                                                                       0889 };
0840 #define PTE_PS
                                                                       0890
0841 #define PTE MBZ
                                                                       0891
0842
                                                                       0892
0843 // Address in page table or page directory entry
                                                                       0893
0844 #define PTE_ADDR(pte) ((uint)(pte) & ~0xFFF)
                                                                       0894
0845 #define PTE FLAGS(pte) ((uint)(pte) & 0xFFF)
                                                                       0895
0846
                                                                       0896
0847 #ifndef __ASSEMBLER__
                                                                       0897
0848 typedef uint pte t;
                                                                       0898
0849
                                                                       0899
```

Sheet 08 Sheet 08

```
0950 // Format of an ELF executable file
0900 // Gate descriptors for interrupts and traps
0901 struct gatedesc {
                                                                               0951
0902 uint off 15 0 : 16; // low 16 bits of offset in segment
                                                                               0952 #define ELF MAGIC 0x464C457FU // "\x7FELF" in little endian
0903 uint cs : 16;
                           // code segment selector
                                                                               0953
0904 uint args : 5;
                           // # args, 0 for interrupt/trap gates
                                                                               0954 // File header
0905 uint rsv1 : 3;
                           // reserved(should be zero I quess)
                                                                               0955 struct elfhdr {
0906 uint type : 4;
                           // type(STS_{TG,IG32,TG32})
                                                                               0956 uint magic; // must equal ELF_MAGIC
0907 uint s : 1;
                           // must be 0 (system)
                                                                               0957 uchar elf[12];
0908 uint dpl : 2;
                           // descriptor(meaning new) privilege level
                                                                               0958 ushort type;
0909 uint p : 1;
                                                                               0959
                                                                                    ushort machine;
                           // Present
0910 uint off_31_16 : 16; // high bits of offset in segment
                                                                               0960 uint version;
0911 };
                                                                               0961 uint entry;
0912
                                                                               0962 uint phoff;
0913 // Set up a normal interrupt/trap gate descriptor.
                                                                               0963 uint shoff;
0914 // - istrap: 1 for a trap (= exception) gate, 0 for an interrupt gate.
                                                                               0964 uint flags;
0915 // interrupt gate clears FL_IF, trap gate leaves FL_IF alone
                                                                               0965 ushort ehsize;
0916 // - sel: Code segment selector for interrupt/trap handler
                                                                               0966 ushort phentsize;
0917 // - off: Offset in code segment for interrupt/trap handler
                                                                               0967 ushort phnum;
0918 // - dpl: Descriptor Privilege Level -
                                                                               0968 ushort shentsize;
              the privilege level required for software to invoke
0919 //
                                                                               0969 ushort shnum;
                                                                               0970 ushort shstrndx;
              this interrupt/trap gate explicitly using an int instruction.
0920 //
0921 #define SETGATE(gate, istrap, sel, off, d)
                                                                               0971 };
0922 {
                                                                               0972
0923 (gate).off_15_0 = (uint)(off) & 0xffff;
                                                                               0973 // Program section header
0924 (gate).cs = (sel);
                                                                               0974 struct proghdr {
0925 	 (qate).args = 0;
                                                                               0975 uint type;
0926 (gate).rsv1 = 0;
                                                                               0976 uint off;
0927
       (gate).type = (istrap) ? STS_TG32 : STS_IG32;
                                                                              0977 uint vaddr;
                                                                               0978 uint paddr;
0928
      (qate).s = 0;
                                                                               0979 uint filesz;
0929
      (qate).dpl = (d);
0930 (gate).p = 1;
                                                                               0980 uint memsz;
0931
      (gate).off_31_16 = (uint)(off) >> 16;
                                                                               0981 uint flags;
                                                                               0982 uint align;
0932 }
0933
                                                                               0983 };
0934 #endif
                                                                               0984
0935
                                                                               0985 // Values for Proghdr type
0936
                                                                               0986 #define ELF_PROG_LOAD
                                                                                                                  1
0937
                                                                               0987
0938
                                                                               0988 // Flag bits for Proghdr flags
                                                                               0989 #define ELF_PROG_FLAG_EXEC
0939
                                                                                                                  1
0940
                                                                               0990 #define ELF PROG FLAG WRITE
                                                                               0991 #define ELF_PROG_FLAG_READ
                                                                                                                   4
0941
0942
                                                                               0992
0943
                                                                               0993
0944
                                                                               0994
0945
                                                                               0995
0946
                                                                               0996
0947
                                                                               0997
0948
                                                                               0998
0949
                                                                               0999
```

Sheet 09 Sheet 09

```
1000 #include "types.h"
1001
1002 struct uproc {
1003 int pid;
1004 int uid;
1005 int gid;
1006 int ppid;
1007 int state;
1008 int priority;
1009 uint size;
1010 char name[16];
1011 };
1012
1013
1014
1015
1016
1017
1018
1019
1020
1021
1022
1023
1024
1025
1026
1027
1028
1029
1030
1031
1032
1033
1034
1035
1036
1037
1038
1039
1040
1041
1042
1043
1044
1045
1046
1047
1048
1049
```

```
1050 # Multiboot header, for multiboot boot loaders like GNU Grub.
1051 # http://www.gnu.org/software/grub/manual/multiboot/multiboot.html
1052 #
1053 # Using GRUB 2, you can boot xv6 from a file stored in a
1054 # Linux file system by copying kernel or kernelmemfs to /boot
1055 # and then adding this menu entry:
1056 #
1057 # menuentry "xv6" {
1058 # insmod ext2
1059 # set root='(hd0,msdos1)'
1060 # set kernel='/boot/kernel'
1061 # echo "Loading ${kernel}..."
1062 # multiboot ${kernel} ${kernel}
1063 # boot
1064 # }
1065
1066 #include "asm.h"
1067 #include "memlayout.h"
1068 #include "mmu.h"
1069 #include "param.h"
1070
1071 # Multiboot header. Data to direct multiboot loader.
1072 .p2align 2
1073 .text
1074 .globl multiboot_header
1075 multiboot header:
1076 #define magic 0x1badb002
1077 #define flags 0
1078 .long magic
1079 .long flags
1080
      .long (-magic-flags)
1081
1082 # By convention, the _start symbol specifies the ELF entry point.
1083 # Since we haven't set up virtual memory yet, our entry point is
1084 # the physical address of 'entry'.
1085 .globl _start
1086 _start = V2P_WO(entry)
1088 # Entering xv6 on boot processor, with paging off.
1089 .globl entry
1090 entry:
1091 # Turn on page size extension for 4Mbyte pages
1092 movl
             %cr4, %eax
1093 orl
              $(CR4 PSE), %eax
1094 movl
             %eax, %cr4
1095 # Set page directory
1096 movl
             $(V2P WO(entrypgdir)), %eax
1097 movl
              %eax, %cr3
1098 # Turn on paging.
1099 movl %cr0, %eax
```

1100	orl \$(CRO_PG CRO_WP), %eax
1101	movl %eax, %cr0
	MOVI SCAX, SCIO
1102	
1103	# Set up the stack pointer.
1104	<pre>movl \$(stack + KSTACKSIZE), %esp</pre>
1105	
1106	<pre># Jump to main(), and switch to executing at</pre>
1107	# high addresses. The indirect call is needed because
1108	# the assembler produces a PC-relative instruction
1100	# for a direct jump.
1110	mov \$main, %eax
1111	jmp *%eax
1112	
1113	.comm stack, KSTACKSIZE
1114	
1115	
1116	
1117	
1118	
1119	
1120	
1121	
1122	
1123	
1124	
1125	
1126	
1127	
1128	
1129	
1130	
1131	
1132	
1133	
1134	
1135	
1136	
1137	
1138	
1139	
1140	
1141	
1142	
1143	
1144	
1145	
1146	
1147	
1148	
1149	

```
1150 #include "asm.h"
1151 #include "memlayout.h"
1152 #include "mmu.h"
1153
1154 # Each non-boot CPU ("AP") is started up in response to a STARTUP
1155 # IPI from the boot CPU. Section B.4.2 of the Multi-Processor
1156 # Specification says that the AP will start in real mode with CS:IP
1157 # set to XY00:0000, where XY is an 8-bit value sent with the
1158 # STARTUP. Thus this code must start at a 4096-byte boundary.
1160 # Because this code sets DS to zero, it must sit
1161 # at an address in the low 2^16 bytes.
1162 #
1163 # Startothers (in main.c) sends the STARTUPs one at a time.
1164 # It copies this code (start) at 0x7000. It puts the address of
1165 # a newly allocated per-core stack in start-4, the address of the
1166 # place to jump to (mpenter) in start-8, and the physical address
1167 # of entrypgdir in start-12.
1168 #
1169 # This code is identical to bootasm.S except:
1170 # - it does not need to enable A20
1171 # - it uses the address at start-4, start-8, and start-12
1172
1173 .code16
1174 .globl start
1175 start:
1176 cli
1177
1178 xorw
               %ax,%ax
1179 movw
               %ax,%ds
1180 movw
               %ax,%es
1181 movw
               %ax,%ss
1182
1183 lgdt
               qdtdesc
1184 movl
               %cr0, %eax
1185 orl
               $CRO_PE, %eax
1186 movl
               %eax, %cr0
1187
1188
1189
1190
1191
1192
1193
1194
1195
1196
1197
1198
1199
```

Sheet 11 Sheet 11

1200 1201	ljmpl	\$(SEG_KCODE<<3), \$(start32)
	.code32	
	start32:	
1203		\$(SEG_KDATA<<3), %ax
		%ax, %ds
1205		%ax, %es
		oda, ses
1207		%ax, %ss \$0, %ax
1210		%ax, %fs
		%ax, %gs
1211		
1212	# Turn	on page size extension for 4Mbyte pages
1213		%cr4, %eax
1214		\$(CR4_PSE), %eax
1215	MOAT	%eax, %cr4
1216		enterpgdir as our initial page table
	movl	
1218		%eax, %cr3
1219		on paging.
1220	MOAT	%cr0, %eax
1221 1222		<pre>\$(CR0_PE CR0_PG CR0_WP), %eax %eax, %cr0</pre>
		seax, sciu
1223		the to the steel ellegated by steet athors ()
1224	# SWILC	th to the stack allocated by startothers()
1225 1226	IIIOVI	(start-4), %esp
1227		mpenter()
1228		*(start-8)
		¢0v8200 %2v
1227	movw movw	\$0x8a00, %ax %ax, %dx
	a	0.0 0.3
1231	outw movw	\$0x8ae0, %ax
1233		%ax, %dx
	spin:	our, our
1235	-	enin
1236	Juip	SPIII
	.p2align	2
1238		2
1239	-	.T.A.SM
1240		I(STA_X STA_R, 0, 0xffffffff)
1241		I(STA_W, 0, 0xffffffff)
1242	520_1151	Non_n, o, online
1243		
	gdtdesc:	
	-	(gdtdesc - gdt - 1)
	.long	
1247	0.1.5	J
1248		
1249		

```
1250 #include "types.h"
1251 #include "defs.h"
1252 #include "param.h"
1253 #include "memlayout.h"
1254 #include "mmu.h"
1255 #include "proc.h"
1256 #include "x86.h"
1257
1258 static void startothers(void);
1259 static void mpmain(void) __attribute__((noreturn));
1260 extern pde_t *kpgdir;
1261 extern char end[]; // first address after kernel loaded from ELF file
1262
1263 // Bootstrap processor starts running C code here.
1264 // Allocate a real stack and switch to it, first
1265 // doing some setup required for memory allocator to work.
1266 int
1267 main(void)
1268 {
1269 kinit1(end, P2V(4*1024*1024)); // phys page allocator
1270 kvmalloc();
                      // kernel page table
1271 mpinit();
                      // collect info about this machine
1272 lapicinit();
1273 seginit();
                      // set up segments
1274 cprintf("\ncpu%d: starting xv6\n\n", cpu->id);
1275 picinit();
                      // interrupt controller
1276 ioapicinit(); // another interrupt controller
1277 consoleinit(); // I/O devices & their interrupts
1278 uartinit();
                      // serial port
1279 pinit();
                      // process table
1280 tvinit();
                      // trap vectors
1281 binit();
                      // buffer cache
1282 fileinit();
                      // file table
1283 ideinit();
                      // disk
1284 if(!ismp)
1285
      timerinit(); // uniprocessor timer
1286 startothers(); // start other processors
1287 kinit2(P2V(4*1024*1024), P2V(PHYSTOP)); // must come after startothers()
1288 userinit();
                     // first user process
1289 // Finish setting up this processor in mpmain.
1290 mpmain();
1291 }
1292
1293
1294
1295
1296
1297
1298
1299
```

```
1300 // Other CPUs jump here from entryother.S.
                                                                               1350
                                                                                       // wait for cpu to finish mpmain()
1301 static void
                                                                               1351
                                                                                       while(c->started == 0)
1302 mpenter(void)
                                                                               1352
                                                                                        ;
1303 {
                                                                               1353 }
1304 switchkvm();
                                                                               1354 }
1305 seginit();
                                                                               1355
1306 lapicinit();
                                                                               1356 // Boot page table used in entry.S and entryother.S.
1307 mpmain();
                                                                               1357 // Page directories (and page tables), must start on a page boundary,
1308 }
                                                                               1358 // hence the " aligned " attribute.
1309
                                                                               1359 // Use PTE_PS in page directory entry to enable 4Mbyte pages.
1310 // Common CPU setup code.
                                                                               1360 __attribute__((__aligned__(PGSIZE)))
1311 static void
                                                                               1361 pde_t entrypgdir[NPDENTRIES] = {
1312 mpmain(void)
                                                                               1362 // Map VA's [0, 4MB) to PA's [0, 4MB)
1313 {
                                                                               1363 [0] = (0) | PTE_P | PTE_W | PTE_PS,
1314 cprintf("cpu%d: starting\n", cpu->id);
                                                                               1364 // Map VA's [KERNBASE, KERNBASE+4MB) to PA's [0, 4MB)
1315 idtinit();
                     // load idt register
                                                                               1365 [KERNBASE>>PDXSHIFT] = (0) | PTE_P | PTE_W | PTE_PS,
1316 xchg(&cpu->started, 1); // tell startothers() we're up
                                                                               1366 };
1317 scheduler(); // start running processes
                                                                               1367
1318 }
                                                                               1368
1319
                                                                               1369
1320 pde_t entrypgdir[]; // For entry.S
                                                                               1370
1321
                                                                               1371
1322 // Start the non-boot (AP) processors.
                                                                               1372
1323 static void
                                                                               1373
1324 startothers(void)
                                                                               1374
1325 {
                                                                               1375
1326 extern uchar _binary_entryother_start[], _binary_entryother_size[];
                                                                               1376
1327 uchar *code;
                                                                               1377
1328 struct cpu *c;
                                                                               1378
1329 char *stack;
                                                                               1379
1330
                                                                               1380
1331 // Write entry code to unused memory at 0x7000.
                                                                               1381
1332 // The linker has placed the image of entryother.S in
                                                                               1382
1333 // _binary_entryother_start.
                                                                               1383
1334 code = p2v(0x7000);
                                                                               1384
1335 memmove(code, _binary_entryother_start, (uint)_binary_entryother_size);
                                                                              1385
1336
                                                                               1386
1337 for(c = cpus; c < cpus+ncpu; c++){
                                                                               1387
1338
       if(c == cpus+cpunum()) // We've started already.
                                                                               1388
1339
          continue;
                                                                               1389
1340
                                                                               1390
1341
        // Tell entryother.S what stack to use, where to enter, and what
                                                                               1391
1342
        // pgdir to use. We cannot use kpgdir yet, because the AP processor
                                                                               1392
1343
        // is running in low memory, so we use entrypgdir for the APs too.
                                                                               1393
1344
        stack = kalloc();
                                                                               1394
1345
        *(void**)(code-4) = stack + KSTACKSIZE;
                                                                               1395
1346
         *(void**)(code-8) = mpenter;
                                                                               1396
1347
        *(int**)(code-12) = (void *) v2p(entrypgdir);
                                                                               1397
1348
                                                                               1398
1349
                                                                               1399
        lapicstartap(c->id, v2p(code));
```

Sheet 13 Sheet 13

1400 // Plank many	1450 // Dl
	1450 // Blank page. 1451
	1452
	1453
	1454
	1455
	1456
	1457
	1458
1409	1459
	1460
	1461
	1462
	1463
	1464
	1465
1416	1466
1417	1467
	1468
	1469
	1470
	1471
1422	1472
	1473
	1474
	1475
	1476
	1477
	1478
	1479
	1480
	1481
	1482
	1483
	1484
	1485
	1486
	1487
	1488
	1489
	1490
	1491
	1492
	1493
	1494
	1495
	1496
	1497
	1498
	1499

Sheet 14

```
1550 // Mutual exclusion lock.
1500 // Blank page.
1501
                                                                             1551 struct spinlock {
1502
                                                                             1552 uint locked;
                                                                                                      // Is the lock held?
1503
                                                                             1553
1504
                                                                             1554 // For debugging:
1505
                                                                             1555 char *name;
                                                                                                      // Name of lock.
1506
                                                                             1556 struct cpu *cpu;
                                                                                                      // The cpu holding the lock.
1507
                                                                             1557
                                                                                    uint pcs[10];
                                                                                                      // The call stack (an array of program counters)
1508
                                                                             1558
                                                                                                      // that locked the lock.
1509
                                                                             1559 };
1510
                                                                             1560
1511
                                                                             1561
1512
                                                                             1562
1513
                                                                              1563
1514
                                                                             1564
1515
                                                                             1565
1516
                                                                              1566
1517
                                                                              1567
                                                                             1568
1518
1519
                                                                              1569
1520
                                                                             1570
1521
                                                                             1571
1522
                                                                             1572
1523
                                                                             1573
1524
                                                                             1574
1525
                                                                             1575
1526
                                                                             1576
1527
                                                                             1577
1528
                                                                              1578
1529
                                                                             1579
1530
                                                                             1580
1531
                                                                             1581
1532
                                                                             1582
1533
                                                                             1583
1534
                                                                              1584
1535
                                                                             1585
1536
                                                                             1586
1537
                                                                             1587
1538
                                                                             1588
1539
                                                                             1589
1540
                                                                             1590
1541
                                                                             1591
1542
                                                                             1592
1543
                                                                             1593
1544
                                                                             1594
1545
                                                                             1595
1546
                                                                             1596
1547
                                                                             1597
1548
                                                                             1598
1549
                                                                             1599
```

Sheet 15 Sheet 15

```
1600 // Mutual exclusion spin locks.
1601
1602 #include "types.h"
1603 #include "defs.h"
1604 #include "param.h"
1605 #include "x86.h"
1606 #include "memlayout.h"
1607 #include "mmu.h"
1608 #include "proc.h"
1609 #include "spinlock.h"
1610
1611 void
1612 initlock(struct spinlock *lk, char *name)
1613 {
1614 lk->name = name;
1615 lk \rightarrow locked = 0;
1616 	 lk->cpu = 0;
1617 }
1618
1619 // Acquire the lock.
1620 // Loops (spins) until the lock is acquired.
1621 // Holding a lock for a long time may cause
1622 // other CPUs to waste time spinning to acquire it.
1623 void
1624 acquire(struct spinlock *lk)
1625 {
1626 pushcli(); // disable interrupts to avoid deadlock.
1627 if(holding(lk))
1628
       panic("acquire");
1629
1630 // The xchg is atomic.
1631 // It also serializes, so that reads after acquire are not
1632 // reordered before it.
1633 while(xchg(&lk->locked, 1) != 0)
1634 ;
1635
1636 // Record info about lock acquisition for debugging.
1637 lk->cpu = cpu;
1638 getcallerpcs(&lk, lk->pcs);
1639 }
1640
1641
1642
1643
1644
1645
1646
1647
1648
1649
```

```
1650 // Release the lock.
1651 void
1652 release(struct spinlock *lk)
1653 {
1654 if(!holding(lk))
        panic("release");
1655
1656
1657 	 lk->pcs[0] = 0;
1658 	 1k - cpu = 0;
1659
1660 // The xchg serializes, so that reads before release are
1661 // not reordered after it. The 1996 PentiumPro manual (Volume 3,
1662 // 7.2) says reads can be carried out speculatively and in
1663 // any order, which implies we need to serialize here.
1664 // But the 2007 Intel 64 Architecture Memory Ordering White
1665 // Paper says that Intel 64 and IA-32 will not move a load
1666 // after a store. So lock->locked = 0 would work here.
1667 // The xchg being asm volatile ensures gcc emits it after
1668 // the above assignments (and after the critical section).
1669 xchq(&lk->locked, 0);
1670
1671 popcli();
1672 }
1673
1674 // Record the current call stack in pcs[] by following the %ebp chain.
1675 void
1676 getcallerpcs(void *v, uint pcs[])
1677 {
1678 uint *ebp;
1679 int i;
1680
1681 ebp = (uint*)v - 2;
1682 for(i = 0; i < 10; i++){
1683
       if(ebp == 0 || ebp < (uint*)KERNBASE || ebp == (uint*)Oxffffffff)</pre>
1684
          break;
1685
        1686
       ebp = (uint*)ebp[0]; // saved %ebp
1687
1688 for(; i < 10; i++)
        pcs[i] = 0;
1689
1690 }
1691
1692 // Check whether this cpu is holding the lock.
1694 holding(struct spinlock *lock)
1695 {
1696 return lock->locked && lock->cpu == cpu;
1697 }
1698
1699
```

```
1700 // Pushcli/popcli are like cli/sti except that they are matched:
                                                                                1750 #include "param.h"
1701 // it takes two popcli to undo two pushcli. Also, if interrupts
                                                                                1751 #include "types.h"
1702 // are off, then pushcli, popcli leaves them off.
                                                                                1752 #include "defs.h"
                                                                               1753 #include "x86.h"
1703
                                                                                1754 #include "memlayout.h"
1704 void
1705 pushcli(void)
                                                                                1755 #include "mmu.h"
                                                                                1756 #include "proc.h"
1706 {
1707 int eflags;
                                                                               1757 #include "elf.h"
1708
                                                                                1758
1709 eflags = readeflags();
                                                                               1759 extern char data[]; // defined by kernel.ld
1710 cli();
                                                                                1760 pde_t *kpgdir; // for use in scheduler()
if(cpu->ncli++==0)
                                                                                1761 struct segdesc gdt[NSEGS];
1712
        cpu->intena = eflags & FL_IF;
                                                                                1762
1713 }
                                                                                1763 // Set up CPU's kernel segment descriptors.
1714
                                                                                1764 // Run once on entry on each CPU.
1715 void
                                                                               1765 void
1716 popcli(void)
                                                                                1766 seginit(void)
1717 {
                                                                                1767 {
1718 if(readeflags()&FL_IF)
                                                                               1768 struct cpu *c;
1719
        panic("popcli - interruptible");
                                                                                1769
1720 if(--cpu->ncli < 0)
                                                                               1770 // Map "logical" addresses to virtual addresses using identity map.
1721
        panic("popcli");
                                                                               1771 // Cannot share a CODE descriptor for both kernel and user
1722 if(cpu->ncli == 0 && cpu->intena)
                                                                                1772 // because it would have to have DPL USR, but the CPU forbids
1723
        sti();
                                                                                1773 // an interrupt from CPL=0 to DPL=3.
1724 }
                                                                               1774 c = &cpus[cpunum()];
1725
                                                                                1775 c->qdt[SEG KCODE] = SEG(STA X|STA R, 0, 0xfffffffff, 0);
                                                                               1776 c->qdt[SEG_KDATA] = SEG(STA_W, 0, 0xfffffffff, 0);
1726
1727
                                                                                1777 c->qdt[SEG_UCODE] = SEG(STA_X|STA_R, 0, 0xffffffff, DPL_USER);
1728
                                                                               1778 c->qdt[SEG UDATA] = SEG(STA W, 0, 0xfffffffff, DPL USER);
1729
                                                                                1779
1730
                                                                               1780 // Map cpu, and curproc
1731
                                                                                1781 c \rightarrow gdt[SEG_KCPU] = SEG(STA_W, &c \rightarrow cpu, 8, 0);
1732
                                                                               1782
1733
                                                                               1783 lgdt(c->gdt, sizeof(c->gdt));
1734
                                                                                1784 loadgs(SEG_KCPU << 3);
1735
                                                                                1785
1736
                                                                               1786 // Initialize cpu-local storage.
1737
                                                                               1787 cpu = c;
                                                                               1788 proc = 0;
1738
1739
                                                                                1789 }
1740
                                                                                1790
                                                                                1791
1741
1742
                                                                                1792
1743
                                                                                1793
1744
                                                                                1794
1745
                                                                                1795
1746
                                                                                1796
1747
                                                                                1797
1748
                                                                                1798
1749
                                                                               1799
```

Sheet 17

```
1800 // Return the address of the PTE in page table pgdir
1801 // that corresponds to virtual address va. If alloc!=0,
1802 // create any required page table pages.
1803 static pte_t *
1804 walkpgdir(pde_t *pgdir, const void *va, int alloc)
1805 {
1806 pde_t *pde;
1807 pte_t *pgtab;
1808
1809 pde = &pgdir[PDX(va)];
1810 if(*pde & PTE_P){
        pgtab = (pte_t*)p2v(PTE_ADDR(*pde));
1811
1812 } else {
1813
        if(!alloc | | (pgtab = (pte_t*)kalloc()) == 0)
1814
          return 0;
1815
        // Make sure all those PTE_P bits are zero.
1816
        memset(pgtab, 0, PGSIZE);
1817
        // The permissions here are overly generous, but they can
1818
        // be further restricted by the permissions in the page table
1819
        // entries, if necessary.
1820
        *pde = v2p(pgtab) | PTE_P | PTE_W | PTE_U;
1821 }
1822 return &pgtab[PTX(va)];
1823 }
1824
1825 // Create PTEs for virtual addresses starting at va that refer to
1826 // physical addresses starting at pa. va and size might not
1827 // be page-aligned.
1828 static int
1829 mappages(pde_t *pgdir, void *va, uint size, uint pa, int perm)
1830 {
1831 char *a, *last;
1832 pte_t *pte;
1833
1834 a = (char*)PGROUNDDOWN((uint)va);
1835 last = (char*)PGROUNDDOWN(((uint)va) + size - 1);
1836 for(;;){
1837
       if((pte = walkpgdir(pgdir, a, 1)) == 0)
1838
          return -1;
1839
        if(*pte & PTE_P)
1840
          panic("remap");
1841
        *pte = pa | perm | PTE_P;
1842
        if(a == last)
1843
          break;
1844
        a += PGSIZE;
        pa += PGSIZE;
1845
1846 }
1847 return 0;
1848 }
1849
```

```
1850 // There is one page table per process, plus one that's used when
1851 // a CPU is not running any process (kpgdir). The kernel uses the
1852 // current process's page table during system calls and interrupts;
1853 // page protection bits prevent user code from using the kernel's
1854 // mappings.
1855 //
1856 // setupkvm() and exec() set up every page table like this:
1858 // 0..KERNBASE: user memory (text+data+stack+heap), mapped to
                      phys memory allocated by the kernel
1859 //
1860 //
         KERNBASE..KERNBASE+EXTMEM: mapped to 0..EXTMEM (for I/O space)
          KERNBASE+EXTMEM..data: mapped to EXTMEM..V2P(data)
1861 //
1862 //
                      for the kernel's instructions and r/o data
1863 //
          data..KERNBASE+PHYSTOP: mapped to V2P(data)..PHYSTOP,
1864 //
                                        rw data + free physical memory
1865 // Oxfe000000..0: mapped direct (devices such as ioapic)
1866 //
1867 // The kernel allocates physical memory for its heap and for user memory
1868 // between V2P(end) and the end of physical memory (PHYSTOP)
1869 // (directly addressable from end..P2V(PHYSTOP)).
1870
1871 // This table defines the kernel's mappings, which are present in
1872 // every process's page table.
1873 static struct kmap {
1874 void *virt;
1875 uint phys start;
1876 uint phys_end;
1877 int perm;
1878 } kmap[] = {
                                                  PTE_W }, // I/O space
      { (void*)KERNBASE, 0,
                                       EXTMEM,
1879
1880
      { (void*)KERNLINK, V2P(KERNLINK), V2P(data), 0}, // kern text+rodata
1881
     { (void*)data,
                        V2P(data),
                                       PHYSTOP, PTE_W }, // kern data+memory
1882 { (void*)DEVSPACE, DEVSPACE,
                                                  PTE_W \ , // more devices
                                       0,
1883 };
1884
1885 // Set up kernel part of a page table.
1886 pde t*
1887 setupkvm(void)
1888 {
1889 pde_t *pgdir;
1890 struct kmap *k;
1891
1892 if((pgdir = (pde t*)kalloc()) == 0)
1893
       return 0;
1894 memset(pgdir, 0, PGSIZE);
1895 if (p2v(PHYSTOP) > (void*)DEVSPACE)
        panic("PHYSTOP too high");
1896
1897
      for(k = kmap; k < &kmap[NELEM(kmap)]; k++)</pre>
         if(mappages(pgdir, k->virt, k->phys end - k->phys start,
1898
1899
                    (uint)k->phys_start, k->perm) < 0)
```

Sheet 18 Sheet 18

```
1900
                                                                               1950 // Load the initcode into address 0 of pgdir.
          return 0;
1901 return pqdir;
                                                                               1951 // sz must be less than a page.
1902 }
                                                                               1952 void
1903
                                                                               1953 inituvm(pde_t *pgdir, char *init, uint sz)
1904 // Allocate one page table for the machine for the kernel address
                                                                               1954 {
1905 // space for scheduler processes.
                                                                               1955 char *mem;
1906 void
                                                                               1956
1907 kvmalloc(void)
                                                                               1957 if(sz \ge PGSIZE)
1908 {
                                                                               1958
                                                                                      panic("inituvm: more than a page");
1909 kpgdir = setupkvm();
                                                                               1959 mem = kalloc();
1910 switchkvm();
                                                                               1960 memset(mem, 0, PGSIZE);
1911 }
                                                                               1961 mappages(pgdir, 0, PGSIZE, v2p(mem), PTE_W|PTE_U);
1912
                                                                               1962 memmove(mem, init, sz);
1913 // Switch h/w page table register to the kernel-only page table,
                                                                               1963 }
1914 // for when no process is running.
                                                                               1964
1915 void
                                                                               1965 // Load a program segment into pgdir. addr must be page-aligned
1916 switchkvm(void)
                                                                               1966 // and the pages from addr to addr+sz must already be mapped.
1917 {
1918 lcr3(v2p(kpgdir)); // switch to the kernel page table
                                                                               1968 loaduvm(pde_t *pqdir, char *addr, struct inode *ip, uint offset, uint sz)
1919 }
                                                                               1969 {
                                                                               1970 uint i, pa, n;
1920
1921 // Switch TSS and h/w page table to correspond to process p.
                                                                               1971 pte_t *pte;
1922 void
                                                                               1972
1923 switchuvm(struct proc *p)
                                                                               1973 if((uint) addr % PGSIZE != 0)
1924 {
                                                                               1974
                                                                                        panic("loaduvm: addr must be page aligned");
1925 pushcli();
                                                                               1975 for(i = 0; i < sz; i += PGSIZE){
1926 cpu->qdt[SEG_TSS] = SEG16(STS_T32A, &cpu->ts, sizeof(cpu->ts)-1, 0);
                                                                               1976
                                                                                      if((pte = walkpgdir(pgdir, addr+i, 0)) == 0)
1927 cpu->gdt[SEG_TSS].s = 0;
                                                                               1977
                                                                                         panic("loaduvm: address should exist");
1928 cpu->ts.ss0 = SEG KDATA << 3;
                                                                               1978
                                                                                        pa = PTE ADDR(*pte);
1929 cpu->ts.esp0 = (uint)proc->kstack + KSTACKSIZE;
                                                                                       if(sz - i < PGSIZE)
                                                                               1979
1930 ltr(SEG_TSS << 3);
                                                                               1980
                                                                                        n = sz - i;
1931 if(p-pqdir == 0)
                                                                               1981
                                                                                        else
                                                                               1982
1932
       panic("switchuvm: no pgdir");
                                                                                        n = PGSIZE;
1933 lcr3(v2p(p->pgdir)); // switch to new address space
                                                                               1983
                                                                                       if(readi(ip, p2v(pa), offset+i, n) != n)
1934 popcli();
                                                                               1984
                                                                                         return -1;
1935 }
                                                                               1985 }
1936
                                                                               1986 return 0;
1937
                                                                               1987 }
1938
                                                                               1988
1939
                                                                               1989
1940
                                                                               1990
                                                                               1991
1941
1942
                                                                               1992
1943
                                                                               1993
1944
                                                                               1994
1945
                                                                               1995
1946
                                                                               1996
1947
                                                                               1997
1948
                                                                               1998
1949
                                                                               1999
```

Sheet 19 Sheet 19

```
2000 // Allocate page tables and physical memory to grow process from oldsz to
                                                                                2050
                                                                                          kfree(v);
2001 // newsz, which need not be page aligned. Returns new size or 0 on error.
                                                                                2051
                                                                                           *pte = 0;
2002 int.
                                                                                2052
                                                                                2053 }
2003 allocuvm(pde_t *pgdir, uint oldsz, uint newsz)
2004 {
                                                                                2054 return newsz;
2005 char *mem;
                                                                                2055 }
2006 uint a;
                                                                                2056
2007
                                                                                2057 // Free a page table and all the physical memory pages
2008 if(newsz >= KERNBASE)
                                                                                2058 // in the user part.
2009
      return 0;
                                                                                2059 void
2010 if(newsz < oldsz)
                                                                                2060 freevm(pde_t *pgdir)
       return oldsz;
2011
                                                                                2061 {
2012
                                                                                2062 uint i;
2013 a = PGROUNDUP(oldsz);
                                                                                2063
2014 for(; a < newsz; a += PGSIZE){
                                                                                2064 if(pgdir == 0)
2015
        mem = kalloc();
                                                                                2065
                                                                                        panic("freevm: no pgdir");
2016
        if(mem == 0)
                                                                                2066 deallocuvm(pgdir, KERNBASE, 0);
                                                                                2067 for(i = 0; i < NPDENTRIES; i++){
2017
          cprintf("allocuvm out of memory\n");
2018
          deallocuvm(pgdir, newsz, oldsz);
                                                                                2068
                                                                                       if(pgdir[i] & PTE_P){
2019
          return 0;
                                                                                2069
                                                                                          char * v = p2v(PTE_ADDR(pgdir[i]));
2020
                                                                                2070
                                                                                          kfree(v);
2021
        memset(mem, 0, PGSIZE);
                                                                                2071
2022
        mappages(pgdir, (char*)a, PGSIZE, v2p(mem), PTE_W | PTE_U);
                                                                                2072
2023 }
                                                                                2073 kfree((char*)pgdir);
2024 return newsz;
                                                                                2074 }
2025 }
                                                                                2075
2026
                                                                                2076 // Clear PTE_U on a page. Used to create an inaccessible
2027 // Deallocate user pages to bring the process size from oldsz to
                                                                                2077 // page beneath the user stack.
2028 // newsz. oldsz and newsz need not be page-aligned, nor does newsz
                                                                                2078 void
2029 // need to be less than oldsz. oldsz can be larger than the actual
                                                                                2079 clearpteu(pde_t *pqdir, char *uva)
2030 // process size. Returns the new process size.
                                                                                2080 {
2031 int.
                                                                                2081 pte_t *pte;
2032 deallocuvm(pde_t *pqdir, uint oldsz, uint newsz)
                                                                                2082
2033 {
                                                                                2083 pte = walkpgdir(pgdir, uva, 0);
                                                                                2084 	 if(pte == 0)
2034 pte_t *pte;
2035 uint a, pa;
                                                                                       panic("clearpteu");
                                                                                2085
2036
                                                                                2086 *pte &= ~PTE_U;
2037 if(newsz >= oldsz)
                                                                                2087 }
2038
       return oldsz;
                                                                                2088
2039
                                                                                2089
2040 a = PGROUNDUP(newsz);
                                                                                2090
2041 for(; a < oldsz; a += PGSIZE) \{
                                                                                2091
2042
        pte = walkpgdir(pgdir, (char*)a, 0);
                                                                                2092
2043
        if(!pte)
                                                                                2093
2044
          a += (NPTENTRIES - 1) * PGSIZE;
                                                                                2094
2045
        else if((*pte & PTE_P) != 0){
                                                                                2095
2046
          pa = PTE_ADDR(*pte);
                                                                                2096
2047
                                                                                2097
          if(pa == 0)
2048
          panic("kfree");
                                                                                2098
2049
                                                                                2099
          char *v = p2v(pa);
```

Sheet 20 Sheet 20

```
2100 // Given a parent process's page table, create a copy
2101 // of it for a child.
2102 pde t*
2103 copyuvm(pde_t *pqdir, uint sz)
2104 {
2105 pde t *d;
2106 pte_t *pte;
2107 uint pa, i, flags;
2108 char *mem;
2109
2110 if((d = setupkvm()) == 0)
      return 0;
2111
2112 for(i = 0; i < sz; i += PGSIZE){
2113
       if((pte = walkpgdir(pgdir, (void *) i, 0)) == 0)
2114
          panic("copyuvm: pte should exist");
2115
        if(!(*pte & PTE_P))
2116
        panic("copyuvm: page not present");
2117
        pa = PTE_ADDR(*pte);
2118
        flags = PTE_FLAGS(*pte);
2119
        if((mem = kalloc()) == 0)
2120
         goto bad;
2121
        memmove(mem, (char*)p2v(pa), PGSIZE);
2122
        if(mappages(d, (void*)i, PGSIZE, v2p(mem), flags) < 0)</pre>
2123
          goto bad;
2124 }
2125 return d;
2126
2127 bad:
2128 freevm(d);
2129 return 0;
2130 }
2131
2132
2133
2134
2135
2136
2137
2138
2139
2140
2141
2142
2143
2144
2145
2146
2147
2148
2149
```

```
2150 // Map user virtual address to kernel address.
2151 char*
2152 uva2ka(pde_t *pgdir, char *uva)
2153 {
2154 pte_t *pte;
2155
2156 pte = walkpgdir(pgdir, uva, 0);
2157 if((*pte & PTE_P) == 0)
2158
      return 0;
2159 if((*pte & PTE_U) == 0)
2160
       return 0;
2161 return (char*)p2v(PTE_ADDR(*pte));
2162 }
2163
2164 // Copy len bytes from p to user address va in page table pgdir.
2165 // Most useful when pgdir is not the current page table.
2166 // uva2ka ensures this only works for PTE_U pages.
2167 int
2168 copyout(pde_t *pgdir, uint va, void *p, uint len)
2169 {
2170 char *buf, *pa0;
2171 uint n, va0;
2172
2173 buf = (char*)p;
2174 while(len > 0){
2175
      va0 = (uint)PGROUNDDOWN(va);
2176 pa0 = uva2ka(pgdir, (char*)va0);
2177 if(pa0 == 0)
2178
        return -1;
2179 n = PGSIZE - (va - va0);
2180 if(n > len)
        n = len;
2181
2182 memmove(pa0 + (va - va0), buf, n);
2183
       len -= n;
       buf += n;
2184
2185
       va = va0 + PGSIZE;
2186 }
2187 return 0;
2188 }
2189
2190
2191
2192
2193
2194
2195
2196
2197
2198
2199
```

2200 // Blank page.	2250 // Blank page.
2201	2251
2202	2252
2203	2253
2204	2254
2205	2255
2206	2256
2207	2257
2208	2258
2209	2259
2210	2260
2211	2261
2212	2262
2213	2263
2214	2264
2215	2265
2216	2266
2217	2267
2218	2268
2219	2269
2220	2270
2221	2271
2222	2272
2223	2273
2224	2274
2225	2275
2226	2276
2227	2277
2228	2278
2229	2279
2230	2280
2231	2281
2232	2282
2233	2283
2234	2284
2235	2285
2236	2286
2237	2287
2238	2288
2239	2289
2240	2290
2241	2291
2242	2292
2243	2293 2294
2244	
2245	2295
2246	2296
2247	2297
2248	2298
2249	2299

Sheet 22

```
2300 // Blank page.
2301
2302
2303
2304
2305
2306
2307
2308
2309
2310
2311
2312
2313
2314
2315
2316
2317
2318
2319
2320
2321
2322
2323
2324
2325
2326
2327
2328
2329
2330
2331
2332
2333
2334
2335
2336
2337
2338
2339
2340
2341
2342
2343
2344
2345
2346
2347
2348
2349
```

```
2350 // Segments in proc->gdt.
2351 #define NSEGS
2352
2353 // Per-CPU state
2354 struct cpu {
2355 uchar id;
                                   // Local APIC ID; index into cpus[] below
2356 struct context *scheduler;
                                  // swtch() here to enter scheduler
2357 struct taskstate ts;
                                   // Used by x86 to find stack for interrupt
2358 struct segdesc gdt[NSEGS];
                                  // x86 global descriptor table
2359 volatile uint started;
                                   // Has the CPU started?
2360 int ncli;
                                   // Depth of pushcli nesting.
2361 int intena;
                                   // Were interrupts enabled before pushcli?
2362
2363 // Cpu-local storage variables; see below
2364 struct cpu *cpu;
2365 struct proc *proc;
                                   // The currently-running process.
2366 };
2367
2368 extern struct cpu cpus[NCPU];
2369 extern int ncpu;
2370
2371 // Per-CPU variables, holding pointers to the
2372 // current cpu and to the current process.
2373 // The asm suffix tells gcc to use "%gs:0" to refer to cpu
2374 // and "%gs:4" to refer to proc. seginit sets up the
2375 // %gs segment register so that %gs refers to the memory
2376 // holding those two variables in the local cpu's struct cpu.
2377 // This is similar to how thread-local variables are implemented
2378 // in thread libraries such as Linux pthreads.
2379 extern struct cpu *cpu asm("%qs:0");
                                               // &cpus[cpunum()]
2380 extern struct proc *proc asm("%gs:4");
                                               // cpus[cpunum()].proc
2381
2382
2383 // Saved registers for kernel context switches.
2384 // Don't need to save all the segment registers (%cs, etc),
2385 // because they are constant across kernel contexts.
2386 // Don't need to save %eax, %ecx, %edx, because the
2387 // x86 convention is that the caller has saved them.
2388 // Contexts are stored at the bottom of the stack they
2389 // describe; the stack pointer is the address of the context.
2390 // The layout of the context matches the layout of the stack in swtch.S
2391 // at the "Switch stacks" comment. Switch doesn't save eip explicitly,
2392 // but it is on the stack and allocproc() manipulates it.
2393 struct context {
2394 uint edi;
2395 uint esi;
2396 uint ebx;
2397 uint ebp;
2398 uint eip;
2399 };
```

```
2400 enum procstate { UNUSED, EMBRYO, SLEEPING, RUNNABLE, RUNNING, ZOMBIE };
                                                                               2450 #include "types.h"
2401
                                                                               2451 #include "defs.h"
2402 // Per-process state
                                                                               2452 #include "param.h"
                                                                               2453 #include "memlayout.h"
2403 struct proc {
                                   // Size of process memory (bytes)
                                                                               2454 #include "mmu.h"
2404 uint sz;
2405 pde t* pqdir;
                                  // Page table
                                                                               2455 #include "x86.h"
2406 char *kstack;
                                  // Bottom of kernel stack for this process
                                                                               2456 #include "proc.h"
2407 enum procstate state;
                                  // Process state
                                                                               2457 #include "spinlock.h"
2408 int pid;
                                  // Process ID
                                                                               2458 #include "ps.h"
2409 struct proc *parent;
                                  // Parent process
                                                                               2459
2410 struct trapframe *tf;
                                  // Trap frame for current syscall
                                                                               2460 struct {
2411 struct context *context;
                                  // swtch() here to run process
                                                                               2461 struct spinlock lock;
2412 void *chan;
                                  // If non-zero, sleeping on chan
                                                                               2462 struct proc proc[NPROC];
2413 int killed;
                                  // If non-zero, have been killed
                                                                               2463 struct proc *pReadyList[3];/*when !p->, the end has been hit*/
2414 struct file *ofile[NOFILE]; // Open files
                                                                               2464 struct proc *pFreeList;//three priority levels
2415 struct inode *cwd;
                                  // Current directory
                                                                               2465 uint TimeToReset;
2416 char name[16];
                                  // Process name (debugging)
                                                                               2466 } ptable;
2417 int uid;
                                  /* User ID */
                                                                               2467
2418 int gid;
                              /* Group ID */
                                                                               2468 static struct proc *initproc;
2419 int ppid;
                                     /* Parent ID*/
                                                                               2469
2420 int priority;
                                     /*0-2*/
                                                                               2470 int nextpid = 1;
2421 struct proc * next;
                                                                               2471 extern void forkret(void);
2422 };
                                                                               2472 extern void trapret(void);
2423
                                                                               2474 static void wakeup1(void *chan);
2424 // Process memory is laid out contiguously, low addresses first:
2425 // text
                                                                               2475
2426 //
         original data and bss
                                                                               2476 void
2427 // fixed-size stack
                                                                               2477 pinit(void)
2428 // expandable heap
                                                                               2478 {
2429
                                                                               2479 initlock(&ptable.lock, "ptable");
2430
                                                                               2480 }
2431
                                                                               2481
2432
                                                                               2482
2433
                                                                               2483
2434
                                                                               2484
2435
                                                                               2485
2436
                                                                               2486
2437
                                                                               2487
2438
                                                                               2488
2439
                                                                               2489
2440
                                                                               2490
2441
                                                                               2491
2442
                                                                               2492
2443
                                                                               2493
2444
                                                                               2494
2445
                                                                               2495
2446
                                                                               2496
2447
                                                                               2497
2448
                                                                               2498
2449
                                                                               2499
```

Sheet 24 Sheet 24

```
2500 // Look in the process table for an UNUSED proc.
                                                                              2550 int
2501 // If found, change state to EMBRYO and initialize
                                                                              2551 removefromready(struct proc *torm)//remove item from ready list
2502 // state required to run in the kernel.
                                                                               2552 {
2503 // Otherwise return 0.
                                                                              2553 struct proc* tmp;
2504 void
                                                                              2554 struct proc* prev;
2505 printlists(void)
                                                                               2555 int val = torm->priority;
2506 {
                                                                              2556 if(!torm)
2507 struct proc * p;
                                                                               2557
                                                                                     panic("tried to remove nothing");
2508 struct proc * tmp;
                                                                               2558   tmp = ptable.pReadyList[val];
2509 int i,j;
                                                                              2559 if(tmp->pid == torm->pid){//first item
2510 //show ready list
                                                                               2560
                                                                                      ptable.pReadyList[val] = ptable.pReadyList[val]->next;
2511 for(i=0;i<3;++i){
                                                                                     torm->next = 0;
                                                                               2561
2512
         p = ptable.pReadyList[i];
                                                                               2562 return 0;
                                                                               2563 }
2513
         j=0;
2514
                                                                              2564 else{
         tmp = p;
2515
         if(!tmp){
                                                                              2565
                                                                                       while(tmp){
2516
        //cprintf("list %d empty\n",i);
                                                                               2566
                                                                                         prev = tmp;
2517
           continue;
                                                                               2567
                                                                                         tmp = tmp->next; //traverse
2518
                                                                               2568
                                                                                         if(tmp->pid == torm->pid){
2519
         while (tmp){
                                                                               2569
                                                                                           prev->next = torm->next;
2520
           cprintf("ready list #%d item: %d: ", i,j);
                                                                               2570
                                                                                           torm->next = 0;
2521
           cprintf("%d %d %d %s\n", tmp->pid, tmp->qid, tmp->uid, tmp->name);
                                                                              2571
                                                                                           return 0;
2522
           tmp = tmp->next;/*last item should point to 0*/
                                                                               2572
2523
           ++j;
                                                                               2573
2524
                                                                               2574
2525 }
                                                                                    cprintf("didnt actually remove anything\n");
                                                                               2576 return 0;
2526 }
2527
                                                                               2577 }
2528
                                                                               2578 void
2529 int
                                                                               2579 addtofree(struct proc *toadd)//add to front of list
2530 addtoready(struct proc *toadd) //add to end of ready list
                                                                               2580 {
2531 {
                                                                               2581 toadd->next = ptable.pFreeList;
2532 if(!toadd)
                                                                               2582 ptable.pFreeList = toadd;
2533
        panic("tried to add nothing");
                                                                               2583 }
2534
                                                                               2584
2535 int val = toadd->priority;
                                                                               2585 static struct proc*
2536 struct proc *p;
                                                                               2586 allocproc(void)
2537 p= ptable.pReadyList[val];
                                                                               2587 {
2538 if (!p){/*empty list*/
                                                                              2588 struct proc *p;
2539
      toadd->next = 0; //just in case
                                                                               2589 char *sp;
2540
        ptable.pReadyList[val] = toadd;
                                                                              2590
2541
       return 0;
                                                                              2591 acquire(&ptable.lock);
2542 }
                                                                              2592 p = ptable.pFreeList;
2543 while(p->next){//get to end of list
                                                                              2593 if(!p){ /*list empty*/
2544
         p = p->next;
                                                                              2594 release(&ptable.lock);
2545 }
                                                                              2595 //panic("free list empty");
2546 toadd->next = 0; //just in case
                                                                              2596 return 0;
                                                                              2597 }
2547 p->next = toadd;
2548 return 0;
                                                                               2598 ptable.pFreeList = ptable.pFreeList->next; /*so it doesnt point at p*/
2549 }
                                                                              2599 /*//how it was originally implemented
```

Sheet 25 Sheet 25

2698 2699

Sheet 26 Sheet 26

2648

2649

May 16 22:03 2016 xy6/proc.c Page 7 2750 for(i = 0; i < NOFILE; i++) 2751 if(proc->ofile[i]) 2752 np->ofile[i] = filedup(proc->ofile[i]); 2753 np->cwd = idup(proc->cwd); 2754 2755 safestrcpy(np->name, proc->name, sizeof(proc->name)); 2756 2757 pid = np->pid; 2758 2759 // lock to force the compiler to emit the np->state write last. 2760 acquire(&ptable.lock); 2761 np->state = RUNNABLE; 2762 addtoreadv(np); 2763 release(&ptable.lock); 2764 return pid; 2765 } 2766 2767 // Exit the current process. Does not return. 2768 // An exited process remains in the zombie state 2769 // until its parent calls wait() to find out it exited. 2770 void 2771 exit(void) 2772 { 2773 struct proc *p; 2774 int fd; 2775 2776 if(proc == initproc) 2777 panic("init exiting"); 2778 2779 // Close all open files. 2780 for(fd = 0; fd < NOFILE; fd++){ 2781 if(proc->ofile[fd]){ 2782 fileclose(proc->ofile[fd]); 2783 proc->ofile[fd] = 0; 2784 2785 } 2786 2787 begin op(); 2788 iput(proc->cwd); 2789 end op(); 2790 proc->cwd = 0;2791 2792 acquire(&ptable.lock); 2793 2794 // Parent might be sleeping in wait(). 2795 wakeup1(proc->parent); 2796

2749

2748 np->tf->eax = 0;

2797

2798

2799

```
2800 // Pass abandoned children to init.
2801 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){
2802
       if(p->parent == proc){
2803
          p->parent = initproc;
2804
         if(p->state == ZOMBIE)
2805
            wakeup1(initproc);
2806
2807 }
2808 //release(&ptable.lock);
2809 // Jump into the scheduler, never to return.
2810 proc->state = ZOMBIE;
2811 p->next = 0;
2812 sched();
2813 panic("zombie exit");
2814 }
2815
2816 // Wait for a child process to exit and return its pid.
2817 // Return -1 if this process has no children.
2818 int
2819 wait(void)
2820 {
2821 struct proc *p;
2822 int havekids, pid;
2823 acquire(&ptable.lock);
2824 for(;;){
2825
        // Scan through table looking for zombie children.
2826
        havekids = 0;
2827
        for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
2828
         if(p->parent != proc)
2829
          continue;
2830
          havekids = 1;
2831
          if(p->state == ZOMBIE){
2832
           // Found one.
2833
       /* reset the data*/
2834
            pid = p->pid;
2835
            kfree(p->kstack);
2836
            p->kstack = 0;
2837
            freevm(p->pqdir);
2838
            p->state = UNUSED;
2839
               p->pid = 0;
2840
       p->uid = 0;
2841
       p->gid = 0;
2842
       p->ppid = 0;
2843
            p->parent = 0;
2844
            p->name[0] = 0;
2845
            p->killed = 0;
2846
       p->next = 0;
2847
       p->priority=0;
2848
            addtofree(p);
2849
            release(&ptable.lock);
```

```
2850
            return pid;
2851
2852
2853
2854
        // No point waiting if we don't have any children.
2855
        if(!havekids || proc->killed){
2856
          release(&ptable.lock);
2857
          return -1;
2858
2859
2860
        // Wait for children to exit. (See wakeup1 call in proc_exit.)
        sleep(proc, &ptable.lock);
2861
2862 }
2863 }
2864 int//called by things that have locks
2865 dspri2(int pid, int priority)
2866 {
2867 struct proc * p;
2868 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){
2869
     if(p->pid == pid)
2870
      if(p->state == RUNNING){
2871
          p->priority = priority;
2872
          return 0;
2873
2874
         removefromready(p);/*remove from the list it was in*/
2875
         p->priority = priority;
         addtoready(p);/*stick in different list*/
2876
2877
         return 0;
2878 }
2879 }
2880 cprintf("no dice, baby\n");
2881 return 1;
2882 }
2883
2884 int//called by stuff that does not have locks
2885 dspri(int pid, int priority)
2886 {
2887 int ret;
2888 if(priority < 0 || priority > 2)//invalid priority number
2889 return 1;
2890 acquire(&ptable.lock);
2891 //printlists();
2892 ret = dspri2(pid, priority);
2893 //printlists();
2894 release(&ptable.lock);
2895 return ret; /*invalid PID*/
2896 }
2897
2898
2899
```

```
2950 void
2900 // Per-CPU process scheduler.
2901 // Each CPU calls scheduler() after setting itself up.
                                                                                 2951 scheduler(void)
2902 // Scheduler never returns. It loops, doing:
                                                                                 2952 {
2903 // - choose a process to run
                                                                                 2953 struct proc *p=0;
2904 // - swtch to start running that process
                                                                                 2954 ptable.TimeToReset=MaxTimeToReset;//initialize
2905 // - eventually that process transfers control
                                                                                 2955 proc = 0;
            via swtch back to the scheduler.
2906 //
                                                                                 2956
                                                                                        //sti();
2907
                                                                                 2957 for(;;){
2908 void
                                                                                 2958
                                                                                          // Enable interrupts on this processor
2909 resetpriority(void)
                                                                                 2959
                                                                                          sti();
2910 {
                                                                                 2960
                                                                                          //manage priority teset
2911 int toset=1;
                                                                                          acquire(&ptable.lock);
                                                                                 2961
2912 struct proc *tmp;
                                                                                 2962
                                                                                          --ptable.TimeToReset;
2913 //toset=1;
                                                                                 2963
                                                                                          release(&ptable.lock);
2914 acquire(&ptable.lock);
                                                                                 2964
                                                                                          if(!ptable.TimeToReset)/*misleading, it is time to reset*/
2915 ptable.TimeToReset = MaxTimeToReset;
                                                                                 2965
                                                                                            resetpriority();
2916 for(tmp = ptable.proc; tmp < &ptable.proc[NPROC];tmp++){
                                                                                 2966
                                                                                          acquire(&ptable.lock);
2917
       if (tmp->state == UNUSED)//dont change unused processes
                                                                                 2967
                                                                                          //find a process to run
2918
        continue;
                                                                                 2968
                                                                                          //lowest priority first
2919
       if(tmp->priority == toset)//dont change processes of the right priority
                                                                                 2969
                                                                                          if(ptable.pReadyList[2]){
2920
        continue;
                                                                                 2970
                                                                                            //cprintf("item on readv2\n");
2921
       removefromready(tmp);//remove from list if on list
                                                                                 2971
                                                                                            p=ptable.pReadyList[2];
2922
       tmp->priority = toset;
                                                                                 2972
2923
       addtoready(tmp);
                                                                                 2973
                                                                                          //default priority
2924
                                                                                 2974
                                                                                          if(ptable.pReadyList[1]){
2925
                                                                                 2975
                                                                                            //cprintf("item on ready1\n");
2926 /* for(i = 0; i<3; i+=2) { //i should go from r[0] to r[2]
                                                                                 2976
                                                                                            p=ptable.pReadyList[1];
2927
         tmp = ptable.pReadyList[i];//no need to change r[1]
                                                                                 2977
2928
         while(tmp){
                                                                                 2978
                                                                                          //highest priority
2929
          ptable.pReadyList=tmp->next; // dspri will ching wher tmp pt
                                                                                 2979
                                                                                          if(ptable.pReadyList[0]){
                                                                                 2980
2930
          removefromready(tmp)
                                                                                            //cprintf("item on ready0\n");
                                                                                 2981
2931
          tmp->priority = toset;
                                                                                            p=ptable.pReadyList[0];
2932
                                                                                 2982
          addtoready(tmp)
           tmp=ptable.pReadyList[i];//move to next item in ready list
2933
                                                                                 2983
                                                                                          if(!p){//lists are all empty
        \frac{1}{tmp} = 0 at end of loop
                                                                                 2984
2934
                                                                                            release(&ptable.lock);
2935 }*/
                                                                                 2985
                                                                                             continue;
2936 release(&ptable.lock);
                                                                                 2986
2937 }
                                                                                 2987
                                                                                          if(p->state != RUNNABLE) {
2938
                                                                                 2988
                                                                                            panic("oops");
2939
                                                                                 2989
                                                                                              //see if process is any of the state we would expect
2940
                                                                                 2990
                                                                                             /* //debug stuff
2941
                                                                                 2991
                                                                                              if(p->state == SLEEPING)
2942
                                                                                 2992
                                                                                           panic("sleeping ");
2943
                                                                                 2993
                                                                                              if(p->state == ZOMBIE)
2944
                                                                                 2994
                                                                                                panic("zombie ");
2945
                                                                                 2995
                                                                                              if(p->state == UNUSED);
                                                                                                panic("unused ");
2946
                                                                                 2996
2947
                                                                                 2997
                                                                                              if(p->state == EMBRYO)
2948
                                                                                 2998
                                                                                                panic("embryo ");
2949
                                                                                 2999
                                                                                              if(p->state == RUNNING)
```

Sheet 29 Sheet 29

```
3050 // Give up the CPU for one scheduling round.
3000
              panic("already running ");
3001
                                                                                3051 void
3002
                                                                                3052 yield(void)
3003
        // Switch to chosen process. It is the process's job
                                                                                3053 {
3004
        // to release ptable.lock and then reacquire it
                                                                                3054 acquire(&ptable.lock);
3005
        // before jumping back to us.
                                                                                3055 proc->state = RUNNABLE;
3006
        //printlists();
                                                                                3056 addtoready(proc);
3007
        proc = p;
                                                                                3057 sched();
3008
        //cprintf("priority of pid %d is %d\n", p->pid, p->priority);
                                                                                3058 release(&ptable.lock);
3009
        switchuvm(p);
                                                                                3059 }
3010
        removefromready(p);
                                                                                3060
3011
        p->state = RUNNING;
                                                                                3061 // A fork child's very first scheduling by scheduler()
3012
        p->next = 0;
                                                                                3062 // will swtch here. "Return" to user space.
3013
        swtch(&cpu->scheduler, proc->context);
                                                                                3063 void
3014
        switchkvm();
                                                                                3064 forkret(void)
3015
        //cprintf("priority of pid %d is %d\n", p->pid, p->priority);
                                                                                3065 {
3016
        // Process is done running for now.
                                                                                3066 static int first = 1;
                                                                                      // Still holding ptable.lock from scheduler.
3017
        // It should have changed its p->state before coming back.
3018
                                                                                3068 release(&ptable.lock);
3019
        proc = 0;//the old scheduelr did this, kept getting panics without it
                                                                                3069
3020
        release(&ptable.lock);
                                                                                3070 if (first) {
                                                                                        // Some initialization functions must be run in the context
3021
        //proc = 0;
                                                                                3071
3022 }
                                                                                3072
                                                                                         // of a regular process (e.g., they call sleep), and thus cannot
3023 }
                                                                                3073
                                                                                        // be run from main().
3024
                                                                                3074
                                                                                       first = 0;
3025 // Enter scheduler. Must hold only ptable.lock
                                                                                3075
                                                                                        iinit(ROOTDEV);
3026 // and have changed proc->state.
                                                                                3076
                                                                                         initlog(ROOTDEV);
3027 void
                                                                                3077 }
3028 sched(void)
                                                                                3078
                                                                                3079 // Return to "caller", actually trapret (see allocproc).
3029 {
3030 int intena;
                                                                                3080 }
3031
                                                                                3081
3032 if(!holding(&ptable.lock))
                                                                                3082 // Atomically release lock and sleep on chan.
3033
        panic("sched ptable.lock");
                                                                                3083 // Reacquires lock when awakened.
3034 if(cpu->ncli != 1)
                                                                                3084 void
3035
      panic("sched locks");
                                                                                3085 sleep(void *chan, struct spinlock *lk)
3036 if(proc->state == RUNNING)
                                                                                3086 {
3037
        panic("sched running");
                                                                                3087 	 if(proc == 0)
3038 if(readeflags()&FL_IF)
                                                                                3088
                                                                                        panic("sleep");
3039
        panic("sched interruptible");
                                                                                3089
3040 intena = cpu->intena;
                                                                                3090 	 if(1k == 0)
3041
      swtch(&proc->context, cpu->scheduler);
                                                                                3091
                                                                                        panic("sleep without lk");
3042 cpu->intena = intena;
                                                                                3092
3043 }
                                                                                3093 // Must acquire ptable.lock in order to
3044
                                                                                3094 // change p->state and then call sched.
3045
                                                                                3095 // Once we hold ptable.lock, we can be
3046
                                                                                3096 // quaranteed that we won't miss any wakeup
3047
                                                                                3097 // (wakeup runs with ptable.lock locked),
3048
                                                                                3098 // so it's okay to release lk.
3049
                                                                                3099 if(lk != &ptable.lock){
```

Sheet 30 Sheet 30

```
acquire(&ptable.lock);
3100
3101
        release(lk);
3102 }
3103
3104 // Go to sleep.
3105 proc->chan = chan;
3106 proc->state = SLEEPING;
3107 proc->next = 0;
3108 //remove from ready list;
3109 sched();
3110 // Tidy up.
3111 proc->chan = 0;
3112
3113 // Reacquire original lock.
3114 if(lk != &ptable.lock){
3115
        release(&ptable.lock);
        acquire(lk);
3116
3117 }
3118 }
3119
3120
3121
3122
3123
3124
3125
3126
3127
3128
3129
3130
3131
3132
3133
3134
3135
3136
3137
3138
3139
3140
3141
3142
3143
3144
3145
3146
3147
3148
3149
```

```
3150 // Wake up all processes sleeping on chan.
3151 // The ptable lock must be held.
3152 static void
3153 wakeup1(void *chan)
3154 {
3155 struct proc *p;
3156
3157 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++)</pre>
3158
        if(p->state == SLEEPING && p->chan == chan){
3159
          p->state = RUNNABLE;
3160
          addtoready(p);
3161
3162 }
3163
3164 // Wake up all processes sleeping on chan.
3165 void
3166 wakeup(void *chan)
3167 {
3168 acquire(&ptable.lock);
3169 wakeup1(chan);
3170 release(&ptable.lock);
3171 }
3172
3173 // Kill the process with the given pid.
3174 // Process won't exit until it returns
3175 // to user space (see trap in trap.c).
3176 int
3177 kill(int pid)
3178 {
3179 struct proc *p;
3180
3181 acquire(&ptable.lock);
3182 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){
3183
       if(p->pid == pid){
3184
          p->killed = 1;
3185
          // Wake process from sleep if necessary.
3186
          if(p->state == SLEEPING){
3187
            p->state = RUNNABLE;
3188
            addtoready(p);
3189
3190
          release(&ptable.lock);
3191
          return 0;
3192
3193 }
3194 release(&ptable.lock);
3195 return -1;
3196 }
3197
3198
3199
```

```
3200 // Print a process listing to console. For debugging.
                                                                                3250 int
3201 // Runs when user types 'P on console.
                                                                                3251 dogetprocs (int max, struct uproc *table)
3202 // No lock to avoid wedging a stuck machine further.
                                                                                3252 {
                                                                                3253 struct proc *p;
3203 void
3204 procdump(void)
                                                                                3254 int ret = 0;
3205 {
                                                                                3255 int i = 0;
3206 static char *states[] = {
                                                                                3256 enum procstate tmp;
3207 [UNUSED]
                  "unused",
                                                                                3257 acquire(&ptable.lock);
3208 [EMBRYO]
                  "embryo",
                                                                                3258 if (max > NPROC)
3209 [SLEEPING] "sleep",
                                                                                3259
                                                                                       return -1;
3210 [RUNNABLE] "runble",
                                                                                3260 for(p=ptable.proc; p<&ptable.proc[NPROC]; p++){
                                                                                3261
                                                                                       table[i].pid = p->pid;
3211 [RUNNING]
                  "run ",
3212 [ZOMBIE]
                  "zombie"
                                                                                3262
                                                                                         table[i].uid = p->uid;
3213
      };
                                                                                3263
                                                                                         table[i].gid = p->gid;
3214 int i;
                                                                                3264
                                                                                         table[i].ppid = p->ppid;
3215 struct proc *p;
                                                                                3265
                                                                                         tmp = p->state;
3216 char *state;
                                                                                3266
                                                                                       if(tmp == UNUSED){
3217 uint pc[10];
                                                                                3267
                                                                                          goto unused;
3218
                                                                                3268
3219
      for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
                                                                                3269
                                                                                       if(tmp == EMBRYO)
3220
       if(p->state == UNUSED)
                                                                                3270
                                                                                          table[i].state =0;// "EMBRYO";
3221
          continue;
                                                                                3271
                                                                                       if(tmp == SLEEPING)
3222
        if(p->state >= 0 && p->state < NELEM(states) && states[p->state])
                                                                                3272
                                                                                          table[i].state =1;// "SLEEPING";
3223
          state = states[p->state];
                                                                                3273
                                                                                       if(tmp == RUNNABLE)
3224
                                                                                3274
                                                                                         table[i].state =2;// "RUNNABLE";
        else
3225
          state = "???";
                                                                                3275
                                                                                       if(tmp == RUNNING)
3226
                                                                                         table[i].state =3;// "RUNNING";
        cprintf("%d %d %d %s %s %d", p->pid, p->qid, p->uid, state, p->name,p->p: 3276
3227
        if(p->state == SLEEPING){
                                                                                3277
                                                                                       if(tmp == ZOMBIE)
3228
          getcallerpcs((uint*)p->context->ebp+2, pc);
                                                                                3278
                                                                                         table[i].state =4;// "ZOMBIE";
3229
                                                                                3279
                                                                                         table[i].priority = p->priority;
          for(i=0; i<10 && pc[i] != 0; i++)
3230
            cprintf(" %p", pc[i]);
                                                                                3280
                                                                                         table[i].size = p->sz;
3231
                                                                                3281
                                                                                         safestrcpy(table[i].name , p->name,16);
3232
                                                                                3282
                                                                                         /**/
        cprintf("\n");
3233 }
                                                                                3283
                                                                                         ++i;
3234 }
                                                                                3284 unused:
3235
                                                                                3285
                                                                                         ret = i; /*return how many times run*/
3236
                                                                                3286 }
3237
                                                                                3287
3238
                                                                                3288 release(&ptable.lock);
3239
                                                                                3289 return ret;
3240
                                                                                3290 }
3241
                                                                                3291
3242
                                                                                3292
3243
                                                                                3293
3244
                                                                                3294
3245
                                                                                3295
3246
                                                                                3296
3247
                                                                                3297
3248
                                                                                3298
3249
                                                                                3299
```

Sheet 32 Sheet 32

```
3300 int
3301 dgpri(int pid)
3302 {
3303 struct proc * p;
3304 acquire(&ptable.lock);
3305 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
3306
      if(p->pid == pid)
3307
        release(&ptable.lock);
3308
         return p->priority;
3309 }
3310 }
3311 release(&ptable.lock);
3312 return -1; /*invalid PID*/
3313 }
3314
3315
3316
3317
3318
3319
3320
3321
3322
3323
3324
3325
3326
3327
3328
3329
3330
3331
3332
3333
3334
3335
3336
3337
3338
3339
3340
3341
3342
3343
3344
3345
3346
3347
3348
3349
```

```
3350 # Context switch
3351 #
3352 # void swtch(struct context **old, struct context *new);
3353 #
3354 # Save current register context in old
3355 # and then load register context from new.
3356
3357 .globl swtch
3358 swtch:
3359 movl 4(%esp), %eax
3360 movl 8(%esp), %edx
3361
3362 # Save old callee-save registers
3363 pushl %ebp
3364 pushl %ebx
3365 pushl %esi
3366 pushl %edi
3367
3368 # Switch stacks
3369 movl %esp, (%eax)
3370 movl %edx, %esp
3371
3372 # Load new callee-save registers
3373 popl %edi
3374 popl %esi
3375 popl %ebx
3376 popl %ebp
3377 ret
3378
3379
3380
3381
3382
3383
3384
3385
3386
3387
3388
3389
3390
3391
3392
3393
3394
3395
3396
3397
3398
3399
```

```
3400 // Physical memory allocator, intended to allocate
                                                                                3450 void
3401 // memory for user processes, kernel stacks, page table pages,
                                                                                3451 freerange(void *vstart, void *vend)
3402 // and pipe buffers. Allocates 4096-byte pages.
                                                                                3452 {
3403
                                                                                3453 char *p;
3404 #include "types.h"
                                                                                3454 p = (char*)PGROUNDUP((uint)vstart);
                                                                                3455 for(; p + PGSIZE <= (char*)vend; p += PGSIZE)
3405 #include "defs.h"
3406 #include "param.h"
                                                                                3456
                                                                                        kfree(p);
3407 #include "memlayout.h"
                                                                                3457 }
3408 #include "mmu.h"
                                                                                3458
3409 #include "spinlock.h"
                                                                                3459
3410
                                                                                3460 // Free the page of physical memory pointed at by v,
3411 void freerange(void *vstart, void *vend);
                                                                                3461 // which normally should have been returned by a
3412 extern char end[]; // first address after kernel loaded from ELF file
                                                                                3462 // call to kalloc(). (The exception is when
3413
                                                                                3463 // initializing the allocator; see kinit above.)
3414 struct run {
                                                                                3464 void
3415 struct run *next;
                                                                                3465 kfree(char *v)
3416 };
                                                                                3466 {
3417
                                                                                3467 struct run *r;
3418 struct {
                                                                                3468
3419 struct spinlock lock;
                                                                                3469 if((uint)v % PGSIZE || v < end || v2p(v) >= PHYSTOP)
3420 int use lock;
                                                                                3470
                                                                                         panic("kfree");
3421 struct run *freelist;
                                                                                3471
3422 } kmem;
                                                                                3472 // Fill with junk to catch dangling refs.
3423
                                                                                3473 memset(v, 1, PGSIZE);
                                                                                3474
3424 // Initialization happens in two phases.
3425 // 1. main() calls kinit1() while still using entrypgdir to place just
                                                                                3475
                                                                                     if(kmem.use lock)
3426 // the pages mapped by entrypgdir on free list.
                                                                                3476
                                                                                         acquire(&kmem.lock);
3427 // 2. main() calls kinit2() with the rest of the physical pages
                                                                                3477 r = (struct run*)v;
3428 // after installing a full page table that maps them on all cores.
                                                                                3478 r->next = kmem.freelist;
                                                                                3479 kmem.freelist = r;
3429 void
3430 kinit1(void *vstart, void *vend)
                                                                                3480 if(kmem.use lock)
3431 {
                                                                                3481
                                                                                        release(&kmem.lock);
3432 initlock(&kmem.lock, "kmem");
                                                                                3482 }
3433 kmem.use lock = 0;
                                                                                3483
3434 freerange(vstart, vend);
                                                                                3484 // Allocate one 4096-byte page of physical memory.
3435 }
                                                                                3485 // Returns a pointer that the kernel can use.
3436
                                                                                3486 // Returns 0 if the memory cannot be allocated.
3437 void
                                                                                3487 char*
3438 kinit2(void *vstart, void *vend)
                                                                                3488 kalloc(void)
3439 {
                                                                                3489 {
3440 freerange(vstart, vend);
                                                                                3490 struct run *r;
3441 kmem.use lock = 1;
                                                                                3491
3442 }
                                                                                3492 if(kmem.use lock)
3443
                                                                                3493
                                                                                        acquire(&kmem.lock);
3444
                                                                                3494 r = kmem.freelist;
3445
                                                                                3495 if(r)
                                                                                         kmem.freelist = r->next;
3446
                                                                                3496
3447
                                                                                3497 if(kmem.use_lock)
3448
                                                                                       release(&kmem.lock);
                                                                                3499 return (char*)r;
3449
```

Sheet 34 Sheet 34

)	
3500 }	3550 // x86 trap and interrupt constants.
3501	3551
3502	3552 // Processor-defined:
3503	3553 #define T_DIVIDE 0 // divide error
3504	3554 #define T_DEBUG 1 // debug exception
3505	3555 #define T_NMI 2 // non-maskable interrupt
3506	3556 #define T_BRKPT 3 // breakpoint
3507	3557 #define T_OFLOW 4 // overflow
3508	3558 #define T_BOUND 5 // bounds check
3509	3559 #define T_ILLOP 6 // illegal opcode
3510	3560 #define T_DEVICE 7 // device not available
3511	3561 #define T_DBLFLT 8 // double fault
3512	3562 // #define T_COPROC 9 // reserved (not used since 486)
3513	3563 #define T_TSS 10 // invalid task switch segment
3514	3564 #define T_SEGNP 11 // segment not present
3515	3565 #define T_STACK 12 // stack exception
3516	3566 #define T_GPFLT 13 // general protection fault
3517	3567 #define T_PGFLT 14 // page fault
3518	3568 // #define T_RES 15 // reserved
3519	3569 #define T_FPERR 16 // floating point error
3520	3570 #define T_ALIGN 17 // aligment check
3521	3571 #define T_MCHK 18 // machine check
3522	3572 #define T_SIMDERR 19 // SIMD floating point error
3523	3573
3524	3574 // These are arbitrarily chosen, but with care not to overlap
3525	3575 // processor defined exceptions or interrupt vectors.
3526	3576 #define T_SYSCALL 64 // system call
3527	3577 #define T_DEFAULT 500 // catchall
3528	3578
3529	3579 #define T_IRQ0 32 // IRQ 0 corresponds to int T_IRQ
3530	3580 SI 77 ING 0 SSIZESFERIUS SS INC 1_ING
3531	3581 #define IRO_TIMER 0
3532	3582 #define IRQ KBD 1
3533	3583 #define IRQ_COM1 4
3534	3584 #define IRO IDE 14
3535	3585 #define IRQ ERROR 19
3536	3586 #define IRQ_SPURIOUS 31
3537	3587
3538	3588
3539	3589
3540	3590
3540 3541	3591
3542	3592
3543	3593
3544	3594
3545	3595
3545 3546	
3547	3596 3597
3547 3548	359 <i>7</i> 3598
3546 3549	3598
	JJJJ

```
3650 #include "mmu.h"
3600 #!/usr/bin/perl -w
3601
                                                                               3651
3602 # Generate vectors.S, the trap/interrupt entry points.
                                                                              3652 # vectors.S sends all traps here.
3603 # There has to be one entry point per interrupt number
                                                                              3653 .globl alltraps
                                                                              3654 alltraps:
3604 # since otherwise there's no way for trap() to discover
                                                                              3655 # Build trap frame.
3605 # the interrupt number.
                                                                              3656 pushl %ds
3606
3607 print "# generated by vectors.pl - do not edit\n";
                                                                              3657 pushl %es
3608 print "# handlers\n";
                                                                              3658 pushl %fs
3609 print ".globl alltraps\n";
                                                                               3659 pushl %gs
3610 for(my $i = 0; $i < 256; $i++){
                                                                               3660 pushal
       print ".globl vector$i\n";
                                                                              3661
3612
        print "vector$i:\n";
                                                                              3662 # Set up data and per-cpu segments.
                                                                               3663 movw $(SEG_KDATA<<3), %ax
3613
       if(!(\$i == 8 \mid | (\$i >= 10 \&\& \$i <= 14) \mid | \$i == 17))
3614
         print " pushl \$0\n";
                                                                              3664 movw %ax, %ds
3615
                                                                              3665 movw %ax, %es
3616
       print " pushl \$$i\n";
                                                                               3666 movw $(SEG KCPU<<3), %ax
       print " jmp alltraps\n";
3617
                                                                               3667 movw %ax, %fs
3618 }
                                                                              3668 movw %ax, %qs
3619
                                                                               3669
3620 print "\n# vector table\n";
                                                                              3670 # Call trap(tf), where tf=%esp
3621 print ".data\n";
                                                                              3671 pushl %esp
3622 print ".globl vectors\n";
                                                                              3672 call trap
3623 print "vectors:\n";
                                                                              3673 addl $4, %esp
3624 \text{ for}(\text{my $i = 0; $i < 256; $i++)}
                                                                              3674
        print " .long vector$i\n";
3625
                                                                               3675 # Return falls through to trapret...
3626 }
                                                                              3676 .globl trapret
3627
                                                                              3677 trapret:
3628 # sample output:
                                                                               3678 popal
3629 # # handlers
                                                                               3679 popl %gs
3630 # .globl alltraps
                                                                               3680 popl %fs
3631 # .globl vector0
                                                                               3681 popl %es
3632 # vector0:
                                                                               3682 popl %ds
3633 #
         pushl $0
                                                                               3683 addl $0x8, %esp # trapno and errcode
3634 #
         pushl $0
                                                                               3684
3635 # jmp alltraps
                                                                               3685
3636 # ...
                                                                               3686
3637 #
                                                                               3687
3638 # # vector table
                                                                               3688
3639 # .data
                                                                               3689
3640 # .globl vectors
                                                                               3690
3641 # vectors:
                                                                               3691
3642 #
        .long vector0
                                                                               3692
3643 #
         .long vector1
                                                                               3693
3644 #
         .long vector2
                                                                               3694
3645 # ...
                                                                               3695
3646
                                                                               3696
3647
                                                                               3697
3648
                                                                               3698
3649
                                                                               3699
```

```
3700 #include "types.h"
                                                                                 3750 void
3701 #include "defs.h"
                                                                                 3751 trap(struct trapframe *tf)
3702 #include "param.h"
3703 #include "memlayout.h"
                                                                                 3753 if(tf->trapno == T_SYSCALL){
3704 #include "mmu.h"
                                                                                 3754
                                                                                        if(proc->killed)
3705 #include "proc.h"
                                                                                 3755
                                                                                           exit();
3706 #include "x86.h"
                                                                                 3756
                                                                                         proc->tf = tf;
3707 #include "traps.h"
                                                                                 3757
                                                                                          syscall();
3708 #include "spinlock.h"
                                                                                 3758
                                                                                          if(proc->killed)
3709
                                                                                 3759
                                                                                            exit();
3710 // Interrupt descriptor table (shared by all CPUs).
                                                                                 3760
                                                                                          return;
3711 struct gatedesc idt[256];
                                                                                 3761
3712 extern uint vectors[]; // in vectors.S: array of 256 entry pointers
                                                                                 3762
3713 struct spinlock tickslock;
                                                                                 3763 switch(tf->trapno){
3714 uint ticks;
                                                                                 3764 case T_IRQ0 + IRQ_TIMER:
3715
                                                                                 3765
                                                                                          if(cpu->id == 0)
3716 void
                                                                                 3766
                                                                                            acquire(&tickslock);
                                                                                            ticks++;
3717 tvinit(void)
                                                                                 3767
3718 {
                                                                                 3768
                                                                                            wakeup(&ticks);
3719 int i;
                                                                                 3769
                                                                                            release(&tickslock);
3720
                                                                                 3770
3721 for(i = 0; i < 256; i++)
                                                                                 3771
                                                                                          lapiceoi();
3722
       SETGATE(idt[i], 0, SEG_KCODE<<3, vectors[i], 0);</pre>
                                                                                 3772
                                                                                          break;
3723 SETGATE(idt[T_SYSCALL], 1, SEG_KCODE<<3, vectors[T_SYSCALL], DPL_USER);
                                                                                 3773 case T_IRQ0 + IRQ_IDE:
3724
                                                                                 3774
                                                                                         ideintr();
3725 initlock(&tickslock, "time");
                                                                                 3775
                                                                                         lapiceoi();
                                                                                 3776
                                                                                         break;
3726 }
3727
                                                                                 3777 case T_IRQ0 + IRQ_IDE+1:
3728 void
                                                                                 3778
                                                                                         // Bochs generates spurious IDE1 interrupts.
3729 idtinit(void)
                                                                                 3779
                                                                                         break;
3730 {
                                                                                 3780 case T_IRQ0 + IRQ_KBD:
3731 lidt(idt, sizeof(idt));
                                                                                 3781
                                                                                         kbdintr();
                                                                                 3782
                                                                                         lapiceoi();
3732 }
3733
                                                                                 3783
                                                                                         break;
3734
                                                                                 3784 case T_IRQ0 + IRQ_COM1:
3735
                                                                                 3785
                                                                                        uartintr();
3736
                                                                                 3786
                                                                                         lapiceoi();
3737
                                                                                 3787
                                                                                         break;
3738
                                                                                 3788 case T_IRQ0 + 7:
3739
                                                                                 3789 case T_IRQ0 + IRQ_SPURIOUS:
3740
                                                                                 3790
                                                                                          cprintf("cpu%d: spurious interrupt at %x:%x\n",
3741
                                                                                 3791
                                                                                                 cpu->id, tf->cs, tf->eip);
3742
                                                                                 3792
                                                                                          lapiceoi();
3743
                                                                                 3793
                                                                                          break;
                                                                                 3794
3744
3745
                                                                                 3795
3746
                                                                                 3796
3747
                                                                                 3797
3748
                                                                                 3798
3749
                                                                                 3799
```

Sheet 37 Sheet 37

```
3800 default:
                                                                                3850 // System call numbers
3801
        if(proc == 0 || (tf->cs&3) == 0)
                                                                                3851 #define SYS fork
3802
          // In kernel, it must be our mistake.
                                                                                3852 #define SYS exit
3803
          cprintf("unexpected trap %d from cpu %d eip %x (cr2=0x%x)\n",
                                                                                3853 #define SYS_wait
3804
                  tf->trapno, cpu->id, tf->eip, rcr2());
                                                                                3854 #define SYS_pipe
3805
                                                                                3855 #define SYS read
          panic("trap");
3806
                                                                                3856 #define SYS_kill
3807
        // In user space, assume process misbehaved.
                                                                                3857 #define SYS_exec
3808
        cprintf("pid %d %s: trap %d err %d on cpu %d "
                                                                                3858 #define SYS_fstat 8
3809
                "eip 0x%x addr 0x%x--kill proc\n",
                                                                                3859 #define SYS_chdir 9
3810
                proc->pid, proc->name, tf->trapno, tf->err, cpu->id, tf->eip,
                                                                                3860 #define SYS_dup 10
3811
                                                                                3861 #define SYS_getpid 11
                rcr2());
3812
        proc->killed = 1;
                                                                                3862 #define SYS_sbrk 12
3813 }
                                                                                3863 #define SYS_sleep 13
3814
                                                                                3864 #define SYS_uptime 14
3815 // Force process exit if it has been killed and is in user space.
                                                                                3865 #define SYS_open 15
      // (If it is still executing in the kernel, let it keep running
                                                                                3866 #define SYS_write 16
3817 // until it gets to the regular system call return.)
                                                                                3867 #define SYS mknod 17
3818 if(proc && proc->killed && (tf->cs&3) == DPL_USER)
                                                                                3868 #define SYS_unlink 18
3819
        exit();
                                                                                3869 #define SYS link 19
3820
                                                                                3870 #define SYS mkdir 20
3821
      // Force process to give up CPU on clock tick.
                                                                                3871 #define SYS_close 21
      // If interrupts were on while locks held, would need to check nlock.
                                                                                3872 #define SYS halt 22
3823 if(proc && proc->state == RUNNING && tf->trapno == T_IRQ0+IRQ_TIMER)
                                                                                3873 #define SYS_date 23
3824
                                                                                3874 #define SYS_getuid 24
        vield();
3825
                                                                                3875 #define SYS getgid 25
      // Check if the process has been killed since we yielded
3826
                                                                                3876 #define SYS_getppid 26
3827
      if(proc && proc->killed && (tf->cs&3) == DPL_USER)
                                                                                3877 #define SYS_setuid 27
3828
        exit();
                                                                                3878 #define SYS setgid 28
3829 }
                                                                                3879 #define SYS_getprocs 29
3830
                                                                                3880 #define SYS_getpriority 30
3831
                                                                                3881 #define SYS_setpriority 31
3832
                                                                                3882
3833
                                                                                3883
3834
                                                                                3884
3835
                                                                                3885
3836
                                                                                3886
3837
                                                                                3887
3838
                                                                                3888
3839
                                                                                3889
3840
                                                                                3890
3841
                                                                                3891
3842
                                                                                3892
3843
                                                                                3893
3844
                                                                                3894
3845
                                                                                3895
3846
                                                                                3896
3847
                                                                                3897
3848
                                                                                3898
                                                                                3899
3849
```

Sheet 38 Sheet 38

```
3900 #include "types.h"
                                                                                 3950 // Fetch the nth word-sized system call argument as a pointer
3901 #include "defs.h"
                                                                                 3951 // to a block of memory of size n bytes. Check that the pointer
3902 #include "param.h"
                                                                                 3952 // lies within the process address space.
3903 #include "memlayout.h"
                                                                                 3953 int
3904 #include "mmu.h"
                                                                                 3954 argptr(int n, char **pp, int size)
3905 #include "proc.h"
                                                                                 3955 {
3906 #include "x86.h"
                                                                                 3956 int i;
3907 #include "syscall.h"
                                                                                 3957
                                                                                 3958 if(argint(n, &i) < 0)
3909 // User code makes a system call with INT T_SYSCALL.
                                                                                        return -1;
                                                                                 3959
3910 // System call number in %eax.
                                                                                 3960 if((uint)i \ge proc > sz \mid (uint)i + size > proc > sz)
3911 // Arguments on the stack, from the user call to the C
                                                                                 3961
                                                                                         return -1;
3912 // library system call function. The saved user %esp points
                                                                                 3962 *pp = (char*)i;
3913 // to a saved program counter, and then the first argument.
                                                                                 3963 return 0;
3914
                                                                                 3964 }
3915 // Fetch the int at addr from the current process.
                                                                                 3965
                                                                                 3966 // Fetch the nth word-sized system call argument as a string pointer.
3916 int.
3917 fetchint(uint addr. int *ip)
                                                                                 3967 // Check that the pointer is valid and the string is nul-terminated.
3918 {
                                                                                 3968 // (There is no shared writable memory, so the string can't change
3919 if(addr \geq proc\geqsz | addr+4 \geq proc\geqsz)
                                                                                 3969 // between this check and being used by the kernel.)
3920
      return -1;
                                                                                 3970 int
                                                                                 3971 argstr(int n, char **pp)
3921 *ip = *(int*)(addr);
3922 return 0;
                                                                                 3972 {
3923 }
                                                                                 3973 int addr;
3924
                                                                                 3974 if(argint(n, &addr) < 0)
3925 // Fetch the nul-terminated string at addr from the current process.
                                                                                 3975
                                                                                         return -1;
3926 // Doesn't actually copy the string - just sets *pp to point at it.
                                                                                 3976 return fetchstr(addr, pp);
3927 // Returns length of string, not including nul.
                                                                                 3977 }
3928 int.
                                                                                 3978
3929 fetchstr(uint addr, char **pp)
                                                                                 3979 extern int sys_chdir(void);
3930 {
                                                                                 3980 extern int sys_close(void);
3931 char *s, *ep;
                                                                                 3981 extern int sys dup(void);
3932
                                                                                 3982 extern int sys_exec(void);
3933 if(addr \geq proc\geqsz)
                                                                                 3983 extern int sys_exit(void);
                                                                                 3984 extern int sys fork(void);
3934
      return -1;
                                                                                 3985 extern int sys_fstat(void);
3935 *pp = (char*)addr;
3936 ep = (char*)proc->sz;
                                                                                 3986 extern int sys_getpid(void);
3937 for(s = *pp; s < ep; s++)
                                                                                 3987 extern int sys kill(void);
3938
      if(*s == 0)
                                                                                 3988 extern int sys_link(void);
3939
          return s - *pp;
                                                                                 3989 extern int sys_mkdir(void);
3940 return -1;
                                                                                 3990 extern int sys_mknod(void);
3941 }
                                                                                 3991 extern int sys_open(void);
                                                                                 3992 extern int sys_pipe(void);
                                                                                 3993 extern int sys_read(void);
3943 // Fetch the nth 32-bit system call argument.
3944 int
                                                                                 3994 extern int sys_sbrk(void);
                                                                                 3995 extern int sys sleep(void);
3945 argint(int n, int *ip)
3946 {
                                                                                 3996 extern int sys unlink(void);
                                                                                 3997 extern int sys_wait(void);
3947 return fetchint(proc->tf->esp + 4 + 4*n, ip);
                                                                                 3998 extern int sys write(void);
3948 }
3949
                                                                                 3999 extern int sys_uptime(void);
```

4002 4003 4004 4005 4006 4007 4008 4010 4011 4012 4013 4014 4015 4016 4017 4018 4022 4023 4022 4023 4024 4025 4026 4027 4028 4030 4031 4032 4033 4034 4035 4036 4037 4038 4038 4038 4038 4038 4038 4038 4038	extern int sys_setuid(void); extern int sys_setgid(void); extern int sys_getprocs(void); extern int sys_getpriority(void); extern int sys_getpriority(void); extern int sys_setpriority(void); extern int sys_setpriority(void); extern int sys_setpriority(void); extern int sys_setpriority(void); extern int sys_getpriority(void); extern int sys_getpriority(void); extern int sys_setpriority(void); extern int sys_setp
4038 4039	[SYS_setgid] sys_setgid, [SYS_getprocs] sys_getprocs,
4041	[SYS_setpriority] sys_setpriority, }:
4043	J'
4044	
4045	
4046	
4047	
4048 4049	
4049	

```
4050 void getname(int num)
4051 {
4052
         switch(num)/*figure out which syscall*/
4053
4054
                case 1:
4055
                        cprintf("fork -> ");
4056
                        return;
4057
                case 2:
4058
                        cprintf("exit -> ");
4059
                        return;
4060
                case 3:
                        cprintf("wait -> ");
4061
4062
                        return;
4063
                case 4:
4064
                        cprintf("pipe -> ");
4065
                        return;
4066
                case 5:
                        cprintf("read -> ");
4067
4068
                        return;
4069
                case 6:
                        cprintf("kill -> ");
4070
4071
                        return;
                case 7:
4072
4073
                        cprintf("exec -> ");
4074
                        return;
4075
                case 8:
4076
                        cprintf("fstat -> ");
4077
                        return;
                case 9:
4078
4079
                        cprintf("chdir -> ");
4080
                        return;
4081
                case 10:
4082
                        cprintf("dup -> ");
4083
                        return;
4084
                case 11:
4085
                        cprintf("getpid -> ");
4086
                        return;
4087
                case 12:
4088
                        cprintf("sbrk -> ");
4089
                        return;
4090
                case 13:
4091
                        cprintf("sleep -> ");
4092
                        return;
                case 14:
4093
4094
                        cprintf("uptime -> ");
4095
                        return;
                case 15:
4096
4097
                        cprintf("open -> ");
4098
                        return;
4099
                case 16:
```

Sheet 40 Sheet 40

4100	<pre>cprintf("write -> ");</pre>	4150 #include "types.h"
4101	return;	4151 #include "x86.h"
4102	case 17:	4152 #include "defs.h"
4103	<pre>cprintf("mknod -> ");</pre>	4153 #include "date.h"
4104	return;	4154 #include "param.h"
4105	case 18:	4155 #include "memlayout.h"
4106	<pre>cprintf("unlink -> ");</pre>	4156 #include "mmu.h"
4107	return;	4157 #include "proc.h"
4108	case 19:	4158 #include "psc.h"
4109	<pre>cprintf("link -> ");</pre>	4159 int
4110	return;	4160 sys_fork(void)
4111	case 20:	4161 {
4112	<pre>cprintf("mkdir -> ");</pre>	4162 return fork();
4113	return;	4163 }
4114	case 21:	4164
4115	<pre>cprintf("close -> ");</pre>	4165 int
4116	return;	4166 sys_exit(void)
4117	case 22:	4167 {
4118	<pre>cprintf("halt -> ");</pre>	4168 exit();
4119	return;	4169 return 0; // not reached
4120	default:	4170 }
4121	cprintf("ERROR \n");	4171
4122 }		4172 int
4123 }		4173 sys_wait(void)
4124		4174 {
4125 void		4175 return wait();
4126 sysca	ll(void)	4176 }
4127 {		4177
4128 int	num;	4178 int
4129 num	= proc->tf->eax;	4179 sys_kill(void)
4130 if(num > 0 && num < NELEM(syscalls) && syscalls[num]) {	4180 {
4131 /	*getname(num);*//*call lookup table*/	4181 int pid;
4132 p	roc->tf->eax = syscalls[num]();	4182
	cprintf("%d\n",proc->tf->eax);/	4183 if(argint(0, &pid) < 0)
4134	opinion va (ii /proo · or · oai) · /	4184 return -1;
4135		4185 return kill(pid);
	lse {	4186 }
,		,
	printf("%d %s: unknown sys call %d\n",	4187
4138	proc->pid, proc->name, num);	4188 int
	roc->tf->eax = -1;	4189 sys_getpid(void)
4140 }		4190 {
4141 }		4191 return proc->pid;
4142		4192 }
4143		4193
4144		4194
4145		4195
4146		4196
4147		4197
4148		4198
4149		4199
-		•

```
4200 int
                                                                              4250 //Turn of the computer
4201 sys_sbrk(void)
                                                                              4251 int sys_halt(void){
4202 {
                                                                              4252 cprintf("Shutting down ...\n");
4203 int addr;
                                                                              4253 outw (0xB004, 0x0 | 0x2000);
4204 int n;
                                                                              4254 return 0;
4205
                                                                              4255 }
4206 if(argint(0, &n) < 0)
                                                                              4256
4207 return -1;
                                                                              4257 int
4208 addr = proc->sz;
                                                                              4258 sys_date(void){
4209 if(growproc(n) < 0)
                                                                              4259
4210
      return -1;
                                                                              4260 struct rtcdate *r;
4211 return addr;
                                                                              4261 if (argptr(0,(void*)&r, sizeof(&r)) < 0)
4212 }
                                                                              4262 return -1;
4213
                                                                              4263 cmostime(r);
4214 int
                                                                              4264 return 0;
4215 sys_sleep(void)
                                                                              4265 }
4216 {
                                                                              4266
                                                                              4267 int
4217 int n;
4218 uint ticks0;
                                                                              4268 sys_getuid(void){
4219
                                                                              4269 return proc->uid;
4220 if(argint(0, &n) < 0)
                                                                              4270 }
4221
      return -1;
                                                                              4271
                                                                              4272 int
4222 acquire(&tickslock);
4223 ticks0 = ticks;
                                                                              4273 sys_getgid(void){
4224 while(ticks - ticks0 < n){
                                                                              4274 return proc->qid;
4225
      if(proc->killed){
                                                                              4275 }
4226
        release(&tickslock);
                                                                              4276
4227
          return -1;
                                                                              4277 int
4228
                                                                              4278 sys getppid(void){
4229
      sleep(&ticks, &tickslock);
                                                                              4279 return proc->parent->pid;
4230 }
                                                                              4280 }
4231 release(&tickslock);
                                                                              4281
4232 return 0;
                                                                              4282 int
4233 }
                                                                              4283 sys_setuid(void){
                                                                              4284 int val;
4234
4235 // return how many clock tick interrupts have occurred
                                                                              4285 if(argint(0, \&val) < 0)
4236 // since start.
                                                                              4286 return -1;
4237 int.
                                                                              4287 proc->uid = val;
4238 sys_uptime(void)
                                                                              4288 return 0;
4239 {
                                                                              4289 }
4240 uint xticks;
                                                                              4290
4241
                                                                              4291 int
4242 acquire(&tickslock);
                                                                              4292 sys_setgid(void){
4243 xticks = ticks;
                                                                              4293 int val;
4244 release(&tickslock);
                                                                              4294 if(argint(0, &val) < 0)
4245 return xticks;
                                                                              4295 return -1;
                                                                              4296 proc->qid = val;
4246 }
4247
                                                                              4297 return 0;
4248
                                                                              4298 }
4249
                                                                              4299
```

Sheet 43 Sheet 43

4400 struct buf {	4450 #define O_RDONLY 0x000
4401 int flags;	4451 #define O_WRONLY 0x001
4402 uint dev;	4452 #define O_RDWR 0x002
4403 uint blockno;	4453 #define O_CREATE 0x200
4404 struct buf *prev; // LRU cache list	4454
4405 struct buf *next;	4455
4406 struct buf *qnext; // disk queue	4456
4407 uchar data[BSIZE];	4457
4408 };	4458
4409 #define B_BUSY 0x1 // buffer is locked by some process	4459
4410 #define B_VALID 0x2 // buffer has been read from disk	4460
4408 }; 4409 #define B_BUSY 0x1 // buffer is locked by some process 4410 #define B_VALID 0x2 // buffer has been read from disk 4411 #define B_DIRTY 0x4 // buffer needs to be written to disk	4461
4412	4462
4413	4463
4414	4464
4415	4465
4416	4466
4417	4467
4418	4468
4419	4469
4420	4470
4421	4471
4422	4472
4423	4473
4424	4474
4425	4475
4426	4476
4427	4477
4428	
	4478
4429	4479
4430	4480
4431	4481
4432	4482
4433	4483
4434	4484
4435	4485
4436	4486
4437	4487
4438	4488
4439	4489
4440	4490
4441	4491
4442	4492
4443	4493
4444	4494
4445	4495
4446	4496
4447	4497
4448	4498
4449	4499
	

Sheet 44 Sheet 44

```
4500 #define T_DIR 1 // Directory
4501 #define T FILE 2 // File
4502 #define T DEV 3 // Device
4503
4504 struct stat {
4505 short type; // Type of file
4506 int dev;
                  // File system's disk device
4507 uint ino;
                // Inode number
4508 short nlink; // Number of links to file
4509 uint size; // Size of file in bytes
4510 };
4511
4512
4513
4514
4515
4516
4517
4518
4519
4520
4521
4522
4523
4524
4525
4526
4527
4528
4529
4530
4531
4532
4533
4534
4535
4536
4537
4538
4539
4540
4541
4542
4543
4544
4545
4546
4547
4548
4549
```

```
4550 // On-disk file system format.
4551 // Both the kernel and user programs use this header file.
4552
4553
4554 #define ROOTINO 1 // root i-number
4555 #define BSIZE 512 // block size
4556
4557 // Disk layout:
4558 // [ boot block | super block | log | inode blocks | free bit map | data block
4560 // mkfs computes the super block and builds an initial file system. The super
4561 // the disk layout:
4562 struct superblock {
4563 uint size;
                         // Size of file system image (blocks)
4564 uint nblocks;
                         // Number of data blocks
4565 uint ninodes;
                         // Number of inodes.
4566 uint nlog;
                         // Number of log blocks
4567 uint logstart;
                         // Block number of first log block
4568 uint inodestart; // Block number of first inode block
4569 uint bmapstart;
                         // Block number of first free map block
4570 };
4571
4572 #define NDIRECT 12
4573 #define NINDIRECT (BSIZE / sizeof(uint))
4574 #define MAXFILE (NDIRECT + NINDIRECT)
4576 // On-disk inode structure
4577 struct dinode {
4578 short type;
                            // File type
4579 short major;
                            // Major device number (T_DEV only)
4580 short minor;
                            // Minor device number (T_DEV only)
4581 short nlink;
                            // Number of links to inode in file system
4582 uint size;
                            // Size of file (bytes)
4583 uint addrs[NDIRECT+1]; // Data block addresses
4584 };
4585
4586
4587
4588
4589
4590
4591
4592
4593
4594
4595
4596
4597
4598
4599
```

```
4600 // Inodes per block.
                                                                                4650 struct file {
4601 #define IPB
                          (BSIZE / sizeof(struct dinode))
                                                                                4651 enum { FD_NONE, FD_PIPE, FD_INODE } type;
4602
                                                                                4652 int ref; // reference count
4603 // Block containing inode i
                                                                                4653 char readable;
4604 #define IBLOCK(i, sb) ((i) / IPB + sb.inodestart)
                                                                                4654 char writable;
                                                                                4655 struct pipe *pipe;
                                                                                4656 struct inode *ip;
4606 // Bitmap bits per block
4607 #define BPB
                                                                                4657 uint off;
                          (BSIZE*8)
4608
                                                                                4658 };
4609 // Block of free map containing bit for block b
                                                                                4659
4610 #define BBLOCK(b, sb) (b/BPB + sb.bmapstart)
                                                                                4660
                                                                                4661 // in-memory copy of an inode
4612 // Directory is a file containing a sequence of dirent structures.
                                                                                4662 struct inode {
4613 #define DIRSIZ 14
                                                                                4663 uint dev;
                                                                                                          // Device number
4614
                                                                                4664 uint inum;
                                                                                                          // Inode number
4615 struct dirent {
                                                                                4665 int ref;
                                                                                                          // Reference count
4616 ushort inum;
                                                                                4666 int flags;
                                                                                                          // I_BUSY, I_VALID
4617 char name[DIRSIZ];
                                                                                4667
4618 };
                                                                                4668 short type;
                                                                                                          // copy of disk inode
4619
                                                                                4669 short major;
4620
                                                                                4670 short minor;
4621
                                                                                4671 short nlink;
4622
                                                                                4672 uint size;
4623
                                                                                4673 uint addrs[NDIRECT+1];
4624
                                                                                4674 };
4625
                                                                                4675 #define I BUSY 0x1
4626
                                                                                4676 #define I_VALID 0x2
4627
                                                                                4677
4628
                                                                                4678 // table mapping major device number to
4629
                                                                                4679 // device functions
4630
                                                                                4680 struct devsw {
4631
                                                                                4681 int (*read)(struct inode*, char*, int);
4632
                                                                                4682 int (*write)(struct inode*, char*, int);
4633
                                                                                4683 };
4634
                                                                                4684
4635
                                                                                4685 extern struct devsw devsw[];
4636
4637
                                                                                4687 #define CONSOLE 1
4638
                                                                                4688
4639
                                                                                4689
4640
                                                                                4690
4641
                                                                                4691
4642
                                                                                4692
4643
                                                                                4693
4644
                                                                                4694
4645
                                                                                4695
4646
                                                                                4696
4647
                                                                                4697
4648
                                                                                4698
4649
                                                                                4699
```

```
4700 // Blank page.
4701
4702
4703
4704
4705
4706
4707
4708
4709
4710
4711
4712
4713
4714
4715
4716
4717
4718
4719
4720
4721
4722
4723
4724
4725
4726
4727
4728
4729
4730
4731
4732
4733
4734
4735
4736
4737
4738
4739
4740
4741
4742
4743
4744
4745
4746
4747
4748
4749
```

```
4750 // Simple PIO-based (non-DMA) IDE driver code.
4751
4752 #include "types.h"
4753 #include "defs.h"
4754 #include "param.h"
4755 #include "memlayout.h"
4756 #include "mmu.h"
4757 #include "proc.h"
4758 #include "x86.h"
4759 #include "traps.h"
4760 #include "spinlock.h"
4761 #include "fs.h"
4762 #include "buf.h"
4763
4764 #define SECTOR_SIZE 512
4765 #define IDE_BSY
                           0x80
4766 #define IDE_DRDY
                           0x40
4767 #define IDE_DF
                           0x20
4768 #define IDE_ERR
                           0x01
4769
4770 #define IDE_CMD_READ 0x20
4771 #define IDE_CMD_WRITE 0x30
4772
4773 // idequeue points to the buf now being read/written to the disk.
4774 // idequeue->qnext points to the next buf to be processed.
4775 // You must hold idelock while manipulating queue.
4777 static struct spinlock idelock;
4778 static struct buf *idequeue;
4779
4780 static int havedisk1;
4781 static void idestart(struct buf*);
4782
4783 // Wait for IDE disk to become ready.
4784 static int
4785 idewait(int checkerr)
4786 {
4787 int r;
4788
4789 while(((r = inb(0x1f7)) & (IDE_BSY|IDE_DRDY)) != IDE_DRDY)
4790
4791 if(checkerr && (r & (IDE_DF|IDE_ERR)) != 0)
4792
       return -1;
4793 return 0;
4794 }
4795
4796
4797
4798
4799
```

```
4800 void
                                                                              4850 // Interrupt handler.
4801 ideinit(void)
                                                                              4851 void
4802 {
                                                                              4852 ideintr(void)
4803 int i;
                                                                              4853 {
4804
                                                                              4854 struct buf *b;
4805 initlock(&idelock, "ide");
                                                                              4855
4806 picenable(IRQ_IDE);
                                                                              4856 // First gueued buffer is the active request.
4807 ioapicenable(IRQ_IDE, ncpu - 1);
                                                                              4857 acquire(&idelock);
4808 idewait(0);
                                                                              4858 if((b = idequeue) == 0){
4809
                                                                                     release(&idelock);
                                                                              4859
4810 // Check if disk 1 is present
                                                                              4860
                                                                                      // cprintf("spurious IDE interrupt\n");
4811 outb(0x1f6, 0xe0 | (1<<4));
                                                                              4861
                                                                                       return;
4812 for(i=0; i<1000; i++){
                                                                              4862
4813
      if(inb(0x1f7) != 0){
                                                                              4863 idequeue = b->qnext;
4814
          havedisk1 = 1;
                                                                              4864
4815
          break;
                                                                              4865 // Read data if needed.
4816
                                                                              4866 if(!(b->flags & B_DIRTY) && idewait(1) >= 0)
                                                                                     insl(0x1f0, b->data, BSIZE/4);
4817 }
                                                                              4867
4818
                                                                              4868
4819 // Switch back to disk 0.
                                                                              4869 // Wake process waiting for this buf.
4820 outb(0x1f6, 0xe0 | (0<<4));
                                                                              4870 b->flags = B_VALID;
4821 }
                                                                              4871 b->flags &= ~B_DIRTY;
4822
                                                                              4872 wakeup(b);
4823 // Start the request for b. Caller must hold idelock.
                                                                              4873
4824 static void
                                                                              4874 // Start disk on next buf in queue.
4825 idestart(struct buf *b)
                                                                              4875 if(idequeue != 0)
                                                                                      idestart(idequeue);
4826 {
                                                                              4876
4827 if(b == 0)
                                                                              4877
4828
      panic("idestart");
                                                                              4878 release(&idelock);
4829 if(b->blockno >= FSSIZE)
                                                                              4879 }
4830
      panic("incorrect blockno");
                                                                              4880
4831 int sector_per_block = BSIZE/SECTOR_SIZE;
                                                                              4881
4832 int sector = b->blockno * sector_per_block;
                                                                              4882
4833
                                                                              4883
4834 if (sector_per_block > 7) panic("idestart");
                                                                              4884
4835
                                                                              4885
4836 idewait(0);
                                                                              4886
4837 outb(0x3f6, 0); // generate interrupt
                                                                              4887
4838 outb(0x1f2, sector_per_block); // number of sectors
                                                                              4888
4839 outb(0x1f3, sector & 0xff);
                                                                              4889
4840 outb(0x1f4, (sector >> 8) & 0xff);
                                                                              4890
4841 outb(0x1f5, (sector >> 16) & 0xff);
                                                                              4891
4842 outb(0x1f6, 0xe0 | ((b->dev&1)<<4) | ((sector>>24)&0x0f));
                                                                              4892
4843 if(b->flags & B DIRTY){
                                                                              4893
      outb(0x1f7, IDE_CMD_WRITE);
4844
                                                                              4894
4845
        outsl(0x1f0, b->data, BSIZE/4);
                                                                              4895
4846 } else {
                                                                              4896
4847
       outb(0x1f7, IDE_CMD_READ);
                                                                              4897
4848 }
                                                                              4898
4849 }
                                                                              4899
```

```
4950 // Buffer cache.
4900 // Sync buf with disk.
4901 // If B DIRTY is set, write buf to disk, clear B DIRTY, set B VALID.
                                                                                 4951 //
4902 // Else if B VALID is not set, read buf from disk, set B VALID.
                                                                                 4952 // The buffer cache is a linked list of buf structures holding
4903 void
                                                                                 4953 // cached copies of disk block contents. Caching disk blocks
4904 iderw(struct buf *b)
                                                                                 4954 // in memory reduces the number of disk reads and also provides
4905 {
                                                                                 4955 // a synchronization point for disk blocks used by multiple processes.
4906 struct buf **pp;
                                                                                 4956 //
                                                                                 4957 // Interface:
4907
4908 if(!(b->flags & B BUSY))
                                                                                 4958 // * To get a buffer for a particular disk block, call bread.
       panic("iderw: buf not busy");
                                                                                 4959 // * After changing buffer data, call bwrite to write it to disk.
4909
4910 if((b->flags & (B_VALID|B_DIRTY)) == B_VALID)
                                                                                 4960 // * When done with the buffer, call brelse.
                                                                                 4961 \text{ // *} Do not use the buffer after calling brelse.
4911
       panic("iderw: nothing to do");
4912 if(b->dev != 0 && !havedisk1)
                                                                                 4962 // * Only one process at a time can use a buffer,
4913
        panic("iderw: ide disk 1 not present");
                                                                                 4963 //
                                                                                            so do not keep them longer than necessary.
4914
                                                                                4964 //
4915 acquire(&idelock);
                                                                                 4965 // The implementation uses three state flags internally:
4916
                                                                                 4966 // * B BUSY: the block has been returned from bread
4917 // Append b to idequeue.
                                                                                 4967 // and has not been passed back to brelse.
4918 b->gnext = 0;
                                                                                 4968 // * B_VALID: the buffer data has been read from the disk.
4919 for(pp=&idequeue; *pp; pp=&(*pp)->qnext)
                                                                                 4969 // * B DIRTY: the buffer data has been modified
                                                                                4970 //
                                                                                            and needs to be written to disk.
4920
4921 *pp = b;
                                                                                4971
4922
                                                                                 4972 #include "types.h"
4923 // Start disk if necessary.
                                                                                4973 #include "defs.h"
4924 if(idequeue == b)
                                                                                4974 #include "param.h"
4925
       idestart(b);
                                                                                4975 #include "spinlock.h"
4926
                                                                                4976 #include "fs.h"
4927 // Wait for request to finish.
                                                                                4977 #include "buf.h"
4928 while((b->flags & (B VALID|B DIRTY)) != B VALID){
                                                                                4978
4929
       sleep(b, &idelock);
                                                                                 4979 struct {
4930 }
                                                                                 4980 struct spinlock lock;
4931
                                                                                 4981 struct buf buf[NBUF];
4932 release(&idelock);
                                                                                4982
4933 }
                                                                                 4983 // Linked list of all buffers, through prev/next.
4934
                                                                                4984 // head.next is most recently used.
4935
                                                                                 4985 struct buf head;
4936
                                                                                 4986 } bcache;
4937
                                                                                 4987
4938
                                                                                 4988 void
4939
                                                                                 4989 binit(void)
4940
                                                                                 4990 {
4941
                                                                                 4991 struct buf *b;
4942
4943
                                                                                 4993 initlock(&bcache.lock, "bcache");
                                                                                4994
4944
                                                                                 4995
4945
4946
                                                                                 4996
4947
                                                                                 4997
4948
                                                                                 4998
                                                                                 4999
4949
```

```
5000 // Create linked list of buffers
                                                                               5050 // Return a B_BUSY buf with the contents of the indicated block.
5001 bcache.head.prev = &bcache.head;
                                                                               5051 struct buf*
5002 bcache.head.next = &bcache.head;
                                                                               5052 bread(uint dev, uint blockno)
5003 for(b = bcache.buf; b < bcache.buf+NBUF; b++){</pre>
                                                                               5053 {
5004
       b->next = bcache.head.next;
                                                                               5054 struct buf *b;
5005
        b->prev = &bcache.head;
                                                                               5055
5006
        b->dev = -1;
                                                                               5056 b = bget(dev, blockno);
5007
        bcache.head.next->prev = b;
                                                                               5057 if(!(b->flags & B_VALID)) {
5008
        bcache.head.next = b;
                                                                               5058
                                                                                        iderw(b);
5009 }
                                                                               5059 }
5010 }
                                                                               5060 return b;
5011
                                                                               5061 }
5012 // Look through buffer cache for block on device dev.
                                                                               5062
5013 // If not found, allocate a buffer.
                                                                               5063 // Write b's contents to disk. Must be B BUSY.
5014 // In either case, return B BUSY buffer.
                                                                               5064 void
5015 static struct buf*
                                                                               5065 bwrite(struct buf *b)
5016 bget(uint dev, uint blockno)
                                                                               5066 {
5017 {
                                                                               5067 if((b->flags & B BUSY) == 0)
5018 struct buf *b;
                                                                               5068
                                                                                        panic("bwrite");
5019
                                                                               5069 b->flags |= B_DIRTY;
                                                                               5070 iderw(b);
5020 acquire(&bcache.lock);
5021
                                                                               5071 }
5022 loop:
                                                                               5072
5023 // Is the block already cached?
                                                                               5073 // Release a B_BUSY buffer.
5024 for(b = bcache.head.next; b != &bcache.head; b = b->next){
                                                                               5074 // Move to the head of the MRU list.
5025
       if(b->dev == dev && b->blockno == blockno){
                                                                               5075 void
5026
          if(!(b->flags & B_BUSY)){
                                                                               5076 brelse(struct buf *b)
5027
            b->flags |= B_BUSY;
                                                                               5077 {
                                                                               5078 if((b->flags & B_BUSY) == 0)
5028
            release(&bcache.lock);
5029
                                                                                        panic("brelse");
            return b;
                                                                               5079
5030
                                                                               5080
5031
          sleep(b, &bcache.lock);
                                                                               5081 acquire(&bcache.lock);
5032
                                                                               5082
          goto loop;
5033
                                                                               5083 b->next->prev = b->prev;
5034 }
                                                                               5084 b->prev->next = b->next;
5035
                                                                               5085 b->next = bcache.head.next;
5036 // Not cached; recycle some non-busy and clean buffer.
                                                                               5086 b->prev = &bcache.head;
5037 // "clean" because B DIRTY and !B BUSY means log.c
                                                                               5087 bcache.head.next->prev = b;
5038 // hasn't yet committed the changes to the buffer.
                                                                               5088 bcache.head.next = b;
for(b = bcache.head.prev; b != &bcache.head; b = b->prev){
                                                                               5089
5040
      if((b-)flags \& B BUSY) == 0 \&\& (b-)flags \& B DIRTY) == 0){
                                                                               5090 b->flags &= ~B BUSY;
5041
          b->dev = dev;
                                                                                    wakeup(b);
                                                                               5091
5042
          b->blockno = blockno;
                                                                               5092
5043
          b->flags = B BUSY;
                                                                               5093 release(&bcache.lock);
5044
          release(&bcache.lock);
                                                                               5094 }
5045
          return b;
                                                                               5095
5046
                                                                               5096
5047 }
                                                                               5097
5048 panic("bget: no buffers");
                                                                               5098
5049 }
                                                                               5099
```

Sheet 50 Sheet 50

```
5100 // Blank page.
5101
5102
5103
5104
5105
5106
5107
5108
5109
5110
5111
5112
5113
5114
5115
5116
5117
5118
5119
5120
5121
5122
5123
5124
5125
5126
5127
5128
5129
5130
5131
5132
5133
5134
5135
5136
5137
5138
5139
5140
5141
5142
5143
5144
5145
5146
5147
5148
5149
```

```
5150 #include "types.h"
5151 #include "defs.h"
5152 #include "param.h"
5153 #include "spinlock.h"
5154 #include "fs.h"
5155 #include "buf.h"
5156
5157 // Simple logging that allows concurrent FS system calls.
5158 //
5159 // A log transaction contains the updates of multiple FS system
5160 // calls. The logging system only commits when there are
5161 // no FS system calls active. Thus there is never
5162 // any reasoning required about whether a commit might
5163 // write an uncommitted system call's updates to disk.
5165 // A system call should call begin_op()/end_op() to mark
5166 // its start and end. Usually begin_op() just increments
5167 // the count of in-progress FS system calls and returns.
5168 // But if it thinks the log is close to running out, it
5169 // sleeps until the last outstanding end op() commits.
5170 //
5171 // The log is a physical re-do log containing disk blocks.
5172 // The on-disk log format:
5173 // header block, containing block #s for block A, B, C, ...
5174 // block A
5175 // block B
5176 // block C
5177 // ...
5178 // Log appends are synchronous.
5180 // Contents of the header block, used for both the on-disk header block
5181 // and to keep track in memory of logged block# before commit.
5182 struct logheader {
5183 int n;
5184 int block[LOGSIZE];
5185 };
5186
5187 struct log {
5188 struct spinlock lock;
5189 int start;
5190 int size;
5191 int outstanding; // how many FS sys calls are executing.
5192 int committing; // in commit(), please wait.
5193 int dev;
5194 struct logheader lh;
5195 };
5196
5197
5198
5199
```

```
5250 // Write in-memory log header to disk.
                                                                               5251 // This is the true point at which the
5201
5202 static void recover from log(void);
                                                                               5252 // current transaction commits.
5203 static void commit();
                                                                               5253 static void
5204
                                                                               5254 write_head(void)
5205 void
                                                                               5255 {
5206 initlog(int dev)
                                                                               5256 struct buf *buf = bread(log.dev, log.start);
                                                                               5257 struct logheader *hb = (struct logheader *) (buf->data);
5207 {
5208 if (sizeof(struct logheader) >= BSIZE)
                                                                               5258 int i;
        panic("initlog: too big logheader");
                                                                               5259 hb->n = log.lh.n;
5209
5210
                                                                               5260 for (i = 0; i < log.lh.n; i++) {
5211 struct superblock sb;
                                                                                       hb->block[i] = log.lh.block[i];
                                                                               5261
5212 initlock(&log.lock, "log");
                                                                               5262 }
                                                                               5263 bwrite(buf);
5213 readsb(dev, &sb);
5214 log.start = sb.logstart;
                                                                               5264 brelse(buf);
5215 log.size = sb.nlog;
                                                                               5265 }
109.dev = dev;
                                                                               5266
5217 recover_from_log();
                                                                               5267 static void
5218 }
                                                                               5268 recover_from_log(void)
5219
                                                                               5269 {
5220 // Copy committed blocks from log to their home location
                                                                               5270 read head();
5221 static void
                                                                               5271 install_trans(); // if committed, copy from log to disk
5222 install trans(void)
                                                                               5272 \log.1h.n = 0;
5223 {
                                                                               5273 write_head(); // clear the log
5224 int tail;
                                                                               5274 }
5225
                                                                               5275
5226 for (tail = 0; tail < log.lh.n; tail++) {
                                                                               5276 // called at the start of each FS system call.
5227
        struct buf *lbuf = bread(log.dev, log.start+tail+1); // read log block 5277 void
5228
        struct buf *dbuf = bread(log.dev, log.lh.block[tail]); // read dst
                                                                               5278 begin op(void)
5229
        memmove(dbuf->data, lbuf->data, BSIZE); // copy block to dst
                                                                               5279 {
5230
        bwrite(dbuf); // write dst to disk
                                                                               5280 acquire(&log.lock);
5231
        brelse(lbuf);
                                                                               5281 while(1){
5232
        brelse(dbuf);
                                                                               5282
                                                                                        if(log.committing){
5233 }
                                                                               5283
                                                                                          sleep(&log, &log.lock);
5234 }
                                                                               5284
                                                                                        } else if(log.lh.n + (log.outstanding+1)*MAXOPBLOCKS > LOGSIZE){
                                                                               5285
                                                                                        // this op might exhaust log space; wait for commit.
5235
                                                                                          sleep(&log, &log.lock);
5236 // Read the log header from disk into the in-memory log header
                                                                               5286
5237 static void
                                                                               5287
                                                                                      } else {
5238 read_head(void)
                                                                               5288
                                                                                          log.outstanding += 1;
5239 {
                                                                               5289
                                                                                          release(&log.lock);
5240 struct buf *buf = bread(log.dev, log.start);
                                                                               5290
                                                                                          break;
5241 struct logheader *lh = (struct logheader *) (buf->data);
                                                                               5291
5242 int i;
                                                                               5292 }
5243 log.lh.n = lh->n;
                                                                               5293 }
5244 for (i = 0; i < log.lh.n; i++)
                                                                               5294
       log.lh.block[i] = lh->block[i];
                                                                               5295
5245
5246 }
                                                                               5296
5247 brelse(buf);
                                                                               5297
5248 }
                                                                               5298
                                                                               5299
5249
```

Sheet 52 Sheet 52

```
5350 static void
5300 // called at the end of each FS system call.
5301 // commits if this was the last outstanding operation.
                                                                               5351 commit()
5302 void
                                                                               5352 {
                                                                               5353 if (log.lh.n > 0) {
5303 end_op(void)
5304 {
                                                                               5354
                                                                                        write_log();
                                                                                                        // Write modified blocks from cache to log
5305 int do commit = 0;
                                                                               5355
                                                                                        write head();  // Write header to disk -- the real commit
5306
                                                                               5356
                                                                                        install_trans(); // Now install writes to home locations
5307 acquire(&log.lock);
                                                                               5357
                                                                                        log.lh.n = 0;
5308 log.outstanding -= 1;
                                                                               5358
                                                                                        write head();  // Erase the transaction from the log
5309 if(log.committing)
                                                                               5359 }
5310
      panic("log.committing");
                                                                               5360 }
5311 if(log.outstanding == 0){
                                                                               5361
5312
        do_commit = 1;
                                                                               5362 // Caller has modified b->data and is done with the buffer.
5313
        log.committing = 1;
                                                                               5363 // Record the block number and pin in the cache with B_DIRTY.
5314 } else {
                                                                               5364 // commit()/write_log() will do the disk write.
5315
        // begin_op() may be waiting for log space.
                                                                               5365 //
5316
        wakeup(&log);
                                                                               5366 // log_write() replaces bwrite(); a typical use is:
5317 }
                                                                               5367 // bp = bread(...)
5318 release(&log.lock);
                                                                               5368 // modify bp->data[]
5319
                                                                               5369 // log_write(bp)
5320 if(do commit){
                                                                               5370 // brelse(bp)
5321
       // call commit w/o holding locks, since not allowed
                                                                               5371 void
5322
        // to sleep with locks.
                                                                               5372 log write(struct buf *b)
5323
        commit();
                                                                               5373 {
5324
        acquire(&log.lock);
                                                                               5374 int i;
5325
        log.committing = 0;
                                                                               5375
5326
                                                                               5376 if (\log. \ln n) = LOGSIZE \mid \log. \ln n > = \log. size - 1)
        wakeup(&log);
5327
        release(&log.lock);
                                                                               5377
                                                                                        panic("too big a transaction");
5328 }
                                                                               5378 if (log.outstanding < 1)
5329 }
                                                                                        panic("log_write outside of trans");
                                                                               5379
5330
                                                                               5380
5331 // Copy modified blocks from cache to log.
                                                                               5381 acquire(&log.lock);
5332 static void
                                                                               5382 for (i = 0; i < log.lh.n; i++) {
5333 write_log(void)
                                                                               5383
                                                                                        if (log.lh.block[i] == b->blockno) // log absorbtion
5334 {
                                                                               5384
5335 int tail;
                                                                               5385 }
                                                                               5386 log.lh.block[i] = b->blockno;
5336
5337 for (tail = 0; tail < log.lh.n; tail++) {
                                                                               5387 if (i == loq.lh.n)
5338
       struct buf *to = bread(log.dev, log.start+tail+1); // log block
                                                                               5388
                                                                                      log.lh.n++;
5339
        struct buf *from = bread(log.dev, log.lh.block[tail]); // cache block
                                                                               5389
                                                                                    b->flags |= B_DIRTY; // prevent eviction
5340
        memmove(to->data, from->data, BSIZE);
                                                                               5390
                                                                                     release(&log.lock);
        bwrite(to); // write the log
                                                                               5391 }
5341
5342
        brelse(from);
                                                                               5392
5343
        brelse(to);
                                                                               5393
5344 }
                                                                               5394
5345 }
                                                                               5395
5346
                                                                               5396
5347
                                                                               5397
5348
                                                                               5398
5349
                                                                               5399
```

Sheet 53 Sheet 53

```
5400 // File system implementation. Five layers:
                                                                                5450 // Blocks.
5401 // + Blocks: allocator for raw disk blocks.
                                                                                5451
5402 // + Log: crash recovery for multi-step updates.
                                                                                5452 // Allocate a zeroed disk block.
5403 // + Files: inode allocator, reading, writing, metadata.
                                                                                5453 static uint
5404 // + Directories: inode with special contents (list of other inodes!)
                                                                                5454 balloc(uint dev)
5405 // + Names: paths like /usr/rtm/xv6/fs.c for convenient naming.
                                                                                5455 {
5406 //
                                                                                5456 int b, bi, m;
5407 // This file contains the low-level file system manipulation
                                                                                5457 struct buf *bp;
5408 // routines. The (higher-level) system call implementations
                                                                                5458
5409 // are in sysfile.c.
                                                                                5459 bp = 0;
5410
                                                                                5460
                                                                                      for(b = 0; b < sb.size; b += BPB){
5411 #include "types.h"
                                                                                        bp = bread(dev, BBLOCK(b, sb));
                                                                                5461
5412 #include "defs.h"
                                                                                5462
                                                                                         for(bi = 0; bi < BPB && b + bi < sb.size; bi++){
5413 #include "param.h"
                                                                                5463
                                                                                           m = 1 << (bi % 8);
5414 #include "stat.h"
                                                                                5464
                                                                                           if((bp->data[bi/8] \& m) == 0){ // Is block free?}
5415 #include "mmu.h"
                                                                                5465
                                                                                             bp->data[bi/8] |= m; // Mark block in use.
5416 #include "proc.h"
                                                                                5466
                                                                                             log write(bp);
5417 #include "spinlock.h"
                                                                                5467
                                                                                             brelse(bp);
5418 #include "fs.h"
                                                                                5468
                                                                                             bzero(dev, b + bi);
5419 #include "buf.h"
                                                                                5469
                                                                                             return b + bi;
5420 #include "file.h"
                                                                                5470
5421
                                                                                5471
5422 #define min(a, b) ((a) < (b) ? (a) : (b))
                                                                                5472
                                                                                         brelse(bp);
5423 static void itrunc(struct inode*);
                                                                                5473
5424 struct superblock sb; // there should be one per dev, but we run with one (5474 panic("balloc: out of blocks");
                                                                                5475 }
5426 // Read the super block.
                                                                                5476
5427 void
                                                                                5477 // Free a disk block.
5428 readsb(int dev, struct superblock *sb)
                                                                                5478 static void
                                                                                5479 bfree(int dev, uint b)
5429 {
5430 struct buf *bp;
                                                                                5480 {
5431
                                                                                5481 struct buf *bp;
5432 bp = bread(dev, 1);
                                                                                5482 int bi, m;
5433 memmove(sb, bp->data, sizeof(*sb));
                                                                                5483
5434 brelse(bp);
                                                                                5484 readsb(dev, &sb);
5435 }
                                                                                5485 bp = bread(dev, BBLOCK(b, sb));
5436
                                                                                5486 bi = b % BPB;
                                                                                5487 \quad m = 1 \ll (bi \% 8);
5437 // Zero a block.
5438 static void
                                                                                5488 if((bp->data[bi/8] & m) == 0)
5439 bzero(int dev, int bno)
                                                                                5489
                                                                                         panic("freeing free block");
5440 {
                                                                                5490 bp->data[bi/8] &= ~m;
5441 struct buf *bp;
                                                                                5491 log_write(bp);
5442
                                                                                5492 brelse(bp);
5443 bp = bread(dev, bno);
                                                                                5493 }
5444 memset(bp->data, 0, BSIZE);
                                                                                5494
5445 log_write(bp);
                                                                                5495
5446 brelse(bp);
                                                                                5496
                                                                                5497
5447 }
5448
                                                                                5498
5449
                                                                                5499
```

Sheet 54 Sheet 54

5500 // Inodes.	5550 //
5501 //	5550 // 5551 // ilock() is separate from iget() so that system calls can
5502 // An inode describes a single unnamed file.	5552 // get a long-term reference to an inode (as for an open file)
5503 // The inode disk structure holds metadata: the file's type,	5553 // and only lock it for short periods (e.g., in read()).
5504 // its size, the number of links referring to it, and the	5554 // The separation also helps avoid deadlock and races during
5505 // list of blocks holding the file's content.	5555 // pathname lookup. iget() increments ip->ref so that the inode
5506 //	5556 // stays cached and pointers to it remain valid.
5507 // The inodes are laid out sequentially on disk at	5557 //
5508 // sb.startinode. Each inode has a number, indicating its	5558 // Many internal file system functions expect the caller to
5509 // position on the disk.	5559 // have locked the inodes involved; this lets callers create
5510 //	5560 // multi-step atomic operations.
5511 // The kernel keeps a cache of in-use inodes in memory	5561
5512 // to provide a place for synchronizing access	5562 struct {
5513 // to inodes used by multiple processes. The cached	5563 struct spinlock lock;
5514 // inodes include book-keeping information that is	5564 struct inode inode[NINODE];
5515 // not stored on disk: ip->ref and ip->flags.	5565 } icache;
5516 //	5566
5517 // An inode and its in-memory represtative go through a	5567 void
5518 // sequence of states before they can be used by the	5568 iinit(int dev)
5519 // rest of the file system code.	5569 {
5520 //	5570 initlock(&icache.lock, "icache");
5521 // * Allocation: an inode is allocated if its type (on disk)	5571 readsb(dev, &sb);
5522 // is non-zero. ialloc() allocates, iput() frees if	5572 cprintf("sb: size %d nblocks %d ninodes %d nlog %d logstart %d inodestart
5523 // the link count has fallen to zero.	5573 sb.nblocks, sb.ninodes, sb.nloq, sb.logstart, sb.inodestart, sb.bma
5524 //	5574 }
5525 // * Referencing in cache: an entry in the inode cache	5575
5526 // is free if ip->ref is zero. Otherwise ip->ref tracks	5576 static struct inode* iget(uint dev, uint inum);
5527 // the number of in-memory pointers to the entry (open	5577
5528 // files and current directories). iget() to find or	5578
5529 // create a cache entry and increment its ref, iput()	5579
5530 // to decrement ref.	5580
5531 //	5581
5532 // * Valid: the information (type, size, &c) in an inode	5582
5533 // cache entry is only correct when the I_VALID bit	5583
5534 // is set in ip->flags. ilock() reads the inode from	5584
5535 // the disk and sets I_VALID, while iput() clears	5585
5536 // I_VALID if ip->ref has fallen to zero.	5586
5537 //	5587
5538 // * Locked: file system code may only examine and modify	5588
5539 // the information in an inode and its content if it	5589
5540 // has first locked the inode. The I_BUSY flag indicates	5590
5541 // that the inode is locked. ilock() sets I_BUSY,	5591
5542 // while iunlock clears it.	5592
5543 //	5593
5544 // Thus a typical sequence is:	5594
5545 // ip = iget(dev, inum)	5595
5546 // ilock(ip)	5596
5547 // examine and modify ip->xxx	5597
5548 // iunlock(ip)	5598
5549 // iput(ip)	5599

Sheet 55 Sheet 55

```
5650 // Find the inode with number inum on device dev
5600 // Allocate a new inode with the given type on device dev.
5601 // A free inode has a type of zero.
5602 struct inode*
5603 ialloc(uint dev, short type)
5604 {
5605 int inum;
                                                                              5655 {
5606 struct buf *bp;
5607 struct dinode *dip;
                                                                              5657
5608
5609 for(inum = 1; inum < sb.ninodes; inum++){</pre>
                                                                              5659
5610
       bp = bread(dev, IBLOCK(inum, sb));
        dip = (struct dinode*)bp->data + inum%IPB;
5611
5612
       if(dip->type == 0){ // a free inode
5613
        memset(dip, 0, sizeof(*dip));
                                                                              5663
5614
          dip->type = type;
                                                                              5664
5615
         log_write(bp); // mark it allocated on the disk
                                                                               5665
5616
          brelse(bp);
                                                                               5666
5617
          return iget(dev, inum);
                                                                              5667
5618
5619
       brelse(bp);
                                                                               5669
5620 }
5621 panic("ialloc: no inodes");
                                                                              5671
5622 }
5623
5624 // Copy a modified in-memory inode to disk.
                                                                              5674
                                                                              5675
5625 void
5626 iupdate(struct inode *ip)
5627 {
5628 struct buf *bp;
5629 struct dinode *dip;
5630
5631 bp = bread(ip->dev, IBLOCK(ip->inum, sb));
5632 dip = (struct dinode*)bp->data + ip->inum%IPB;
                                                                              5682
5633 dip->type = ip->type;
5634 dip->major = ip->major;
                                                                              5684 }
5635 dip->minor = ip->minor;
                                                                              5685
5636 dip->nlink = ip->nlink;
5637 dip->size = ip->size;
5638 memmove(dip->addrs, ip->addrs, sizeof(ip->addrs));
5639 log_write(bp);
5640 brelse(bp);
                                                                               5690 {
5641 }
5642
5643
5644
5645
                                                                              5695 }
5646
                                                                               5696
5647
                                                                              5697
5648
                                                                               5698
5649
                                                                              5699
```

```
5651 // and return the in-memory copy. Does not lock
5652 // the inode and does not read it from disk.
5653 static struct inode*
5654 iget(uint dev, uint inum)
5656 struct inode *ip, *empty;
5658 acquire(&icache.lock);
5660 // Is the inode already cached?
5661 empty = 0;
5662 for(ip = &icache.inode[0]; ip < &icache.inode[NINODE]; ip++){</pre>
       if(ip->ref > 0 && ip->dev == dev && ip->inum == inum){
          ip->ref++;
          release(&icache.lock);
          return ip;
if (empty == 0 \&\& ip > ref == 0) // Remember empty slot.
          empty = ip;
5670 }
5672 // Recycle an inode cache entry.
if(empty == 0)
        panic("iget: no inodes");
5676 ip = empty;
5677 ip->dev = dev;
5678 ip->inum = inum;
5679 	 ip->ref = 1;
5680 ip->flags = 0;
5681 release(&icache.lock);
5683 return ip;
5686 // Increment reference count for ip.
5687 // Returns ip to enable ip = idup(ip1) idiom.
5688 struct inode*
5689 idup(struct inode *ip)
5691 acquire(&icache.lock);
5692 ip->ref++;
5693 release(&icache.lock);
5694 return ip;
```

Sheet 56 Sheet 56

```
5750 // Drop a reference to an in-memory inode.
5751 // If that was the last reference, the inode cache entry can
5752 // be recycled.
5753 // If that was the last reference and the inode has no links
5754 // to it, free the inode (and its content) on disk.
5755 // All calls to iput() must be inside a transaction in
5756 // case it has to free the inode.
5757 void
5758 iput(struct inode *ip)
5759 {
5760 acquire(&icache.lock);
5761 if(ip->ref == 1 && (ip->flags & I_VALID) && ip->nlink == 0){
5762
        // inode has no links and no other references: truncate and free.
5763
        if(ip->flags & I BUSY)
5764
          panic("iput busy");
5765
        ip->flags |= I_BUSY;
5766
        release(&icache.lock);
5767
        itrunc(ip);
5768
        ip->type = 0;
5769
        iupdate(ip);
5770
        acquire(&icache.lock);
5771
        ip->flags = 0;
5772
        wakeup(ip);
5773
5774 ip->ref--;
5775 release(&icache.lock);
5776 }
5777
5778 // Common idiom: unlock, then put.
5779 void
5780 iunlockput(struct inode *ip)
5781 {
5782 iunlock(ip);
5783 iput(ip);
5784 }
5785
5786
5787
5788
5789
5790
5791
5792
5793
5794
5795
5796
5797
5798
5799
```

5748

5749

```
5800 // Inode content
5801 //
5802 // The content (data) associated with each inode is stored
5803 // in blocks on the disk. The first NDIRECT block numbers
5804 // are listed in ip->addrs[]. The next NINDIRECT blocks are
5805 // listed in block ip->addrs[NDIRECT].
5807 // Return the disk block address of the nth block in inode ip.
5808 // If there is no such block, bmap allocates one.
5809 static uint
5810 bmap(struct inode *ip, uint bn)
5811 {
5812 uint addr, *a;
5813 struct buf *bp;
5814
5815 if(bn < NDIRECT){
5816
       if((addr = ip->addrs[bn]) == 0)
5817
          ip->addrs[bn] = addr = balloc(ip->dev);
5818
        return addr;
5819
5820 bn -= NDIRECT;
5821
5822 if(bn < NINDIRECT){
5823
       // Load indirect block, allocating if necessary.
5824
        if((addr = ip->addrs[NDIRECT]) == 0)
5825
          ip->addrs[NDIRECT] = addr = balloc(ip->dev);
5826
        bp = bread(ip->dev, addr);
5827
        a = (uint*)bp->data;
5828
        if((addr = a[bn]) == 0)
5829
          a[bn] = addr = balloc(ip->dev);
5830
          log_write(bp);
5831
5832
        brelse(bp);
5833
        return addr;
5834 }
5835
5836 panic("bmap: out of range");
5837 }
5838
5839
5840
5841
5842
5843
5844
5845
5846
5847
5848
5849
```

```
5850 // Truncate inode (discard contents).
5851 // Only called when the inode has no links
5852 // to it (no directory entries referring to it)
5853 // and has no in-memory reference to it (is
5854 // not an open file or current directory).
5855 static void
5856 itrunc(struct inode *ip)
5857 {
5858 int i, j;
5859 struct buf *bp;
5860 uint *a;
5861
5862 for(i = 0; i < NDIRECT; i++){
5863
       if(ip->addrs[i]){
5864
          bfree(ip->dev, ip->addrs[i]);
5865
          ip->addrs[i] = 0;
5866
5867
      }
5868
5869 if(ip->addrs[NDIRECT]){
5870
        bp = bread(ip->dev, ip->addrs[NDIRECT]);
5871
        a = (uint*)bp->data;
5872
        for(j = 0; j < NINDIRECT; j++)
5873
          if(a[j])
5874
            bfree(ip->dev, a[j]);
5875
5876
        brelse(bp);
5877
        bfree(ip->dev, ip->addrs[NDIRECT]);
5878
        ip->addrs[NDIRECT] = 0;
5879 }
5880
5881 ip->size = 0;
5882 iupdate(ip);
5883 }
5884
5885 // Copy stat information from inode.
5886 void
5887 stati(struct inode *ip, struct stat *st)
5888 {
5889 st->dev = ip->dev;
5890 st->ino = ip->inum;
5891 st->type = ip->type;
5892 st->nlink = ip->nlink;
5893 st->size = ip->size;
5894 }
5895
5896
5897
5898
5899
```

5994

5995

5996

5997

5998

5999

Sheet 59 Sheet 59

5944

5945

5946

5947

5948

5949

May 16 22:03 2016 xv6/fs.c Page 14

Sheet 60 Sheet 60

May 16 22:03 2016 xv6/fs.c Page 13

```
6100 // Paths
                                                                               6150 // Look up and return the inode for a path name.
6101
                                                                               6151 // If parent != 0, return the inode for the parent and copy the final
6102 // Copy the next path element from path into name.
                                                                               6152 // path element into name, which must have room for DIRSIZ bytes.
                                                                               6153 // Must be called inside a transaction since it calls iput().
6103 // Return a pointer to the element following the copied one.
6104 // The returned path has no leading slashes,
                                                                               6154 static struct inode*
6105 // so the caller can check *path=='\0' to see if the name is the last one. 6155 namex(char *path, int nameiparent, char *name)
6106 // If no name to remove, return 0.
                                                                               6156 {
                                                                               6157 struct inode *ip, *next;
6107 //
6108 // Examples:
                                                                               6158
6109 // skipelem("a/bb/c", name) = "bb/c", setting name = "a"
                                                                               6159 if(*path == '/')
6110 // skipelem("//a//bb", name) = "bb", setting name = "a"
                                                                               6160
                                                                                        ip = iget(ROOTDEV, ROOTINO);
6111 // skipelem("a", name) = "", setting name = "a"
                                                                               6161
6112 // skipelem("", name) = skipelem("///", name) = 0
                                                                               6162
                                                                                        ip = idup(proc->cwd);
6113 //
                                                                               6163
6114 static char*
                                                                               6164 while((path = skipelem(path, name)) != 0){
6115 skipelem(char *path, char *name)
                                                                               6165
                                                                                        ilock(ip);
6116 {
                                                                               6166
                                                                                        if(ip->type != T_DIR){
6117 char *s;
                                                                               6167
                                                                                          iunlockput(ip);
6118 int len;
                                                                               6168
                                                                                          return 0;
6119
                                                                               6169
6120 while(*path == '/')
                                                                               6170
                                                                                        if(nameiparent && *path == '\0'){
6121
        path++;
                                                                               6171
                                                                                          // Stop one level early.
6122 if(*path == 0)
                                                                               6172
                                                                                          iunlock(ip);
6123
      return 0;
                                                                               6173
                                                                                          return ip;
6124 s = path;
                                                                               6174
6125 while(*path != '/' && *path != 0)
                                                                               6175
                                                                                        if((next = dirlookup(ip, name, 0)) == 0){
                                                                               6176
6126
      path++;
                                                                                          iunlockput(ip);
6127 len = path - s;
                                                                               6177
                                                                                          return 0;
6128 if(len >= DIRSIZ)
                                                                               6178
      memmove(name, s, DIRSIZ);
                                                                               6179
6129
                                                                                        iunlockput(ip);
6130 else {
                                                                               6180
                                                                                        ip = next;
6131
        memmove(name, s, len);
                                                                               6181
6132
        name[len] = 0;
                                                                               6182 if(nameiparent){
6133 }
                                                                               6183
                                                                                        iput(ip);
6134 while(*path == '/')
                                                                               6184
                                                                                        return 0;
6135
      path++;
                                                                               6185 }
6136 return path;
                                                                               6186 return ip;
6137 }
                                                                               6187 }
6138
                                                                               6188
6139
                                                                               6189 struct inode*
6140
                                                                               6190 namei(char *path)
6141
                                                                               6191 {
6142
                                                                               6192 char name[DIRSIZ];
6143
                                                                               6193 return namex(path, 0, name);
6144
                                                                               6194 }
6145
                                                                               6195
6146
                                                                               6196
6147
                                                                               6197
6148
                                                                               6198
6149
                                                                               6199
```

Sheet 61 Sheet 61

```
6200 struct inode*
6201 nameiparent(char *path, char *name)
6202 {
6203 return namex(path, 1, name);
6204 }
6205
6206
6207
6208
6209
6210
6211
6212
6213
6214
6215
6216
6217
6218
6219
6220
6221
6222
6223
6224
6225
6226
6227
6228
6229
6230
6231
6232
6233
6234
6235
6236
6237
6238
6239
6240
6241
6242
6243
6244
6245
6246
6247
6248
6249
```

```
6250 //
6251 // File descriptors
6252 //
6253
6254 #include "types.h"
6255 #include "defs.h"
6256 #include "param.h"
6257 #include "fs.h"
6258 #include "file.h"
6259 #include "spinlock.h"
6260
6261 struct devsw devsw[NDEV];
6262 struct {
6263 struct spinlock lock;
6264 struct file file[NFILE];
6265 } ftable;
6266
6267 void
6268 fileinit(void)
6269 {
6270 initlock(&ftable.lock, "ftable");
6271 }
6272
6273 // Allocate a file structure.
6274 struct file*
6275 filealloc(void)
6276 {
6277 struct file *f;
6278
6279 acquire(&ftable.lock);
6280 for(f = ftable.file; f < ftable.file + NFILE; f++){
6281 if(f->ref == 0)
6282
          f->ref = 1;
          release(&ftable.lock);
6283
6284
          return f;
6285
6286 }
6287 release(&ftable.lock);
6288 return 0;
6289 }
6290
6291
6292
6293
6294
6295
6296
6297
6298
6299
```

May 16 22:03 2016 xv6/file.c Page 3

Sheet 63 Sheet 63

May 16 22:03 2016 xv6/file.c Page 2

6448

6449

```
6450 //
6451 // File-system system calls.
6452 // Mostly argument checking, since we don't trust
6453 // user code, and calls into file.c and fs.c.
6454 //
6455
6456 #include "types.h"
6457 #include "defs.h"
6458 #include "param.h"
6459 #include "stat.h"
6460 #include "mmu.h"
6461 #include "proc.h"
6462 #include "fs.h"
6463 #include "file.h"
6464 #include "fcntl.h"
6465
6466 // Fetch the nth word-sized system call argument as a file descriptor
6467 // and return both the descriptor and the corresponding struct file.
6468 static int
6469 argfd(int n, int *pfd, struct file **pf)
6470 {
6471 int fd;
6472 struct file *f;
6473
6474 if(argint(n, &fd) < 0)
6475
       return -1;
6476 if(fd < 0 | | fd >= NOFILE | | (f=proc->ofile[fd]) == 0)
6477
       return -1;
6478 if(pfd)
6479
       *pfd = fd;
6480 if(pf)
6481
       *pf = f;
6482 return 0;
6483 }
6484
6485 // Allocate a file descriptor for the given file.
6486 // Takes over file reference from caller on success.
6487 static int
6488 fdalloc(struct file *f)
6489 {
6490 int fd;
6491
6492 for(fd = 0; fd < NOFILE; fd++){
if (proc - sofile[fd] == 0)
6494
       proc->ofile[fd] = f;
6495
          return fd;
6496
6497 }
6498 return -1;
6499 }
```

Sheet 64 Sheet 64

```
6500 int
                                                                               6550 int
6501 sys_dup(void)
                                                                               6551 sys_fstat(void)
6502 {
                                                                               6552 {
6503 struct file *f;
                                                                               6553 struct file *f;
6504 int fd;
                                                                               6554 struct stat *st;
6505
                                                                               6555
6506 if(argfd(0, 0, &f) < 0)
                                                                               6556 if(arqfd(0, 0, &f) < 0 | arqptr(1, (void*)&st, sizeof(*st)) < 0)
      return -1;
                                                                                      return -1;
6507
                                                                               6557
6508 if((fd=fdalloc(f)) < 0)
                                                                               6558 return filestat(f, st);
6509
      return -1;
                                                                               6559 }
6510 filedup(f);
                                                                               6560
6511 return fd;
                                                                               6561 // Create the path new as a link to the same inode as old.
6512 }
                                                                               6562 int
6513
                                                                               6563 sys_link(void)
6514 int
                                                                               6564 {
6515 sys_read(void)
                                                                               6565 char name[DIRSIZ], *new, *old;
6516 {
                                                                               6566 struct inode *dp, *ip;
6517 struct file *f;
                                                                               6567
6518 int n;
                                                                               6568 if(argstr(0, &old) < 0 | argstr(1, &new) < 0)
6519 char *p;
                                                                               6569
                                                                                      return -1;
6520
                                                                               6570
6521 if (arqfd(0, 0, \&f) < 0 \mid arqint(2, \&n) < 0 \mid arqptr(1, \&p, n) < 0)
                                                                               6571 begin_op();
6522
      return -1;
                                                                               6572 if((ip = namei(old)) == 0)
6523 return fileread(f, p, n);
                                                                               6573
                                                                                       end_op();
6524 }
                                                                               6574
                                                                                     return -1;
6525
                                                                               6575 }
6526 int
                                                                               6576
6527 sys_write(void)
                                                                               6577 ilock(ip);
6528 {
                                                                               6578 if(ip->type == T_DIR){
6529 struct file *f;
                                                                               6579
                                                                                     iunlockput(ip);
6530 int n;
                                                                               6580 end_op();
6531 char *p;
                                                                               6581
                                                                                       return -1;
                                                                               6582 }
6532
6533 if (argfd(0, 0, &f) < 0 \mid | argint(2, &n) < 0 \mid | argptr(1, &p, n) < 0)
                                                                               6583
      return -1;
                                                                               6584 ip->nlink++;
6534
6535 return filewrite(f, p, n);
                                                                               6585 iupdate(ip);
6536 }
                                                                               6586 iunlock(ip);
6537
                                                                               6587
6538 int
                                                                               6588 if((dp = nameiparent(new, name)) == 0)
6539 sys_close(void)
                                                                               6589
                                                                                       goto bad;
6540 {
                                                                               6590 ilock(dp);
                                                                               if (dp\rightarrow dev != ip\rightarrow dev || dirlink(dp, name, ip\rightarrow inum) < 0)
6541 int fd;
6542 struct file *f;
                                                                               6592
                                                                                       iunlockput(dp);
6543
                                                                               6593
                                                                                       goto bad;
6544 if (argfd(0, \&fd, \&f) < 0)
                                                                               6594
6545
      return -1;
                                                                               6595 iunlockput(dp);
6546 proc->ofile[fd] = 0;
                                                                               6596 iput(ip);
6547 fileclose(f);
                                                                               6597
6548 return 0;
                                                                               6598 end_op();
                                                                               6599
6549 }
```

Sheet 65 Sheet 65

Sheet 66 Sheet 66

```
6700 bad:
                                                                            6750 int
6701 iunlockput(dp);
                                                                            6751 sys_open(void)
6702 end op();
                                                                            6752 {
6703 return -1;
                                                                            6753 char *path;
6704 }
                                                                            6754 int fd, omode;
6705
                                                                            6755 struct file *f;
6706 static struct inode*
                                                                            6756 struct inode *ip;
6707 create(char *path, short type, short major, short minor)
                                                                            6757
6708 {
                                                                            6758 if(argstr(0, &path) < 0 | argint(1, &omode) < 0)
6709 uint off;
                                                                            6759
                                                                                   return -1;
6710 struct inode *ip, *dp;
                                                                            6760
6711 char name[DIRSIZ];
                                                                            6761 begin_op();
6712
                                                                            6762
6713 if((dp = nameiparent(path, name)) == 0)
                                                                            6763 if(omode & O_CREATE){
6714
      return 0;
                                                                            6764 ip = create(path, T_FILE, 0, 0);
6715 ilock(dp);
                                                                            6765 if(ip == 0)
6716
                                                                            6766
                                                                                      end_op();
6717 if((ip = dirlookup(dp, name, &off)) != 0){
                                                                            6767
                                                                                      return -1;
6718
      iunlockput(dp);
                                                                            6768
6719
       ilock(ip);
                                                                            6769 } else {
       if(type == T_FILE && ip->type == T_FILE)
6720
                                                                            6770
                                                                                  if((ip = namei(path)) == 0){
6721
         return ip;
                                                                            6771
                                                                                      end op();
        iunlockput(ip);
6722
                                                                            6772
                                                                                     return -1;
6723
       return 0;
                                                                            6773
6724 }
                                                                            6774
                                                                                   ilock(ip);
6725
                                                                            6775 if(ip->type == T_DIR && omode != O_RDONLY){
if((ip = ialloc(dp->dev, type)) == 0)
                                                                                      iunlockput(ip);
                                                                            6776
6727
       panic("create: ialloc");
                                                                            6777
                                                                                      end_op();
6728
                                                                            6778
                                                                                      return -1;
6729 ilock(ip);
                                                                            6779
                                                                            6780 }
6730 ip->major = major;
6731 ip->minor = minor;
                                                                            6781
6732 ip->nlink = 1;
                                                                            6782 if((f = filealloc()) == 0 \mid | (fd = fdalloc(f)) < 0) 
6733 iupdate(ip);
                                                                            6783
                                                                                   if(f)
                                                                            6784
                                                                                      fileclose(f);
6734
6735 if(type == T_DIR){ // Create . and .. entries.
                                                                            6785
                                                                                    iunlockput(ip);
6736
      dp->nlink++; // for ".."
                                                                            6786
                                                                                    end_op();
6737
       iupdate(dp);
                                                                            6787
                                                                                    return -1;
6738
        // No ip->nlink++ for ".": avoid cyclic ref count.
                                                                            6788
                                                                            6789 iunlock(ip);
6739
        if(dirlink(ip, ".", ip->inum) < 0 | | dirlink(ip, "..", dp->inum) < 0)
6740
          panic("create dots");
                                                                            6790 end op();
6741 }
                                                                            6791
6742
                                                                            6792 f->type = FD INODE;
6743 if(dirlink(dp, name, ip->inum) < 0)
                                                                            6793 	 f->ip = ip;
        panic("create: dirlink");
                                                                            6794 f -> off = 0;
6744
                                                                            6795 f->readable = !(omode & O WRONLY);
6745
6746 iunlockput(dp);
                                                                            6796 f->writable = (omode & O WRONLY) | (omode & O RDWR);
6747
                                                                            6797 return fd;
6748 return ip;
                                                                            6798 }
                                                                            6799
6749 }
```

Sheet 67 Sheet 67

```
6800 int
                                                                              6850 int
6801 sys_mkdir(void)
                                                                              6851 sys_chdir(void)
6802 {
                                                                             6852 {
6803 char *path;
                                                                              6853 char *path;
6804 struct inode *ip;
                                                                             6854 struct inode *ip;
6805
                                                                              6855
6806 begin_op();
                                                                             6856 begin_op();
6807 if(argstr(0, &path) < 0 | | (ip = create(path, T_DIR, 0, 0)) == 0){
                                                                             6857 if(argstr(0, &path) < 0 | (ip = namei(path)) == 0){
6808
      end op();
                                                                              6858
                                                                                      end op();
6809
      return -1;
                                                                             6859
                                                                                      return -1;
6810 }
                                                                             6860
6811 iunlockput(ip);
                                                                              6861 ilock(ip);
6812 end_op();
                                                                              6862 if(ip->type != T_DIR){
6813 return 0;
                                                                             6863
                                                                                    iunlockput(ip);
6814 }
                                                                             6864
                                                                                     end op();
6815
                                                                             6865 return -1;
                                                                             6866 }
6816 int
6817 sys_mknod(void)
                                                                             6867 iunlock(ip);
                                                                             6868 iput(proc->cwd);
6818 {
6819 struct inode *ip;
                                                                             6869 end_op();
6820 char *path;
                                                                             6870 proc->cwd = ip;
6821 int len;
                                                                             6871 return 0;
6822 int major, minor;
                                                                             6872 }
6823
                                                                             6873
6824 begin_op();
                                                                             6874 int
6825 if((len=argstr(0, &path)) < 0 |
                                                                             6875 sys_exec(void)
6826
      argint(1, \&major) < 0 \mid \mid
                                                                             6876 {
6827
         argint(2, &minor) < 0 ||
                                                                             6877 char *path, *argv[MAXARG];
6828
       (ip = create(path, T_DEV, major, minor)) == 0){
                                                                             6878 int i;
6829
        end_op();
                                                                             6879 uint uargy, uarg;
6830
        return -1;
                                                                             6880
6831 }
                                                                             6881 if(argstr(0, &path) < 0 | argint(1, (int*)&uargv) < 0){
6832 iunlockput(ip);
                                                                             6882
                                                                                      return -1;
6833 end_op();
                                                                             6883 }
                                                                             6884 memset(argv, 0, sizeof(argv));
6834 return 0;
6835 }
                                                                             6885 for(i=0;; i++){
6836
                                                                             6886
                                                                                    if(i >= NELEM(argv))
6837
                                                                             6887
                                                                                       return -1;
6838
                                                                             6888
                                                                                     if(fetchint(uargv+4*i, (int*)&uarg) < 0)</pre>
6839
                                                                             6889
                                                                                       return -1;
6840
                                                                             6890
                                                                                      if(uarg == 0){
6841
                                                                             6891
                                                                                        argv[i] = 0;
6842
                                                                             6892
                                                                                        break;
6843
                                                                             6893
6844
                                                                             6894
                                                                                      if(fetchstr(uarg, &argv[i]) < 0)</pre>
6845
                                                                             6895
                                                                                        return -1;
6846
                                                                             6896 }
6847
                                                                             6897 return exec(path, argv);
6848
                                                                              6898 }
                                                                             6899
6849
```

```
6900 int
6901 sys_pipe(void)
6902 {
6903 int *fd;
6904 struct file *rf, *wf;
6905 int fd0, fd1;
6906
6907 if(argptr(0, (void*)&fd, 2*sizeof(fd[0])) < 0)
6908
      return -1;
6909 if(pipealloc(&rf, &wf) < 0)
6910
      return -1;
6911 fd0 = -1;
6912 if((fd0 = fdalloc(rf)) < 0 \mid | (fd1 = fdalloc(wf)) < 0)
6913
       if(fd0 >= 0)
6914
          proc->ofile[fd0] = 0;
6915
        fileclose(rf);
6916
        fileclose(wf);
6917
       return -1;
6918 }
6919 \quad fd[0] = fd0;
6920 fd[1] = fd1;
6921 return 0;
6922 }
6923
6924
6925
6926
6927
6928
6929
6930
6931
6932
6933
6934
6935
6936
6937
6938
6939
6940
6941
6942
6943
6944
6945
6946
6947
6948
6949
```

```
6950 #include "types.h"
6951 #include "param.h"
6952 #include "memlayout.h"
6953 #include "mmu.h"
6954 #include "proc.h"
6955 #include "defs.h"
6956 #include "x86.h"
6957 #include "elf.h"
6958
6959 int
6960 exec(char *path, char **argv)
6962 char *s, *last;
6963 int i, off;
6964 uint argc, sz, sp, ustack[3+MAXARG+1];
6965 struct elfhdr elf;
6966 struct inode *ip;
6967 struct proghdr ph;
6968 pde_t *pgdir, *oldpgdir;
6969
6970 begin op();
6971 if((ip = namei(path)) == 0)
6972
        end op();
6973
        return -1;
6974
6975 ilock(ip);
6976 pgdir = 0;
6977
6978 // Check ELF header
6979 if(readi(ip, (char*)&elf, 0, sizeof(elf)) < sizeof(elf))
       goto bad;
6981 if(elf.magic != ELF_MAGIC)
6982
        goto bad;
6983
6984 if((pgdir = setupkvm()) == 0)
6985
        goto bad;
6986
6987 // Load program into memory.
6988 	 sz = 0;
6989 for(i=0, off=elf.phoff; i<elf.phnum; i++, off+=sizeof(ph)){
6990
        if(readi(ip, (char*)&ph, off, sizeof(ph)) != sizeof(ph))
6991
          goto bad;
6992
         if(ph.type != ELF_PROG_LOAD)
6993
          continue;
6994
        if(ph.memsz < ph.filesz)</pre>
6995
          goto bad;
6996
         if((sz = allocuvm(pqdir, sz, ph.vaddr + ph.memsz)) == 0)
6997
6998
         if(loaduvm(pgdir, (char*)ph.vaddr, ip, ph.off, ph.filesz) < 0)</pre>
6999
          goto bad;
```

Sheet 69 Sheet 69

May 16 22:03 2016 xv6/exec.c Page 3

May 16 22:03 2016 xv6/exec.c Page 2

Sheet 70 Sheet 70

```
7100 #include "types.h"
                                                                              7150 bad:
7101 #include "defs.h"
                                                                              7151 if(p)
7102 #include "param.h"
                                                                              7152
                                                                                      kfree((char*)p);
                                                                             7153 if(*f0)
7103 #include "mmu.h"
7104 #include "proc.h"
                                                                              7154
                                                                                     fileclose(*f0);
7105 #include "fs.h"
                                                                              7155 if(*f1)
7106 #include "file.h"
                                                                              7156
                                                                                    fileclose(*f1);
7107 #include "spinlock.h"
                                                                              7157 return -1;
                                                                              7158 }
7108
7109 #define PIPESIZE 512
                                                                              7159
7110
                                                                              7160 void
7111 struct pipe {
                                                                              7161 pipeclose(struct pipe *p, int writable)
7112 struct spinlock lock;
                                                                              7162 {
7113 char data[PIPESIZE];
                                                                              7163 acquire(&p->lock);
7114 uint nread;
                     // number of bytes read
                                                                              7164 if(writable){
7115 uint nwrite; // number of bytes written
                                                                              7165
                                                                                      p->writeopen = 0;
7116 int readopen; // read fd is still open
                                                                              7166
                                                                                      wakeup(&p->nread);
7117 int writeopen; // write fd is still open
                                                                              7167 } else {
7118 };
                                                                              7168
                                                                                     p->readopen = 0;
7119
                                                                              7169
                                                                                      wakeup(&p->nwrite);
7120 int
                                                                              7170 }
7121 pipealloc(struct file **f0, struct file **f1)
                                                                              7171 if(p->readopen == 0 && p->writeopen == 0){
7122 {
                                                                              7172
                                                                                      release(&p->lock);
7123 struct pipe *p;
                                                                              7173
                                                                                      kfree((char*)p);
7124
                                                                              7174 } else
7125 p = 0;
                                                                              7175
                                                                                      release(&p->lock);
7126 *f0 = *f1 = 0;
                                                                              7176 }
7127 if((*f0 = filealloc()) == 0 | (*f1 = filealloc()) == 0)
                                                                              7177
7128
      goto bad;
                                                                              7178
7129 if((p = (struct pipe*)kalloc()) == 0)
                                                                              7179 int
7130 goto bad;
                                                                              7180 pipewrite(struct pipe *p, char *addr, int n)
7131 p->readopen = 1;
                                                                              7181 {
7132 p->writeopen = 1;
                                                                              7182 int i;
7133 p->nwrite = 0;
                                                                              7183
7134 p->nread = 0;
                                                                              7184 acquire(&p->lock);
7135 initlock(&p->lock, "pipe");
                                                                              7185 for(i = 0; i < n; i++){
7136 (*f0)->type = FD_PIPE;
                                                                              7186
                                                                                      while(p->nwrite == p->nread + PIPESIZE){
7137 (*f0)->readable = 1;
                                                                              7187
                                                                                        if(p->readopen == 0 | proc->killed){
7138 (*f0)->writable = 0;
                                                                              7188
                                                                                          release(&p->lock);
7139 (*f0)->pipe = p;
                                                                              7189
                                                                                          return -1;
7140 (*f1)->type = FD_PIPE;
                                                                              7190
7141 (*f1)->readable = 0;
                                                                              7191
                                                                                        wakeup(&p->nread);
7142 (*f1)->writable = 1;
                                                                              7192
                                                                                        sleep(&p->nwrite, &p->lock);
7143 (*f1)->pipe = p;
                                                                              7193
7144 return 0;
                                                                              7194
                                                                                      p->data[p->nwrite++ % PIPESIZE] = addr[i];
7145
                                                                              7195 }
7146
                                                                              7196 wakeup(&p->nread);
7147
                                                                              7197 release(&p->lock);
7148
                                                                              7198 return n;
7149
                                                                              7199 }
```

```
7200 int
7201 piperead(struct pipe *p, char *addr, int n)
7202 {
7203 int i;
7204
7205 acquire(&p->lock);
7206 while(p->nread == p->nwrite && p->writeopen){
7207
      if(proc->killed){
7208
          release(&p->lock);
7209
          return -1;
7210
7211
       sleep(&p->nread, &p->lock);
7212 }
7213 for(i = 0; i < n; i++)
7214
       if(p->nread == p->nwrite)
7215
          break;
7216
        addr[i] = p->data[p->nread++ % PIPESIZE];
7217 }
7218 wakeup(&p->nwrite);
7219 release(&p->lock);
7220 return i;
7221 }
7222
7223
7224
7225
7226
7227
7228
7229
7230
7231
7232
7233
7234
7235
7236
7237
7238
7239
7240
7241
7242
7243
7244
7245
7246
7247
7248
7249
```

```
7250 #include "types.h"
7251 #include "x86.h"
7252
7253 void*
7254 memset(void *dst, int c, uint n)
7255 {
7256 if ((int)dst%4 == 0 \&\& n%4 == 0)
      c &= 0xFF;
7257
7258
        stosl(dst, (c<<24)|(c<<16)|(c<<8)|c, n/4);
7259 } else
7260
        stosb(dst, c, n);
7261 return dst;
7262 }
7263
7264 int
7265 memcmp(const void *v1, const void *v2, uint n)
7266 {
7267 const uchar *s1, *s2;
7268
7269 	 s1 = v1;
7270 	 s2 = v2;
7271 while(n-- > 0){
      if(*s1 != *s2)
7272
7273
        return *s1 - *s2;
7274
      s1++, s2++;
7275 }
7276
7277 return 0;
7278 }
7279
7280 void*
7281 memmove(void *dst, const void *src, uint n)
7282 {
7283 const char *s;
7284 char *d;
7285
7286 s = src;
7287 d = dst;
7288 if (s < d \&\& s + n > d)
7289
     s += n;
7290 d += n;
7291
       while(n-- > 0)
7292
        *--d = *--s;
7293 } else
7294
        while(n-- > 0)
7295
          *d++ = *s++;
7296
7297 return dst;
7298 }
7299
```

F300 // source of the land of GGG . The source	7250 last
7300 // memcpy exists to placate GCC. Use memmove.	7350 int
7301 void*	7351 strlen(const char *s)
7302 memcpy(void *dst, const void *src, uint n)	7352 {
7303 {	7353 int n;
7304 return memmove(dst, src, n);	7354
7305 }	7355 $for(n = 0; s[n]; n++)$
7306	7356 ;
7307 int	7357 return n;
7308 strncmp(const char *p, const char *q, uint n)	7358 }
7309 {	7359
7310 while(n > 0 && *p && *p == *q)	7360
7311 n, p++, q++;	7361
7312 if(n == 0)	7362
7313 return 0;	7363
7314 return (uchar)*p - (uchar)*q;	7364
7315 }	7365
7316	7366
7317 char*	7367
7318 strncpy(char *s, const char *t, int n)	7368
7319 {	7369
7320 char *os;	7370
7321	7371
7322 os = s;	7372
7323 while(n > 0 && (*s++ = *t++) != 0)	7373
7324 ;	7374
7325 while($n->0$)	7375
7326 *s++ = 0;	7376
7327 return os;	7377
7328 }	7378
7329	7379
7330 // Like strncpy but guaranteed to NUL-terminate.	7380
7331 char*	7381
7332 safestrcpy(char *s, const char *t, int n)	7382
7333 {	7383
7334 char *os;	7384
7335	7385
7336 os = s;	7386
7337 $if(n \le 0)$	7387
7338 return os;	7388
7339 while $(-n > 0 \& (*s++ = *t++) != 0)$	7389
7340 ;	7390
7341 *s = 0;	7391
7342 return os;	7392
7343 }	7393
7344	7394
7345	7395
7346	7396
7347	7397
7348	7398
7349	7399

```
7400 // See MultiProcessor Specification Version 1.[14]
                                                                                7450 // Table entry types
7401
                                                                                7451 #define MPPROC
                                                                                                      0x00 // One per processor
7402 struct mp {
                            // floating pointer
                                                                                7452 #define MPBUS
                                                                                                      0x01 // One per bus
7403 uchar signature[4];
                                    // "_MP_"
                                                                                7453 #define MPIOAPIC
                                                                                                     0x02 // One per I/O APIC
                                    // phys addr of MP config table
7404 void *physaddr;
                                                                                7454 #define MPIOINTR
                                                                                                     0x03 // One per bus interrupt source
7405 uchar length;
                                    // 1
                                                                                7455 #define MPLINTR
                                                                                                      0x04 // One per system interrupt source
                                    // [14]
7406 uchar specrev;
                                                                               7456
                                    // all bytes must add up to 0
                                                                                7457
7407 uchar checksum;
7408 uchar type;
                                    // MP system config type
                                                                                7458
7409 uchar imcrp;
                                                                                7459
7410 uchar reserved[3];
                                                                                7460
7411 };
                                                                                7461
7412
                                                                                7462
7413 struct mpconf {
                            // configuration table header
                                                                                7463
7414 uchar signature[4];
                                                                                7464
                                    // "PCMP"
7415 ushort length;
                                    // total table length
                                                                                7465
7416 uchar version;
                                    // [14]
                                                                                7466
7417 uchar checksum;
                                    // all bytes must add up to 0
                                                                                7467
7418 uchar product[20];
                                    // product id
                                                                                7468
7419 uint *oemtable;
                                    // OEM table pointer
                                                                                7469
7420 ushort oemlength;
                                    // OEM table length
                                                                               7470
7421 ushort entry;
                                    // entry count
                                                                                7471
7422 uint *lapicaddr;
                                    // address of local APIC
                                                                                7472
7423 ushort xlength;
                                    // extended table length
                                                                                7473
7424 uchar xchecksum;
                                    // extended table checksum
                                                                                7474
7425 uchar reserved;
                                                                                7475
7426 };
                                                                                7476
7427
                                                                                7477
7428 struct mpproc {
                            // processor table entry
                                                                                7478
7429 uchar type;
                                                                                7479
                                    // entry type (0)
7430 uchar apicid;
                                    // local APIC id
                                                                                7480
7431 uchar version;
                                    // local APIC verison
                                                                                7481
7432 uchar flags;
                                                                                7482
                                    // CPU flags
7433
        #define MPBOOT 0x02
                                     // This proc is the bootstrap processor.
                                                                               7483
7434 uchar signature[4];
                                    // CPU signature
                                                                                7484
7435 uint feature;
                                    // feature flags from CPUID instruction
                                                                                7485
7436 uchar reserved[8];
                                                                                7486
7437 };
                                                                                7487
7438
                                                                                7488
7439 struct mpioapic {
                            // I/O APIC table entry
                                                                                7489
7440 uchar type;
                                    // entry type (2)
                                                                                7490
                                                                                7491
7441 uchar apicno;
                                    // I/O APIC id
7442 uchar version;
                                    // I/O APIC version
                                                                                7492
7443 uchar flags;
                                    // I/O APIC flags
                                                                                7493
7444 uint *addr;
                                   // I/O APIC address
                                                                                7494
7445 };
                                                                                7495
7446
                                                                                7496
7447
                                                                                7497
7448
                                                                                7498
7449
                                                                                7499
```

Sheet 74 Sheet 74

```
7500 // Blank page.
7501
7502
7503
7504
7505
7506
7507
7508
7509
7510
7511
7512
7513
7514
7515
7516
7517
7518
7519
7520
7521
7522
7523
7524
7525
7526
7527
7528
7529
7530
7531
7532
7533
7534
7535
7536
7537
7538
7539
7540
7541
7542
7543
7544
7545
7546
7547
7548
7549
```

```
7550 // Multiprocessor support
7551 // Search memory for MP description structures.
7552 // http://developer.intel.com/design/pentium/datashts/24201606.pdf
7553
7554 #include "types.h"
7555 #include "defs.h"
7556 #include "param.h"
7557 #include "memlayout.h"
7558 #include "mp.h"
7559 #include "x86.h"
7560 #include "mmu.h"
7561 #include "proc.h"
7562
7563 struct cpu cpus[NCPU];
7564 static struct cpu *bcpu;
7565 int ismp;
7566 int ncpu;
7567 uchar ioapicid;
7568
7569 int
7570 mpbcpu(void)
7571 {
7572 return bcpu-cpus;
7573 }
7574
7575 static uchar
7576 sum(uchar *addr, int len)
7577 {
7578 int i, sum;
7579
7580 \quad \text{sum} = 0;
7581 for(i=0; i<len; i++)
7582 sum += addr[i];
7583 return sum;
7584 }
7585
7586 // Look for an MP structure in the len bytes at addr.
7587 static struct mp*
7588 mpsearch1(uint a, int len)
7589 {
7590 uchar *e, *p, *addr;
7591
7592 addr = p2v(a);
7593 e = addr + len;
7594 for(p = addr; p < e; p += sizeof(struct mp))
7595
       if(memcmp(p, "_MP_", 4) == 0 && sum(p, sizeof(struct mp)) == 0)
7596
           return (struct mp*)p;
7597 return 0;
7598 }
7599
```

```
7600 // Search for the MP Floating Pointer Structure, which according to the
                                                                               7650 void
7601 // spec is in one of the following three locations:
                                                                               7651 mpinit(void)
7602 // 1) in the first KB of the EBDA;
                                                                               7652 {
7603 // 2) in the last KB of system base memory;
                                                                               7653 uchar *p, *e;
7604 // 3) in the BIOS ROM between 0xE0000 and 0xFFFFF.
                                                                               7654 struct mp *mp;
7605 static struct mp*
                                                                               7655 struct mpconf *conf;
7606 mpsearch(void)
                                                                               7656 struct mpproc *proc;
7607 {
                                                                               7657 struct mpioapic *ioapic;
7608 uchar *bda;
                                                                               7658
7609 uint p;
                                                                               7659 bcpu = &cpus[0];
7610 struct mp *mp;
                                                                               7660 if((conf = mpconfig(&mp)) == 0)
                                                                                       return;
7611
                                                                               7661
7612 bda = (uchar *) P2V(0x400);
                                                                               7662 ismp = 1;
7613 if((p = ((bda[0x0F]<<8)| bda[0x0E]) << 4)){
                                                                               7663 lapic = (uint*)conf->lapicaddr;
      if((mp = mpsearch1(p, 1024)))
                                                                               7664 for(p=(uchar*)(conf+1), e=(uchar*)conf+conf->length; p<e; ){
7615
          return mp;
                                                                               7665
                                                                                        switch(*p){
7616 } else {
                                                                               7666
                                                                                        case MPPROC:
      p = ((bda[0x14] << 8)|bda[0x13])*1024;
7617
                                                                               7667
                                                                                          proc = (struct mpproc*)p;
7618
      if((mp = mpsearch1(p-1024, 1024)))
                                                                               7668
                                                                                          if(ncpu != proc->apicid){
                                                                                            cprintf("mpinit: ncpu=%d apicid=%d\n", ncpu, proc->apicid);
7619
          return mp;
                                                                               7669
7620 }
                                                                               7670
                                                                                            ismp = 0;
7621 return mpsearch1(0xF0000, 0x10000);
                                                                               7671
7622 }
                                                                               7672
                                                                                          if(proc->flags & MPBOOT)
7623
                                                                               7673
                                                                                            bcpu = &cpus[ncpu];
7624 // Search for an MP configuration table. For now,
                                                                               7674
                                                                                          cpus[ncpu].id = ncpu;
7625 // don't accept the default configurations (physaddr == 0).
                                                                               7675
                                                                                          ncpu++;
7626 // Check for correct signature, calculate the checksum and,
                                                                               7676
                                                                                          p += sizeof(struct mpproc);
7627 // if correct, check the version.
                                                                               7677
                                                                                          continue;
7628 // To do: check extended table checksum.
                                                                               7678
                                                                                        case MPIOAPIC:
7629 static struct mpconf*
                                                                                          ioapic = (struct mpioapic*)p;
                                                                               7679
7630 mpconfig(struct mp **pmp)
                                                                               7680
                                                                                          ioapicid = ioapic->apicno;
7631 {
                                                                               7681
                                                                                          p += sizeof(struct mpioapic);
7632 struct mpconf *conf;
                                                                               7682
                                                                                          continue;
7633 struct mp *mp;
                                                                               7683
                                                                                        case MPBUS:
                                                                                        case MPIOINTR:
7634
                                                                               7684
7635 if((mp = mpsearch()) == 0 || mp->physaddr == 0)
                                                                               7685
                                                                                        case MPLINTR:
7636 return 0;
                                                                               7686
                                                                                          p += 8;
7637 conf = (struct mpconf*) p2v((uint) mp->physaddr);
                                                                               7687
                                                                                          continue;
7638 if(memcmp(conf, "PCMP", 4) != 0)
                                                                               7688
                                                                                        default:
7639
      return 0;
                                                                               7689
                                                                                          cprintf("mpinit: unknown config type %x\n", *p);
7640 if(conf->version != 1 && conf->version != 4)
                                                                               7690
                                                                                          ismp = 0;
                                                                               7691
7641
      return 0;
7642 if(sum((uchar*)conf, conf->length) != 0)
                                                                               7692 }
7643
      return 0;
                                                                               7693 if(!ismp){
7644 *pmp = mp;
                                                                                       // Didn't like what we found; fall back to no MP.
                                                                               7694
7645 return conf;
                                                                                        ncpu = 1;
                                                                               7695
                                                                                        lapic = 0;
7646 }
                                                                               7696
7647
                                                                               7697
                                                                                        ioapicid = 0;
7648
                                                                               7698
                                                                                        return;
7649
                                                                               7699 }
```

Sheet 76 Sheet 76

```
7700 if(mp->imcrp){
7701
        // Bochs doesn't support IMCR, so this doesn't run on Bochs.
7702
        // But it would on real hardware.
7703
        outb(0x22, 0x70); // Select IMCR
        outb(0x23, inb(0x23) | 1); // Mask external interrupts.
7704
7705 }
7706 }
7707
7708
7709
7710
7711
7712
7713
7714
7715
7716
7717
7718
7719
7720
7721
7722
7723
7724
7725
7726
7727
7728
7729
7730
7731
7732
7733
7734
7735
7736
7737
7738
7739
7740
7741
7742
7743
7744
7745
7746
7747
7748
7749
```

```
7750 // The local APIC manages internal (non-I/O) interrupts.
7751 // See Chapter 8 & Appendix C of Intel processor manual volume 3.
7752
7753 #include "types.h"
7754 #include "defs.h"
7755 #include "date.h"
7756 #include "memlayout.h"
7757 #include "traps.h"
7758 #include "mmu.h"
7759 #include "x86.h"
7760
7761 // Local APIC registers, divided by 4 for use as uint[] indices.
7762 #define ID
                   (0x0020/4) // ID
7763 #define VER
                   (0x0030/4) // Version
7764 #define TPR
                   (0x0080/4) // Task Priority
7765 #define EOI
                   (0x00B0/4) // EOI
7766 #define SVR
                    (0x00F0/4) // Spurious Interrupt Vector
7767 #define ENABLE
                        0x00000100 // Unit Enable
7768 #define ESR
                   (0x0280/4) // Error Status
7769 #define ICRLO (0x0300/4) // Interrupt Command
7770 #define INIT
                        0x00000500 // INIT/RESET
                        0x00000600 // Startup IPI
7771 #define STARTUP
7772 #define DELIVS
                        0x00001000 // Delivery status
7773 #define ASSERT
                        0x00004000 // Assert interrupt (vs deassert)
7774 #define DEASSERT 0x00000000
7775 #define LEVEL
                        0x00008000 // Level triggered
7776 #define BCAST
                        0x00080000 // Send to all APICs, including self.
7777 #define BUSY
                        0x00001000
7778 #define FIXED
                        0x00000000
7779 #define ICRHI (0x0310/4) // Interrupt Command [63:32]
7780 #define TIMER (0x0320/4) // Local Vector Table 0 (TIMER)
7781 #define X1
                        0x0000000B // divide counts by 1
7782 #define PERIODIC 0x00020000 // Periodic
7783 #define PCINT
                   (0x0340/4) // Performance Counter LVT
7784 #define LINTO
                   (0x0350/4) // Local Vector Table 1 (LINT0)
7785 #define LINT1
                   (0x0360/4) // Local Vector Table 2 (LINT1)
7786 #define ERROR
                   (0x0370/4) // Local Vector Table 3 (ERROR)
7787 #define MASKED
                        0x00010000 // Interrupt masked
7788 #define TICR
                   (0x0380/4) // Timer Initial Count
7789 #define TCCR
                    (0x0390/4) // Timer Current Count
7790 #define TDCR
                   (0x03E0/4) // Timer Divide Configuration
7791
7792 volatile uint *lapic; // Initialized in mp.c
7794 static void
7795 lapicw(int index, int value)
7796 {
7797 lapic[index] = value;
7798 lapic[ID]; // wait for write to finish, by reading
7799 }
```

```
7800
7801
7802
7803
7804
7805
7806
7807
7808
7809
7810
7811
7812
7813
7814
7815
7816
7817
7818
7819
7820
7821
7822
7823
7824
7825
7826
7827
7828
7829
7830
7831
7832
7833
7834
7835
7836
7837
7838
7839
7840
7841
7842
7843
7844
7845
7846
7847
7848
7849
```

```
7850 void
7851 lapicinit(void)
7852 {
7853 if(!lapic)
7854
        return;
7855
7856 // Enable local APIC; set spurious interrupt vector.
     lapicw(SVR, ENABLE | (T_IRQ0 + IRQ_SPURIOUS));
7858
7859 // The timer repeatedly counts down at bus frequency
7860 // from lapic[TICR] and then issues an interrupt.
7861 // If xv6 cared more about precise timekeeping,
7862 // TICR would be calibrated using an external time source.
7863 lapicw(TDCR, X1);
7864 lapicw(TIMER, PERIODIC | (T_IRQO + IRQ_TIMER));
7865 lapicw(TICR, 10000000);
7866
7867 // Disable logical interrupt lines.
7868 lapicw(LINTO, MASKED);
7869
      lapicw(LINT1, MASKED);
7870
7871 // Disable performance counter overflow interrupts
     // on machines that provide that interrupt entry.
7873 if(((lapic[VER]>>16) & 0xFF) >= 4)
7874
        lapicw(PCINT, MASKED);
7875
7876 // Map error interrupt to IRQ_ERROR.
7877 lapicw(ERROR, T_IRQ0 + IRQ_ERROR);
7878
7879 // Clear error status register (requires back-to-back writes).
7880 lapicw(ESR, 0);
7881 lapicw(ESR, 0);
7882
7883 // Ack any outstanding interrupts.
7884 lapicw(EOI, 0);
7885
7886 // Send an Init Level De-Assert to synchronise arbitration ID's.
7887 lapicw(ICRHI, 0);
7888 lapicw(ICRLO, BCAST | INIT | LEVEL);
7889
      while(lapic[ICRLO] & DELIVS)
7890
7891
7892
      // Enable interrupts on the APIC (but not on the processor).
7893
      lapicw(TPR, 0);
7894 }
7895
7896
7897
7898
7899
```

```
7900 int
                                                                                7950 outb(CMOS_PORT+1, 0x0A);
7901 cpunum(void)
                                                                               7951 wrv = (ushort^*)P2V((0x40 << 4 \mid 0x67)); // Warm reset vector
7902 {
                                                                               7952 \text{ wrv}[0] = 0;
                                                                               7953 wrv[1] = addr >> 4;
7903 // Cannot call cpu when interrupts are enabled:
7904 // result not guaranteed to last long enough to be used!
                                                                               7954
7905 // Would prefer to panic but even printing is chancy here:
                                                                                7955 // "Universal startup algorithm."
7906 // almost everything, including cprintf and panic, calls cpu,
                                                                                7956
                                                                                     // Send INIT (level-triggered) interrupt to reset other CPU.
7907 // often indirectly through acquire and release.
                                                                               7957 lapicw(ICRHI, apicid<<24);
7908 if(readeflags()&FL_IF){
                                                                                7958 lapicw(ICRLO, INIT | LEVEL | ASSERT);
7909
        static int n;
                                                                               7959 microdelay(200);
7910
        if(n++==0)
                                                                               7960
                                                                                      lapicw(ICRLO, INIT | LEVEL);
7911
          cprintf("cpu called from %x with interrupts enabled\n",
                                                                                      microdelay(100); // should be 10ms, but too slow in Bochs!
                                                                               7961
7912
            __builtin_return_address(0));
                                                                                7962
7913 }
                                                                                7963 // Send startup IPI (twice!) to enter code.
7914
                                                                               7964 // Regular hardware is supposed to only accept a STARTUP
7915 if(lapic)
                                                                               7965 // when it is in the halted state due to an INIT. So the second
7916
      return lapic[ID]>>24;
                                                                                     // should be ignored, but it is part of the official Intel algorithm.
7917 return 0;
                                                                                     // Bochs complains about the second one. Too bad for Bochs.
7918 }
                                                                               7968 for(i = 0; i < 2; i++){
7919
                                                                                7969
                                                                                       lapicw(ICRHI, apicid<<24);
                                                                                        lapicw(ICRLO, STARTUP | (addr>>12));
7920 // Acknowledge interrupt.
                                                                               7970
7921 void
                                                                               7971
                                                                                        microdelay(200);
                                                                               7972 }
7922 lapiceoi(void)
7923 {
                                                                               7973 }
7924 if(lapic)
                                                                               7974
7925
        lapicw(EOI, 0);
                                                                               7975 #define CMOS STATA
                                                                                                         0x0a
7926 }
                                                                               7976 #define CMOS_STATB
                                                                                                         0x0b
7927
                                                                               7977 #define CMOS_UIP (1 << 7)
                                                                                                                       // RTC update in progress
7928 // Spin for a given number of microseconds.
                                                                               7978
7929 // On real hardware would want to tune this dynamically.
                                                                               7979 #define SECS
                                                                                                    0x00
7930 void
                                                                               7980 #define MINS
                                                                                                    0x02
7931 microdelay(int us)
                                                                                7981 #define HOURS
                                                                                                    0 \times 04
7932 {
                                                                               7982 #define DAY
                                                                                                    0x07
7933 }
                                                                                7983 #define MONTH
                                                                                                    0x08
7934
                                                                                7984 #define YEAR
                                                                                                    0x09
7935 #define CMOS_PORT
                                                                               7985
7936 #define CMOS RETURN 0x71
                                                                               7986 static uint cmos_read(uint reg)
                                                                               7987 {
7938 // Start additional processor running entry code at addr.
                                                                               7988 outb(CMOS_PORT, reg);
7939 // See Appendix B of MultiProcessor Specification.
                                                                               7989 microdelay(200);
                                                                               7990
7941 lapicstartap(uchar apicid, uint addr)
                                                                               7991 return inb(CMOS_RETURN);
7942 {
                                                                                7992 }
7943 int i;
                                                                               7993
7944 ushort *wrv;
                                                                                7994
                                                                                7995
7945
7946 // "The BSP must initialize CMOS shutdown code to OAH
                                                                                7996
7947 // and the warm reset vector (DWORD based at 40:67) to point at
                                                                                7997
7948 // the AP startup code prior to the [universal startup algorithm]."
                                                                                7998
7949 outb(CMOS_PORT, 0xF); // offset 0xF is shutdown code
                                                                               7999
```

Sheet 79 Sheet 79

```
8000 static void fill_rtcdate(struct rtcdate *r)
8001 {
8002 r->second = cmos read(SECS);
8003 r->minute = cmos_read(MINS);
8004 r->hour = cmos_read(HOURS);
8005 \quad r\rightarrow day = cmos read(DAY);
8006 r->month = cmos_read(MONTH);
8007 r->year = cmos_read(YEAR);
8008 }
8009
8010 // qemu seems to use 24-hour GWT and the values are BCD encoded
8011 void cmostime(struct rtcdate *r)
8012 {
8013 struct rtcdate t1, t2;
8014 int sb. bcd;
8015
8016    sb = cmos_read(CMOS_STATB);
8017
8018 bcd = (sb \& (1 << 2)) == 0;
8019
8020 // make sure CMOS doesn't modify time while we read it
8021 for (;;) {
8022
        fill rtcdate(&t1);
8023
        if (cmos_read(CMOS_STATA) & CMOS_UIP)
8024
            continue;
8025
        fill rtcdate(&t2);
8026
        if (memcmp(&t1, &t2, sizeof(t1)) == 0)
8027
          break;
8028 }
8029
8030 // convert
8031 if (bcd) {
8032 #define
               CONV(x)
                           (t1.x = ((t1.x >> 4) * 10) + (t1.x & 0xf))
8033
        CONV(second);
8034
        CONV(minute);
8035
        CONV(hour);
8036
        CONV(day);
8037
        CONV(month);
8038
        CONV(year );
8039 #undef
               CONV
8040 }
8041
8042 	 *r = t1;
8043 r->year += 2000;
8044 }
8045
8046
8047
8048
8049
```

```
8050 // The I/O APIC manages hardware interrupts for an SMP system.
8051 // http://www.intel.com/design/chipsets/datashts/29056601.pdf
8052 // See also picirg.c.
8053
8054 #include "types.h"
8055 #include "defs.h"
8056 #include "traps.h"
8057
8058 #define IOAPIC 0xFEC00000 // Default physical address of IO APIC
8059
8060 #define REG ID
                       0x00 // Register index: ID
8061 #define REG VER
                       0x01 // Register index: version
8062 #define REG_TABLE 0x10 // Redirection table base
8063
8064 // The redirection table starts at REG TABLE and uses
8065 // two registers to configure each interrupt.
8066 // The first (low) register in a pair contains configuration bits.
8067 // The second (high) register contains a bitmask telling which
8068 // CPUs can serve that interrupt.
8069 #define INT DISABLED 0x00010000 // Interrupt disabled
8070 #define INT LEVEL
                           0x00008000 // Level-triggered (vs edge-)
8071 #define INT ACTIVELOW 0x00002000 // Active low (vs high)
                           0x00000800 // Destination is CPU id (vs APIC ID)
8072 #define INT LOGICAL
8074 volatile struct ioapic *ioapic;
8076 // IO APIC MMIO structure: write req, then read or write data.
8077 struct ioapic {
8078 uint reg;
8079 uint pad[3];
8080 uint data;
8081 };
8082
8083 static uint
8084 ioapicread(int reg)
8085 {
8086 ioapic->reg = reg;
8087 return ioapic->data;
8088 }
8089
8090 static void
8091 ioapicwrite(int reg, uint data)
8092 {
8093 ioapic->req = req;
8094 ioapic->data = data;
8095 }
8096
8097
8098
8099
```

Sheet 80 Sheet 80

```
8100 void
                                                                               8150 // Intel 8259A programmable interrupt controllers.
8101 ioapicinit(void)
                                                                               8151
8102 {
                                                                               8152 #include "types.h"
8103 int i, id, maxintr;
                                                                               8153 #include "x86.h"
8104
                                                                               8154 #include "traps.h"
8105 if(!ismp)
                                                                               8155
8106
      return;
                                                                               8156 // I/O Addresses of the two programmable interrupt controllers
8107
                                                                                                          0x20 // Master (IRQs 0-7)
                                                                              8157 #define IO_PIC1
8108 ioapic = (volatile struct ioapic*)IOAPIC;
                                                                               8158 #define IO_PIC2
                                                                                                          0xA0 // Slave (IRQs 8-15)
8109 maxintr = (ioapicread(REG_VER) >> 16) & 0xFF;
                                                                               8159
8110 id = ioapicread(REG_ID) >> 24;
                                                                               8160 #define IRQ_SLAVE
                                                                                                                 // IRO at which slave connects to master
8111 if(id != ioapicid)
                                                                               8161
8112
        cprintf("ioapicinit: id isn't equal to ioapicid; not a MP\n");
                                                                               8162 // Current IRQ mask.
8113
                                                                               8163 // Initial IRQ mask has interrupt 2 enabled (for slave 8259A).
8114 // Mark all interrupts edge-triggered, active high, disabled,
                                                                               8164 static ushort irgmask = 0xFFFF & ~(1<<IRQ_SLAVE);</pre>
8115 // and not routed to any CPUs.
                                                                               8165
8116 for(i = 0; i \le maxintr; i++)
                                                                               8166 static void
      ioapicwrite(REG_TABLE+2*i, INT_DISABLED | (T_IRQ0 + i));
8117
                                                                               8167 picsetmask(ushort mask)
8118
        ioapicwrite(REG_TABLE+2*i+1, 0);
                                                                               8168 {
8119 }
                                                                               8169 irgmask = mask;
8120 }
                                                                               8170 outb(IO PIC1+1, mask);
8121
                                                                               8171 outb(IO_PIC2+1, mask >> 8);
8122 void
                                                                               8172 }
8123 ioapicenable(int irg, int cpunum)
                                                                               8173
                                                                               8174 void
8124 {
8125 if(!ismp)
                                                                               8175 picenable(int irg)
8126
      return;
                                                                               8176 {
8127
                                                                               8177 picsetmask(irgmask & ~(1<<irg));
8128 // Mark interrupt edge-triggered, active high,
                                                                               8178 }
8129 // enabled, and routed to the given cpunum,
                                                                               8179
8130 // which happens to be that cpu's APIC ID.
                                                                               8180 // Initialize the 8259A interrupt controllers.
8131 ioapicwrite(REG_TABLE+2*irg, T_IRQ0 + irg);
                                                                               8181 void
8132 ioapicwrite(REG_TABLE+2*irq+1, cpunum << 24);
                                                                               8182 picinit(void)
8133 }
                                                                               8183 {
8134
                                                                               8184 // mask all interrupts
8135
                                                                               8185 outb(IO_PIC1+1, 0xFF);
8136
                                                                               8186 outb(IO_PIC2+1, 0xFF);
8137
                                                                               8187
8138
                                                                              8188 // Set up master (8259A-1)
8139
                                                                               8189
8140
                                                                               8190 // ICW1: 0001q0hi
8141
                                                                               8191 // g: 0 = edge triggering, 1 = level triggering
8142
                                                                               8192 // h: 0 = cascaded PICs, 1 = master only
8143
                                                                               8193 // i: 0 = no ICW4, 1 = ICW4 required
                                                                               8194 outb(IO PIC1, 0x11);
8144
8145
                                                                               8195
8146
                                                                               8196 // ICW2: Vector offset
8147
                                                                               8197 outb(IO_PIC1+1, T_IRQ0);
8148
                                                                               8198
                                                                               8199
8149
```

Sheet 81 Sheet 81

```
8200 // ICW3: (master PIC) bit mask of IR lines connected to slaves
                                                                             8250 // PC keyboard interface constants
8201 //
                (slave PIC) 3-bit # of slave's connection to master
                                                                             8251
8202 outb(IO PIC1+1, 1<<IRO SLAVE);
                                                                             8252 #define KBSTATP
                                                                                                         0x64
                                                                                                               // kbd controller status port(I)
                                                                             8253 #define KBS_DIB
                                                                                                        0x01
                                                                                                              // kbd data in buffer
8203
                                                                             8254 #define KBDATAP
                                                                                                        0x60
8204 // ICW4: 000nbmap
                                                                                                              // kbd data port(I)
8205 // n: 1 = special fully nested mode
                                                                             8255
8206 // b: 1 = buffered mode
                                                                             8256 #define NO
                                                                                                        0
8207 // m: 0 = slave PIC, 1 = master PIC
                                                                             8257
8208 //
           (ignored when b is 0, as the master/slave role
                                                                             8258 #define SHIFT
                                                                                                         (1 << 0)
8209 //
           can be hardwired).
                                                                             8259 #define CTL
                                                                                                         (1 << 1)
8210 // a: 1 = Automatic EOI mode
                                                                             8260 #define ALT
                                                                                                         (1 << 2)
8211 // p: 0 = MCS-80/85 mode, 1 = intel x86 mode
                                                                             8261
8212 outb(IO_PIC1+1, 0x3);
                                                                             8262 #define CAPSLOCK
                                                                                                         (1 << 3)
8213
                                                                             8263 #define NUMLOCK
                                                                                                         (1 << 4)
8214 // Set up slave (8259A-2)
                                                                             8264 #define SCROLLLOCK
                                                                                                        (1 < < 5)
8215 outb(IO_PIC2, 0x11);
                                          // ICW1
                                                                             8265
8216 outb(IO_PIC2+1, T_IRQ0 + 8);
                                      // ICW2
                                                                             8266 #define E0ESC
                                                                                                        (1 < < 6)
8217 outb(IO_PIC2+1, IRQ_SLAVE);
                                         // ICW3
                                                                             8267
8218 // NB Automatic EOI mode doesn't tend to work on the slave.
                                                                             8268 // Special keycodes
8219 // Linux source code says it's "to be investigated".
                                                                             8269 #define KEY HOME
                                                                                                         0xE0
8220 outb(IO_PIC2+1, 0x3);
                                                                             8270 #define KEY END
                                                                                                         0xE1
8221
                                                                             8271 #define KEY UP
                                                                                                        0xE2
8222 // OCW3: 0ef01prs
                                                                             8272 #define KEY DN
                                                                                                        0xE3
8223 // ef: 0x = NOP, 10 = clear specific mask, <math>11 = set specific mask
                                                                             8273 #define KEY_LF
                                                                                                        0xE4
8224 // p: 0 = no polling, 1 = polling mode
                                                                             8274 #define KEY RT
                                                                                                        0xE5
8225 // rs: 0x = NOP, 10 = read IRR, 11 = read ISR
                                                                             8275 #define KEY PGUP
                                                                                                        0xE6
8226 outb(IO_PIC1, 0x68);
                            // clear specific mask
                                                                                                        0xE7
                                                                             8276 #define KEY_PGDN
8227 outb(IO_PIC1, 0x0a);
                                   // read IRR by default
                                                                             8277 #define KEY_INS
                                                                                                         0xE8
8228
                                                                             8278 #define KEY DEL
                                                                                                        0xE9
8229 outb(IO_PIC2, 0x68);
                                   // OCW3
                                                                             8279
8230 outb(IO_PIC2, 0x0a);
                                   // OCW3
                                                                             8280 // C('A') == Control-A
8231
                                                                             8281 #define C(x) (x - '@')
8232 if(irqmask != 0xFFFF)
                                                                             8282
8233
        picsetmask(irqmask);
                                                                             8283 static uchar shiftcode[256] =
8234 }
                                                                             8284 {
8235
                                                                             8285 [0x1D] CTL,
8236
                                                                             8286 [0x2A] SHIFT,
                                                                             8287 [0x36] SHIFT.
8237
8238
                                                                             8288 [0x38] ALT,
8239
                                                                             8289 [0x9D] CTL,
8240
                                                                             8290 [0xB8] ALT
8241
                                                                             8291 };
8242
8243
                                                                             8293 static uchar togglecode[256] =
8244
                                                                             8294 {
8245
                                                                             8295 [0x3A] CAPSLOCK,
8246
                                                                             8296 [0x45] NUMLOCK,
8247
                                                                             8297 [0x46] SCROLLLOCK
8248
                                                                             8298 };
                                                                             8299
8249
```

Sheet 82 Sheet 82

```
8300 static uchar normalmap[256] =
8301 {
8302 NO,
            0x1B, '1', '2', '3', '4', '5', '6', // 0x00
      777.
                  191,
                             '-',
                                        '\b', '\t',
8303
           181,
                        ′0′,
                                  ′=′,
8304
      'q', 'w',
                  'e',
                       'r', 't',
                                  ′У′,
                                        'u', 'i', // 0x10
8305
      'o', 'p',
                 ′[′,
                        ′]′,
                             '\n', NO,
                                         'a', 's',
8306
      'd', 'f',
                  ′g′,
                        'h',
                             ′j′,
                                   'k',
                                         '1',
                                             ';', // 0x20
      '\'', '\',
                        '\\', 'Z',
                                              'v',
8307
                 NO,
                                   'x',
                                        'C',
8308
      'b', 'n',
                 'm',
                                              '*', // 0x30
                                        NO,
            ′′,
                 NO,
                             NO,
8309
      NO,
                       NO,
                                   NO,
                                        NO,
                                              NO,
8310
      NO,
            NO,
                 NO,
                       NO,
                             NO,
                                  NO,
                                        NO,
                                              '7', // 0x40
                 ′-′,
      181, 191,
                       '4', '5', '6',
                                        '+', '1',
8311
      '2', '3', '0', '.', NO,
                                  NO, NO,
                                              NO, // 0x50
8312
      [0x9C] '\n',
8313
                       // KP_Enter
8314
      [0xB5] '/',
                       // KP Div
      [0xC8] KEY_UP,
                       [0xD0] KEY_DN,
8315
8316
      [0xC9] KEY PGUP,
                       [0xD1] KEY_PGDN,
8317
      [0xCB] KEY_LF,
                        [0xCD] KEY_RT,
8318
      [0x97] KEY_HOME,
                       [0xCF] KEY_END,
8319
      [0xD2] KEY_INS,
                       [0xD3] KEY_DEL
8320 };
8321
8322 static uchar shiftmap[256] =
8323 {
8324 NO.
                       '@', '#', '$', '%', '^', // 0x00
            033, '!',
8325
      '&',
            1 * 1 .
                 ′(′,
                       ′)′,
                                  ' + ' .
                                        '\b', '\t',
8326
      'Q', 'W', 'E',
                       'R', 'T', 'Y',
                                        'U', 'I', // 0x10
      'O', 'P',
                        '}',
                             '\n', NO,
8327
                                         'A',
                                              'S',
8328
      'D'.
                             ΊΙ',
                                  ′K′
                                              ':', // 0x20
           'F',
                  'G',
                        Ή',
                                        'L',
8329
      '"', '~', NO,
                        '|', 'Z',
                                              ′Υ′,
                                  ′Χ′,
                                        'C',
                                              '*', // 0x30
8330 'B', 'N',
                  'M',
                        '<',
                             '>',
                                   ′?′,
                                        NO,
            , ,
8331
      NO,
                 NO,
                       NO,
                             NO,
                                  NO,
                                        NO,
                                              NO,
                             NO,
8332
      NO,
            NO,
                 NO,
                       NO,
                                  NO,
                                        NO,
                                              '7', // 0x40
                 ′-′,
8333
      181,
            191,
                       '4', '5', '6',
                                        ' + ' ,
                                             11',
8334 '2', '3', '0', '.', NO, NO, NO,
                                             NO, // 0x50
      [0x9C] '\n',
8335
                       // KP_Enter
8336
      [0xB5] '/',
                       // KP_Div
8337
      [0xC8] KEY_UP,
                       [0xD0] KEY_DN,
8338
      [0xC9] KEY_PGUP, [0xD1] KEY_PGDN,
8339
      [0xCB] KEY_LF,
                        [0xCD] KEY_RT,
8340
      [0x97] KEY HOME,
                       [OxCF] KEY END,
8341
      [0xD2] KEY_INS,
                       [0xD3] KEY_DEL
8342 };
8343
8344
8345
8346
8347
8348
8349
```

```
8350 static uchar ctlmap[256] =
8351 {
8352 NO,
               NO,
                        NO,
                                 NO,
                                          NO,
                                                   NO,
                                                            NO,
                                                                     NO,
8353
      NO,
               NO,
                        NO,
                                 NO,
                                          NO,
                                                   NO,
                                                            NO,
                                                                     NO,
8354
      C('Q'), C('W'), C('E'), C('R'), C('T'), C('Y'), C('U'), C('I'),
      C('O'), C('P'), NO,
                                           ′\r′,
8355
                                 NO,
                                                   NO,
                                                            C('A'), C('S'),
8356
      C('D'), C('F'), C('G'), C('H'), C('J'), C('K'), C('L'), NO,
8357
                        NO,
                                 C('\setminus '), C('Z'), C('X'), C('C'), C('V'),
      NO,
               NO,
8358
      C('B'), C('N'), C('M'), NO,
                                          NO,
                                                   C('/'), NO,
                                                                     NO,
       [0x9C] '\r',
8359
                         // KP_Enter
8360
      [0xB5] C('/'),
                        // KP_Div
8361
       [0xC8] KEY_UP,
                        [0xD0] KEY_DN,
8362
       [0xC9] KEY_PGUP, [0xD1] KEY_PGDN,
8363
       [0xCB] KEY_LF,
                         [0xCD] KEY_RT,
8364
      [0x97] KEY_HOME, [0xCF] KEY_END,
8365
      [0xD2] KEY_INS,
                        [0xD3] KEY_DEL
8366 };
8367
8368
8369
8370
8371
8372
8373
8374
8375
8376
8377
8378
8379
8380
8381
8382
8383
8384
8385
8386
8387
8388
8389
8390
8391
8392
8393
8394
8395
8396
8397
8398
8399
```

```
8400 #include "types.h"
8401 #include "x86.h"
8402 #include "defs.h"
8403 #include "kbd.h"
8404
8405 int
8406 kbdgetc(void)
8407 {
8408 static uint shift;
8409 static uchar *charcode[4] = {
8410
      normalmap, shiftmap, ctlmap, ctlmap
8411 };
8412 uint st, data, c;
8413
8414 st = inb(KBSTATP);
8415 if((st & KBS_DIB) == 0)
8416 return -1;
8417 data = inb(KBDATAP);
8418
8419 if (data == 0xE0) {
8420
      shift |= E0ESC;
8421
      return 0;
8422 } else if(data & 0x80){
8423
      // Key released
8424
      data = (shift & EOESC ? data : data & 0x7F);
8425
       shift &= ~(shiftcode[data] | E0ESC);
8426
      return 0;
8427 } else if(shift & EOESC){
8428
      // Last character was an EO escape; or with 0x80
8429
       data |= 0x80;
8430
      shift &= ~E0ESC;
8431 }
8432
8433 shift |= shiftcode[data];
8434 shift ^= togglecode[data];
8435 c = charcode[shift & (CTL | SHIFT)][data];
8436 if(shift & CAPSLOCK){
8437
      if('a' <= c && c <= 'z')
8438
        c += 'A' - 'a';
      else if('A' <= c && c <= 'Z')
8439
8440
         c += 'a' - 'A';
8441 }
8442 return c;
8443 }
8444
8445 void
8446 kbdintr(void)
8447 {
8448 consoleintr(kbdgetc);
8449 }
```

```
8450 // Console input and output.
8451 // Input is from the keyboard or serial port.
8452 // Output is written to the screen and serial port.
8453
8454 #include "types.h"
8455 #include "defs.h"
8456 #include "param.h"
8457 #include "traps.h"
8458 #include "spinlock.h"
8459 #include "fs.h"
8460 #include "file.h"
8461 #include "memlayout.h"
8462 #include "mmu.h"
8463 #include "proc.h"
8464 #include "x86.h"
8465
8466 static void consputc(int);
8468 static int panicked = 0;
8469
8470 static struct {
8471 struct spinlock lock;
8472 int locking;
8473 } cons;
8474
8475 static void
8476 printint(int xx, int base, int sign)
8477 {
8478 static char digits[] = "0123456789abcdef";
8479 char buf[16];
8480 int. i;
8481 uint x;
8482
8483 if(sign && (sign = xx < 0))
8484
      x = -xx;
8485 else
8486
       x = xx;
8487
8488 i = 0;
8489 do{
8490
      buf[i++] = digits[x % base];
8491 \}while((x /= base) != 0);
8492
8493 if(sign)
       buf[i++] = '-';
8494
8495
8496 while(--i >= 0)
8497
        consputc(buf[i]);
8498 }
8499
```

```
8550 if(locking)
8500 // Print to the console. only understands %d, %x, %p, %s.
8501 void
                                                                             8551
                                                                                      release(&cons.lock);
8502 cprintf(char *fmt, ...)
                                                                             8552 }
8503 {
                                                                             8553
8504 int i, c, locking;
                                                                             8554 void
8505 uint *arqp;
                                                                             8555 panic(char *s)
8506 char *s;
                                                                             8556 {
8507
                                                                             8557 int i;
8508 locking = cons.locking;
                                                                             8558 uint pcs[10];
8509 if(locking)
                                                                             8559
8510
      acquire(&cons.lock);
                                                                             8560 cli();
8511
                                                                             8561 cons.locking = 0;
8512 if (fmt == 0)
                                                                             8562 cprintf("cpu%d: panic: ", cpu->id);
8513
        panic("null fmt");
                                                                             8563 cprintf(s);
8514
                                                                             8564 cprintf("\n");
8515 argp = (uint*)(void*)(&fmt + 1);
                                                                             8565 getcallerpcs(&s, pcs);
8516 for(i = 0; (c = fmt[i] & 0xff) != 0; i++){
                                                                             8566 for(i=0; i<10; i++)
8517
      if(c != '%'){
                                                                             8567
                                                                                   cprintf(" %p", pcs[i]);
8518
          consputc(c);
                                                                             8568 panicked = 1; // freeze other CPU
8519
          continue;
                                                                             8569 for(;;)
8520
                                                                             8570
                                                                                    ;
        c = fmt[++i] & Oxff;
8521
                                                                             8571 }
8522
        if(c == 0)
                                                                             8572
8523
         break;
                                                                             8573
8524
        switch(c){
                                                                             8574
8525
        case 'd':
                                                                             8575
8526
          printint(*argp++, 10, 1);
                                                                             8576
8527
         break;
                                                                             8577
8528
        case 'x':
                                                                             8578
8529
        case 'p':
                                                                             8579
8530
          printint(*argp++, 16, 0);
                                                                             8580
8531
          break;
                                                                             8581
8532
        case 's':
                                                                             8582
8533
         if((s = (char*)*argp++) == 0)
                                                                              8583
8534
          s = "(null)";
                                                                             8584
8535
          for(; *s; s++)
                                                                             8585
8536
          consputc(*s);
                                                                              8586
8537
          break;
                                                                             8587
8538
        case '%':
                                                                             8588
8539
          consputc('%');
                                                                             8589
8540
          break;
                                                                             8590
8541
                                                                             8591
        default:
8542
         // Print unknown % sequence to draw attention.
                                                                             8592
8543
          consputc('%');
                                                                             8593
8544
          consputc(c);
                                                                             8594
8545
          break;
                                                                             8595
8546
                                                                             8596
8547 }
                                                                             8597
8548
                                                                              8598
8549
                                                                             8599
```

```
8600 #define BACKSPACE 0x100
8601 #define CRTPORT 0x3d4
8602 static ushort *crt = (ushort*)P2V(0xb8000); // CGA memory
8603
8604 static void
8605 cgaputc(int c)
8606 {
8607 int pos;
8608
8609 // Cursor position: col + 80*row.
8610 outb(CRTPORT, 14);
8611 pos = inb(CRTPORT+1) << 8;
8612 outb(CRTPORT, 15);
8613 pos |= inb(CRTPORT+1);
8614
8615 if(c == ' \ n')
8616
      pos += 80 - pos%80;
8617 else if(c == BACKSPACE){
8618
      if(pos > 0) --pos;
8619 } else
        crt[pos++] = (c&0xff) \mid 0x0700; // black on white
8620
8621
8622 if(pos < 0 || pos > 25*80)
8623
       panic("pos under/overflow");
8624
8625 if((pos/80) >= 24){ // Scroll up.
8626
        memmove(crt, crt+80, sizeof(crt[0])*23*80);
8627
        pos -= 80;
8628
        memset(crt+pos, 0, sizeof(crt[0])*(24*80 - pos));
8629 }
8630
8631 outb(CRTPORT, 14);
8632 outb(CRTPORT+1, pos>>8);
8633 outb(CRTPORT, 15);
8634 outb(CRTPORT+1, pos);
8635 crt[pos] = ' ' | 0x0700;
8636 }
8637
8638
8639
8640
8641
8642
8643
8644
8645
8646
8647
8648
8649
```

```
8650 void
8651 consputc(int c)
8652 {
8653 if(panicked){
8654
        cli();
8655
      for(;;)
8656
          ;
8657 }
8658
8659 if(c == BACKSPACE){
8660
        uartputc('\b'); uartputc(' '); uartputc('\b');
8661 } else
8662
       uartputc(c);
8663 cgaputc(c);
8664 }
8665
8666 #define INPUT_BUF 128
8667 struct {
8668 char buf[INPUT_BUF];
8669 uint r; // Read index
8670 uint w; // Write index
8671 uint e; // Edit index
8672 } input;
8673
8674 #define C(x) ((x)-'@') // Control-x
8675
8676 void
8677 consoleintr(int (*getc)(void))
8678 {
8679 int c, doprocdump = 0;
8680
8681 acquire(&cons.lock);
8682 while((c = qetc()) >= 0){
8683
        switch(c){
8684
        case C('P'): // Process listing.
8685
          doprocdump = 1;  // procdump() locks cons.lock indirectly; invoke late
8686
          break;
8687
        case C('U'): // Kill line.
8688
          while(input.e != input.w &&
8689
                input.buf[(input.e-1) % INPUT_BUF] != '\n'){
8690
            input.e--;
8691
            consputc(BACKSPACE);
8692
8693
          break;
8694
        case C('H'): case '\x7f': // Backspace
8695
          if(input.e != input.w){
8696
            input.e--;
8697
            consputc(BACKSPACE);
8698
8699
          break;
```

```
8700
        default:
                                                                               8750 }
8701
          if(c != 0 && input.e-input.r < INPUT BUF){</pre>
                                                                               8751 release(&cons.lock);
            c = (c == '\r') ? '\n' : c;
8702
                                                                               8752 ilock(ip);
            input.buf[input.e++ % INPUT_BUF] = c;
8703
                                                                               8753
8704
            consputc(c);
                                                                               8754 return target - n;
            if(c == '\n' \mid c == C('D') \mid input.e == input.r+INPUT_BUF)
8705
                                                                               8755 }
              input.w = input.e;
8706
                                                                               8756
8707
              wakeup(&input.r);
                                                                               8757 int
8708
                                                                               8758 consolewrite(struct inode *ip, char *buf, int n)
8709
                                                                               8759 {
8710
          break;
                                                                               8760 int i;
8711
                                                                               8761
8712
                                                                               8762 iunlock(ip);
8713 release(&cons.lock);
                                                                               8763 acquire(&cons.lock);
8714 if(doprocdump) {
                                                                               8764 for(i = 0; i < n; i++)
8715
        procdump(); // now call procdump() wo. cons.lock held
                                                                               8765
                                                                                        consputc(buf[i] & 0xff);
8716 }
                                                                               8766 release(&cons.lock);
8717 }
                                                                               8767 ilock(ip);
8718
                                                                               8768
8719 int
                                                                               8769 return n;
8720 consoleread(struct inode *ip, char *dst, int n)
                                                                               8770 }
8721 {
                                                                               8771
8722 uint target;
                                                                               8772 void
8723 int c;
                                                                               8773 consoleinit(void)
                                                                               8774 {
8724
8725 iunlock(ip);
                                                                               8775 initlock(&cons.lock, "console");
8726 target = n;
                                                                               8776
8727 acquire(&cons.lock);
                                                                               8777 devsw[CONSOLE].write = consolewrite;
8728 while(n > 0){
                                                                               8778 devsw[CONSOLE].read = consoleread;
8729
        while(input.r == input.w){
                                                                               8779 cons.locking = 1;
8730
         if(proc->killed){
                                                                               8780
8731
            release(&cons.lock);
                                                                               8781 picenable(IRO KBD);
8732
            ilock(ip);
                                                                               8782 ioapicenable(IRQ_KBD, 0);
                                                                               8783 }
8733
            return -1;
8734
                                                                               8784
8735
          sleep(&input.r, &cons.lock);
                                                                               8785
8736
                                                                               8786
        c = input.buf[input.r++ % INPUT_BUF];
8737
                                                                               8787
8738
        if(c == C('D')) \{ // EOF
                                                                               8788
8739
          if(n < target){
                                                                               8789
8740
          // Save ^D for next time, to make sure
                                                                               8790
8741
           // caller gets a 0-byte result.
                                                                               8791
8742
            input.r--;
                                                                               8792
8743
                                                                               8793
8744
                                                                               8794
          break;
8745
                                                                               8795
8746
        *dst++ = c;
                                                                               8796
8747
                                                                               8797
        --n;
8748
        if(c == ' \n')
                                                                               8798
8749
                                                                               8799
          break;
```

```
8800 // Intel 8253/8254/82C54 Programmable Interval Timer (PIT).
                                                                               8850 // Intel 8250 serial port (UART).
8801 // Only used on uniprocessors;
                                                                               8851
8802 // SMP machines use the local APIC timer.
                                                                               8852 #include "types.h"
                                                                               8853 #include "defs.h"
8803
8804 #include "types.h"
                                                                               8854 #include "param.h"
8805 #include "defs.h"
                                                                               8855 #include "traps.h"
                                                                               8856 #include "spinlock.h"
8806 #include "traps.h"
8807 #include "x86.h"
                                                                               8857 #include "fs.h"
                                                                               8858 #include "file.h"
8808
                                                                               8859 #include "mmu.h"
8809 #define IO_TIMER1
                            0 \times 040
                                         // 8253 Timer #1
8810
                                                                               8860 #include "proc.h"
8811 // Frequency of all three count-down timers;
                                                                               8861 #include "x86.h"
8812 // (TIMER_FREQ/freq) is the appropriate count
                                                                               8862
8813 // to generate a frequency of freq Hz.
                                                                               8863 #define COM1
                                                                                                   0x3f8
8814
                                                                               8864
8815 #define TIMER_FREQ
                                                                               8865 static int uart; // is there a uart?
                            1193182
8816 #define TIMER_DIV(x) ((TIMER_FREQ+(x)/2)/(x))
                                                                               8866
8817
                                                                               8867 void
8818 #define TIMER MODE
                            (IO_TIMER1 + 3) // timer mode port
                                                                               8868 uartinit(void)
8819 #define TIMER_SEL0
                            0x00 // select counter 0
                                                                               8869 {
8820 #define TIMER RATEGEN 0x04 // mode 2, rate generator
                                                                               8870 char *p;
8821 #define TIMER 16BIT
                            0x30 // r/w counter 16 bits, LSB first
                                                                               8871
8822
                                                                               8872 // Turn off the FIFO
8823 void
                                                                               8873 outb(COM1+2, 0);
8824 timerinit(void)
                                                                               8874
8825 {
                                                                               8875 // 9600 baud, 8 data bits, 1 stop bit, parity off.
8826 // Interrupt 100 times/sec.
                                                                               8876 outb(COM1+3, 0x80); // Unlock divisor
8827 outb(TIMER_MODE, TIMER_SELO | TIMER_RATEGEN | TIMER_16BIT);
                                                                               8877 outb(COM1+0, 115200/9600);
8828 outb(IO TIMER1, TIMER DIV(100) % 256);
                                                                               8878 outb(COM1+1, 0);
8829 outb(IO_TIMER1, TIMER_DIV(100) / 256);
                                                                               8879 outb(COM1+3, 0x03); // Lock divisor, 8 data bits.
8830 picenable(IRQ_TIMER);
                                                                               8880 outb(COM1+4, 0);
8831 }
                                                                               8881 outb(COM1+1, 0x01); // Enable receive interrupts.
8832
                                                                               8882
8833
                                                                               8883 // If status is OxFF, no serial port.
8834
                                                                               8884 if(inb(COM1+5) == 0xFF)
8835
                                                                               8885
                                                                                     return;
8836
                                                                               8886 \quad uart = 1;
8837
                                                                               8887
8838
                                                                               8888 // Acknowledge pre-existing interrupt conditions;
8839
                                                                               8889 // enable interrupts.
8840
                                                                               8890 inb(COM1+2);
8841
                                                                               8891 inb(COM1+0);
8842
                                                                               8892 picenable(IRO COM1);
8843
                                                                               8893 ioapicenable(IRO COM1, 0);
8844
                                                                               8894
8845
                                                                               8895 // Announce that we're here.
8846
                                                                               8896 for(p="xv6...\n"; *p; p++)
8847
                                                                                       uartputc(*p);
                                                                               8897
8848
                                                                               8898 }
8849
                                                                               8899
```

Sheet 88 Sheet 88

Sheet 89 Sheet 89

9000	<pre>#include "syscall.h"</pre>
9001	<pre>#include "traps.h"</pre>
9002	
9003	<pre>#define SYSCALL(name) \</pre>
9004	.globl name; \
9005	name: \
9006	movl \$SYS_ ## name, %eax; \
9007	int \$T_SYSCALL; \
9008	ret
9009	
9010	SYSCALL(fork)
9011	SYSCALL(exit)
9012	
9013	SYSCALL(pipe)
9014	
9015	SYSCALL(write)
9016	SYSCALL(close)
9017	SYSCALL(kill)
9018	SYSCALL(exec)
9019	SYSCALL(open)
9020	
9021	SYSCALL(unlink)
9022	
9023	SYSCALL(link)
9024	
9025	SYSCALL(chdir)
9026	SYSCALL(dup)
9027	SYSCALL(getpid)
9028	SYSCALL(sbrk)
9029	SYSCALL(sleep)
9030	SYSCALL(uptime)
9031	SYSCALL(halt)
9032	
9033	SYSCALL(getuid)
9034	_
9035	SYSCALL(getppid)
9036	SYSCALL(setuid)
9037	SYSCALL(setgid)
9038	SYSCALL(getprocs)
9039	SYSCALL(getpriority)
9040	SYSCALL(setpriority);
9041	SISCALD(Secpriority)/
9042	
9043	
9044	
9044	
9045	
9047 9048	
9049	

```
9050 // init: The initial user-level program
9051
9052 #include "types.h"
9053 #include "stat.h"
9054 #include "user.h"
9055 #include "fcntl.h"
9056
9057 char *argv[] = { "sh", 0 };
9058
9059 int
9060 main(void)
9061 {
9062 int pid, wpid;
9063 if(open("console", O_RDWR) < 0){
       mknod("console", 1, 1);
9065
        open("console", O_RDWR);
9066 }
9067 dup(0); // stdout
9068 dup(0); // stderr
9069
9070 for(;;){
9071
        printf(1, "init: starting sh\n");
9072
        pid = fork();
9073
        if(pid < 0){
9074
          printf(1, "init: fork failed\n");
9075
          exit();
9076
9077
        if(pid == 0){
9078
          exec("sh", argv);
9079
          printf(1, "init: exec sh failed\n");
9080
          exit();
9081
9082
        while((wpid=wait()) >= 0 && wpid != pid)
          printf(1, "zombie!\n");
9083
9084 }
9085 }
9086
9087
9088
9089
9090
9091
9092
9093
9094
9095
9096
9097
9098
9099
```

Sheet 90 Sheet 90

9141 struct listcmd {

9143 struct cmd *left;

9144 struct cmd *right;

9142 int type;

9145 };

9146

9147

9148

9149

```
9150 struct backcmd {
9151 int type;
9152 struct cmd *cmd;
9153 };
9154
9155 int fork1(void); // Fork but panics on failure.
9156 void panic(char*);
9157 struct cmd *parsecmd(char*);
9158
9159 // Execute cmd. Never returns.
9160 void
9161 runcmd(struct cmd *cmd)
9162 {
9163 int p[2];
9164 struct backcmd *bcmd;
9165 struct execomd *ecmd;
9166 struct listcmd *lcmd;
9167 struct pipecmd *pcmd;
9168 struct redircmd *rcmd;
9169
9170 	 if(cmd == 0)
9171
       exit();
9172
9173 switch(cmd->type){
9174 default:
9175
        panic("runcmd");
9176
9177 case EXEC:
9178
       ecmd = (struct execcmd*)cmd;
9179
       if(ecmd->argv[0] == 0)
9180
          exit();
9181
        exec(ecmd->argv[0], ecmd->argv);
9182
        printf(2, "exec %s failed\n", ecmd->argv[0]);
9183
        break;
9184
9185 case REDIR:
9186
       rcmd = (struct redircmd*)cmd;
9187
        close(rcmd->fd);
9188
        if(open(rcmd->file, rcmd->mode) < 0){</pre>
9189
          printf(2, "open %s failed\n", rcmd->file);
9190
          exit();
9191
9192
        runcmd(rcmd->cmd);
9193
        break;
9194
9195 case LIST:
9196
       lcmd = (struct listcmd*)cmd;
9197
        if(fork1() == 0)
9198
        runcmd(lcmd->left);
9199
        wait();
```

Sheet 91 Sheet 91

```
9200
        runcmd(lcmd->right);
9201
        break;
9202
9203
      case PIPE:
9204
        pcmd = (struct pipecmd*)cmd;
9205
        if(pipe(p) < 0)
9206
          panic("pipe");
9207
        if(fork1() == 0){
9208
          close(1);
9209
          dup(p[1]);
9210
          close(p[0]);
9211
          close(p[1]);
9212
          runcmd(pcmd->left);
9213
9214
        if(fork1() == 0){
9215
          close(0);
9216
          dup(p[0]);
9217
          close(p[0]);
9218
          close(p[1]);
9219
          runcmd(pcmd->right);
9220
9221
        close(p[0]);
9222
        close(p[1]);
9223
        wait();
9224
        wait();
9225
        break;
9226
9227 case BACK:
9228
        bcmd = (struct backcmd*)cmd;
9229
        if(fork1() == 0)
9230
          runcmd(bcmd->cmd);
9231
        break;
9232 }
9233 exit();
9234 }
9235
9236 int
9237 getcmd(char *buf, int nbuf)
9238 {
9239 printf(2, "$ ");
9240 memset(buf, 0, nbuf);
9241 gets(buf, nbuf);
9242 if(buf[0] == 0) // EOF
9243
      return -1;
9244 return 0;
9245 }
9246
9247
9248
9249
```

```
9250 // ***** processing for shell builtins begins here *****
9251
9252 int
9253 strncmp(const char *p, const char *q, uint n)
        while(n > 0 && *p && *p == *q)
9255
9256
         n--, p++, q++;
9257
        if(n == 0)
9258
         return 0;
9259
        return (uchar)*p - (uchar)*g;
9260 }
9261
9262 int
9263 makeint(char *p)
9264 {
9265 int val = 0;
9266
9267 while ((*p >= '0') && (*p <= '9')) {
9268
       val = 10*val + (*p-'0');
9269
        ++p;
9270 }
9271 return val;
9272 }
9273
9274 int
9275 setbuiltin(char *p)
9276 {
9277 int i;
9278
9279 p += strlen("_set");
9280 while (strncmp(p, "", 1) == 0) p++; // chomp spaces
9281 if (strncmp("uid", p, 3) == 0) {
9282    p += strlen("uid");
9283
        while (strncmp(p, "", 1) == 0) p++; // chomp spaces
9284
        i = makeint(p); // ugly
9285
        return (setuid(i));
9286 } else
9287 if (strncmp("gid", p, 3) == 0) {
9288
       p += strlen("gid");
9289
        while (strncmp(p, "", 1) == 0) p++; // chomp spaces
9290
        i = makeint(p); // ugly
9291
        return (setgid(i));
9292 }
9293 printf(2, "Invalid _set parameter\n");
9294 return -1;
9295 }
9296
9297
9298
9299
```

May 16 22:03 2016 xy6/sh.c Page 6

Sheet 93 Sheet 93

May 16 22:03 2016 xy6/sh.c Page 5

```
9400 int
9401 fork1(void)
9402 {
9403 int pid;
9404
9405 pid = fork();
9406 if(pid == -1)
      panic("fork");
9407
9408 return pid;
9409 }
9410
9411
9412
9413
9414
9415
9416
9417
9418
9419
9420
9421
9422
9423
9424
9425
9426
9427
9428
9429
9430
9431
9432
9433
9434
9435
9436
9437
9438
9439
9440
9441
9442
9443
9444
9445
9446
9447
9448
9449
```

```
9450 // Constructors
9451
9452 struct cmd*
9453 execcmd(void)
9454 {
9455 struct execomd *cmd;
9456
9457 cmd = malloc(sizeof(*cmd));
9458 memset(cmd, 0, sizeof(*cmd));
9459 cmd->type = EXEC;
9460 return (struct cmd*)cmd;
9461 }
9462
9463 struct cmd*
9464 redircmd(struct cmd *subcmd, char *file, char *efile, int mode, int fd)
9465 {
9466 struct redircmd *cmd;
9467
9468 cmd = malloc(sizeof(*cmd));
9469 memset(cmd, 0, sizeof(*cmd));
9470 cmd->type = REDIR;
9471 cmd->cmd = subcmd;
9472 cmd->file = file;
9473 cmd->efile = efile;
9474 cmd->mode = mode;
9475 \quad cmd \rightarrow fd = fd;
9476 return (struct cmd*)cmd;
9477 }
9478
9479 struct cmd*
9480 pipecmd(struct cmd *left, struct cmd *right)
9481 {
9482 struct pipecmd *cmd;
9483
9484 cmd = malloc(sizeof(*cmd));
9485 memset(cmd, 0, sizeof(*cmd));
9486 cmd->type = PIPE;
9487 cmd->left = left;
9488 cmd->right = right;
9489 return (struct cmd*)cmd;
9490 }
9491
9492
9493
9494
9495
9496
9497
9498
9499
```

Sheet 94 Sheet 94

```
9500 struct cmd*
9501 listcmd(struct cmd *left, struct cmd *right)
9502 {
9503 struct listcmd *cmd;
9504
9505 cmd = malloc(sizeof(*cmd));
9506 memset(cmd, 0, sizeof(*cmd));
9507 cmd->type = LIST;
9508 cmd->left = left;
9509 cmd->right = right;
9510 return (struct cmd*)cmd;
9511 }
9512
9513 struct cmd*
9514 backcmd(struct cmd *subcmd)
9515 {
9516 struct backcmd *cmd;
9517
9518 cmd = malloc(sizeof(*cmd));
9519 memset(cmd, 0, sizeof(*cmd));
9520 cmd->type = BACK;
9521 cmd->cmd = subcmd;
9522 return (struct cmd*)cmd;
9523 }
9524
9525
9526
9527
9528
9529
9530
9531
9532
9533
9534
9535
9536
9537
9538
9539
9540
9541
9542
9543
9544
9545
9546
9547
9548
9549
```

```
9550 // Parsing
9551
9552 char whitespace[] = " t\r\n\v";
9553 char symbols[] = "<|>&;()";
9554
9555 int
9556 gettoken(char **ps, char *es, char **q, char **eq)
9558 char *s;
9559 int ret;
9560
9561 s = *ps;
9562 while(s < es && strchr(whitespace, *s))
9563
        s++;
9564 if(a)
9565
        *q = s;
9566 ret = *s;
9567 switch(*s){
9568 case 0:
9569
       break;
9570 case '|':
9571 case '(':
9572 case ')':
9573 case ';':
9574 case '&':
9575 case '<':
9576
      s++;
9577
     break;
9578 case '>':
9579 s++;
9580 if(*s == '>'){
9581
       ret = '+';
9582
          s++;
9583
9584
        break;
9585 default:
9586
       ret = 'a';
9587
        while(s < es && !strchr(whitespace, *s) && !strchr(symbols, *s))</pre>
9588
          s++;
9589
        break;
9590 }
9591 if(eq)
9592
        *eq = s;
9593
9594 while(s < es && strchr(whitespace, *s))
9595
       s++;
9596 *ps = s;
9597 return ret;
9598 }
9599
```

```
9600 int
9601 peek(char **ps, char *es, char *toks)
9602 {
9603 char *s;
9604
9605 s = *ps;
9606 while(s < es && strchr(whitespace, *s))
9607
      s++;
9608 *ps = s;
9609 return *s && strchr(toks, *s);
9610 }
9611
9612 struct cmd *parseline(char**, char*);
9613 struct cmd *parsepipe(char**, char*);
9614 struct cmd *parseexec(char**, char*);
9615 struct cmd *nulterminate(struct cmd*);
9616
9617 struct cmd*
9618 parsecmd(char *s)
9619 {
9620 char *es;
9621 struct cmd *cmd;
9622
9623 es = s + strlen(s);
9624 cmd = parseline(&s, es);
9625 peek(&s, es, "");
9626 if(s != es){
9627
      printf(2, "leftovers: %s\n", s);
9628
      panic("syntax");
9629 }
9630 nulterminate(cmd);
9631 return cmd;
9632 }
9633
9634 struct cmd*
9635 parseline(char **ps, char *es)
9636 {
9637 struct cmd *cmd;
9638
9639 cmd = parsepipe(ps, es);
9640 while(peek(ps, es, "&")){
      gettoken(ps, es, 0, 0);
9641
9642
      cmd = backcmd(cmd);
9643 }
9644 if(peek(ps, es, ";")){
9645
      gettoken(ps, es, 0, 0);
9646
      cmd = listcmd(cmd, parseline(ps, es));
9647 }
9648 return cmd;
9649 }
```

```
9650 struct cmd*
9651 parsepipe(char **ps, char *es)
9652 {
9653 struct cmd *cmd;
9654
9655 cmd = parseexec(ps, es);
9656 if(peek(ps, es, "|")){
9657 gettoken(ps, es, 0, 0);
9658 cmd = pipecmd(cmd, parsepipe(ps, es));
9659 }
9660 return cmd;
9661 }
9662
9663 struct cmd*
9664 parseredirs(struct cmd *cmd, char **ps, char *es)
9665 {
9666 int tok;
9667 char *q, *eq;
9668
9669 while(peek(ps, es, "<>")){
9670 tok = gettoken(ps, es, 0, 0);
9671
       if(gettoken(ps, es, &q, &eq) != 'a')
       panic("missing file for redirection");
9672
9673
        switch(tok){
9674 case '<':
9675
          cmd = redircmd(cmd, q, eq, O_RDONLY, 0);
9676
         break;
9677 case '>':
9678
         cmd = redircmd(cmd, q, eq, O_WRONLY|O_CREATE, 1);
9679
         break;
9680 case '+': // >>
9681
          cmd = redircmd(cmd, q, eq, O_WRONLY|O_CREATE, 1);
9682
          break;
9683
9684 }
9685 return cmd;
9686 }
9687
9688
9689
9690
9691
9692
9693
9694
9695
9696
9697
9698
9699
```

```
9700 struct cmd*
9701 parseblock(char **ps, char *es)
9702 {
9703 struct cmd *cmd;
9704
9705 if(!peek(ps, es, "("))
9706 panic("parseblock");
9707 gettoken(ps, es, 0, 0);
9708 cmd = parseline(ps, es);
9709 if(!peek(ps, es, ")"))
9710
      panic("syntax - missing )");
9711 gettoken(ps, es, 0, 0);
9712 cmd = parseredirs(cmd, ps, es);
9713 return cmd;
9714 }
9715
9716 struct cmd*
9717 parseexec(char **ps, char *es)
9718 {
9719 char *q, *eq;
9720 int tok, argc;
9721 struct execomd *cmd;
9722 struct cmd *ret;
9723
9724 if(peek(ps, es, "("))
9725
       return parseblock(ps, es);
9726
9727 ret = execcmd();
9728 cmd = (struct execcmd*)ret;
9729
9730 argc = 0;
9731 ret = parseredirs(ret, ps, es);
9732 while(!peek(ps, es, "|)&;")){
9733
        if((tok=gettoken(ps, es, &q, &eq)) == 0)
9734
          break;
9735
        if(tok != 'a')
9736
          panic("syntax");
9737
        cmd->arqv[arqc] = q;
9738
        cmd->eargv[argc] = eq;
9739
        arqc++;
9740
        if(argc >= MAXARGS)
9741
          panic("too many args");
9742
        ret = parseredirs(ret, ps, es);
9743 }
9744 cmd->argv[argc] = 0;
9745 cmd \rightarrow earqv[arqc] = 0;
9746 return ret;
9747 }
9748
9749
```

```
9750 // NUL-terminate all the counted strings.
9751 struct cmd*
9752 nulterminate(struct cmd *cmd)
9753 {
9754 int i;
9755 struct backemd *bcmd;
9756 struct execomd *ecmd;
9757 struct listcmd *lcmd;
9758 struct pipecmd *pcmd;
9759 struct redircmd *rcmd;
9760
9761 	 if(cmd == 0)
9762
      return 0;
9763
9764 switch(cmd->type){
9765 case EXEC:
9766
        ecmd = (struct execcmd*)cmd;
9767
        for(i=0; ecmd->argv[i]; i++)
9768
          *ecmd->eargv[i] = 0;
9769
        break;
9770
9771 case REDIR:
        rcmd = (struct redircmd*)cmd;
9773
        nulterminate(rcmd->cmd);
9774
       *rcmd->efile = 0;
9775
        break;
9776
9777 case PIPE:
9778
        pcmd = (struct pipecmd*)cmd;
9779
        nulterminate(pcmd->left);
9780
        nulterminate(pcmd->right);
9781
        break;
9782
9783 case LIST:
        lcmd = (struct listcmd*)cmd;
9784
9785
        nulterminate(lcmd->left);
9786
        nulterminate(lcmd->right);
9787
        break;
9788
9789 case BACK:
9790
       bcmd = (struct backcmd*)cmd;
9791
        nulterminate(bcmd->cmd);
9792
      break;
9793 }
9794 return cmd;
9795 }
9796
9797
9798
9799
```

```
9800 #include "asm.h"
                                                                                      # Complete transition to 32-bit protected mode by using long jmp
9801 #include "memlayout.h"
                                                                                      # to reload %cs and %eip. The segment descriptors are set up with no
9802 #include "mmu.h"
                                                                               9852
                                                                                      # translation, so that the mapping is still the identity mapping.
                                                                                            $(SEG_KCODE<<3), $start32
9803
                                                                               9853 ljmp
9804 # Start the first CPU: switch to 32-bit protected mode, jump into C.
                                                                               9854
9805 # The BIOS loads this code from the first sector of the hard disk into
                                                                               9855 .code32 # Tell assembler to generate 32-bit code now.
9806 # memory at physical address 0x7c00 and starts executing in real mode
                                                                               9856 start32:
9807 # with %cs=0 %ip=7c00.
                                                                               9857
                                                                                      # Set up the protected-mode data segment registers
9808
                                                                               9858
                                                                                     movw
                                                                                              $(SEG_KDATA<<3), %ax # Our data segment selector
9809 .code16
                                  # Assemble for 16-bit mode
                                                                               9859
                                                                                              %ax, %ds
                                                                                                                     # -> DS: Data Segment
                                                                                      movw
9810 .globl start
                                                                               9860
                                                                                      movw
                                                                                              %ax, %es
                                                                                                                     # -> ES: Extra Segment
9811 start:
                                                                                              %ax, %ss
                                                                                                                     # -> SS: Stack Segment
                                                                               9861
                                                                                      movw
9812 cli
                                  # BIOS enabled interrupts; disable
                                                                               9862
                                                                                              $0, %ax
                                                                                                                     # Zero segments not ready for use
                                                                                      movw
9813
                                                                               9863
                                                                                      movw
                                                                                              %ax, %fs
                                                                                                                     # -> FS
9814 # Zero data segment registers DS, ES, and SS.
                                                                               9864
                                                                                              %ax, %qs
                                                                                                                     # -> GS
                                                                                      movw
9815 xorw
              %ax,%ax
                                  # Set %ax to zero
                                                                               9865
9816 movw
              %ax.%ds
                                  # -> Data Segment
                                                                               9866
                                                                                      # Set up the stack pointer and call into C.
9817 movw
              %ax,%es
                                  # -> Extra Segment
                                                                               9867
                                                                                      movl
                                                                                              $start, %esp
9818 movw
              %ax,%ss
                                  # -> Stack Segment
                                                                               9868 call
                                                                                              bootmain
9819
                                                                               9869
9820 # Physical address line A20 is tied to zero so that the first PCs
                                                                               9870
                                                                                     # If bootmain returns (it shouldn't), trigger a Bochs
9821 # with 2 MB would run software that assumed 1 MB. Undo that.
                                                                               9871
                                                                                      # breakpoint if running under Bochs, then loop.
9822 seta20.1:
                                                                               9872
                                                                                     movw
                                                                                              $0x8a00, %ax
                                                                                                                     # 0x8a00 -> port 0x8a00
9823 inb
              $0x64,%al
                                      # Wait for not busy
                                                                               9873 movw
                                                                                              %ax, %dx
9824 testb $0x2,%al
                                                                               9874 outw
                                                                                              %ax, %dx
9825
              seta20.1
                                                                               9875
                                                                                              $0x8ae0, %ax
                                                                                                                     # 0x8ae0 -> port 0x8a00
      jnz
                                                                                      movw
9826
                                                                               9876 outw
                                                                                              %ax, %dx
9827 movb
              $0xd1,%al
                                      # 0xd1 -> port 0x64
                                                                               9877 spin:
9828 out.b
              %al,$0x64
                                                                                    qmj
                                                                               9878
                                                                                              spin
9829
                                                                               9879
9830 seta20.2:
                                                                               9880 # Bootstrap GDT
9831 inb
              $0x64,%al
                                      # Wait for not busy
                                                                               9881 .p2align 2
                                                                                                                             # force 4 byte alignment
9832 testb $0x2,%al
                                                                               9882 gdt:
9833 jnz
              seta20.2
                                                                               9883 SEG NULLASM
                                                                                                                             # null seg
9834
                                                                               9884 SEG_ASM(STA_X|STA_R, 0x0, 0xffffffff)
                                                                                                                             # code seq
9835 movb
              $0xdf,%al
                                      # 0xdf -> port 0x60
                                                                               9885 SEG_ASM(STA_W, 0x0, 0xffffffff)
                                                                                                                             # data seg
9836 out.b
              %al,$0x60
                                                                               9886
9837
                                                                               9887 qdtdesc:
9838 # Switch from real to protected mode. Use a bootstrap GDT that makes
                                                                               9888
                                                                                      .word
                                                                                              (gdtdesc - gdt - 1)
                                                                                                                             # sizeof(qdt) - 1
      # virtual addresses map directly to physical addresses so that the
                                                                               9889
                                                                                      .long
                                                                                             qdt
                                                                                                                             # address qdt
9840 # effective memory map doesn't change during the transition.
                                                                               9890
              adtdesc
                                                                               9891
9841 ladt
9842 movl
              %cr0, %eax
                                                                               9892
9843 orl
              $CRO PE, %eax
                                                                               9893
              %eax, %cr0
9844 movl
                                                                               9894
9845
                                                                               9895
9846
                                                                               9896
9847
                                                                               9897
9848
                                                                               9898
9849
                                                                               9899
```

Sheet 98 Sheet 98

```
9900 // Boot loader.
                                                                               9950 void
9901 //
                                                                               9951 waitdisk(void)
9902 // Part of the boot block, along with bootasm.S, which calls bootmain().
                                                                               9952 {
9903 // bootasm.S has put the processor into protected 32-bit mode.
                                                                               9953 // Wait for disk ready.
9904 // bootmain() loads an ELF kernel image from the disk starting at
                                                                               9954 while((inb(0x1F7) & 0xC0) != 0x40)
9905 // sector 1 and then jumps to the kernel entry routine.
                                                                               9955
9906
                                                                               9956 }
                                                                               9957
9907 #include "types.h"
9908 #include "elf.h"
                                                                               9958 // Read a single sector at offset into dst.
9909 #include "x86.h"
                                                                               9959 void
9910 #include "memlayout.h"
                                                                               9960 readsect(void *dst, uint offset)
9912 #define SECTSIZE 512
                                                                               9962 // Issue command.
                                                                               9963 waitdisk();
9914 void readseg(uchar*, uint, uint);
                                                                               9964 outb(0x1F2, 1); // count = 1
9915
                                                                               9965 outb(0x1F3, offset);
9916 void
                                                                               9966 outb(0x1F4, offset >> 8);
9917 bootmain(void)
                                                                               9967 outb(0x1F5, offset >> 16);
9918 {
                                                                               9968 outb(0x1F6, (offset >> 24) | 0xE0);
9919 struct elfhdr *elf;
                                                                               9969 outb(0x1F7, 0x20); // cmd 0x20 - read sectors
9920 struct proghdr *ph, *eph;
                                                                               9970
9921 void (*entry)(void);
                                                                              9971 // Read data.
9922 uchar* pa;
                                                                               9972 waitdisk();
9923
                                                                               9973 insl(0x1F0, dst, SECTSIZE/4);
                                                                               9974 }
9924 elf = (struct elfhdr*)0x10000; // scratch space
9925
                                                                               9975
                                                                               9976 // Read 'count' bytes at 'offset' from kernel into physical address 'pa'.
9926 // Read 1st page off disk
9927 readseg((uchar*)elf, 4096, 0);
                                                                               9977 // Might copy more than asked.
9928
                                                                               9978 void
9929 // Is this an ELF executable?
                                                                               9979 readseg(uchar* pa, uint count, uint offset)
9930 if(elf->magic != ELF_MAGIC)
                                                                               9980 {
9931
      return; // let bootasm.S handle error
                                                                               9981 uchar* epa;
9932
                                                                               9982
9933 // Load each program segment (ignores ph flags).
                                                                               9983 epa = pa + count;
9934 ph = (struct proghdr*)((uchar*)elf + elf->phoff);
                                                                               9984
9935 eph = ph + elf->phnum;
                                                                               9985 // Round down to sector boundary.
9936 for(; ph < eph; ph++){
                                                                               9986 pa -= offset % SECTSIZE;
9937
       pa = (uchar*)ph->paddr;
                                                                               9987
9938
        readseg(pa, ph->filesz, ph->off);
                                                                               9988 // Translate from bytes to sectors; kernel starts at sector 1.
9939
        if(ph->memsz > ph->filesz)
                                                                               9989 offset = (offset / SECTSIZE) + 1;
9940
          stosb(pa + ph->filesz, 0, ph->memsz - ph->filesz);
                                                                               9990
                                                                               9991 // If this is too slow, we could read lots of sectors at a time.
9941 }
9942
                                                                               9992 // We'd write more to memory than asked, but it doesn't matter --
9943 // Call the entry point from the ELF header.
                                                                               9993 // we load in increasing order.
                                                                               9994 for(; pa < epa; pa += SECTSIZE, offset++)
9944 // Does not return!
9945 entry = (void(*)(void))(elf->entry);
                                                                                       readsect(pa, offset);
                                                                               9995
9946 entry();
                                                                               9996 }
                                                                               9997
9947 }
9948
                                                                               9998
9949
                                                                               9999
```

Sheet 99 Sheet 99

```
10000 #include "types.h"
10001 #include "user.h"
10002 #include "date.h"
10003
10004 int
10005 printdate(struct rtcdate *r)
10006 {
10007
          if (!r) /*avoid kernel panic*/
10008 return -1;
10009
10010 printf(1,"%d :%d :%d :%d :%d :%d\n",
10011
              r->year, r->month, r->day,
10012
             r->hour, r->minute, r->second);
10013
          return 0;
10014 }
10015
10016 int
10017 main(int argc, char*argv[])
10018 {
10019
10020 struct rtcdate r;
10021 if (date(&r)){
10022 printf(1, "date failed\n");
10023 exit();
10024 }
10025 printdate(&r);
10026 exit();
10027 }
10028
10029
10030
10031
10032
10033
10034
10035
10036
10037
10038
10039
10040
10041
10042
10043
10044
10045
10046
10047
10048
10049
```

```
10050 #include "types.h"
10051 #include "user.h"
10052 #include "date.h"
10053
10054 int
10055 main(int argc ,char*argv[])
10056 {
10057 struct rtcdate t1;
10058 struct rtcdate t2;
10059 struct rtcdate tf;
10060 int rc;
10061
10062 tf.minute=0;
10063 date(&t1);
10064
10065 /*run program argv*/
10066 rc = fork();
10067
10068
10069 if(rc > 0) /*if parent function*/
10070 wait();
10071 else{
10072 exec(argv[1],argv);
10073 exit();
10074 }
10075 date (&t2);
10076 /*do the time math*/
10077
10078 if (t2.second < t1.second) {
10079 t1.second = 60-t1.second;
10080 tf.second = t1.second + t2.second;
10081 tf.minute = -1;
10082 }
10083 else
10084 tf.second = t2.second-t1.second;
10085
10086 if(t2.minute < t1.minute){
10087
          t1.minute = 60-t1.minute;
10088
          tf.minute += t1.minute + t2.minute;
10089 }
10090 else
       tf.minute += (t2.minute)-(t1.minute);
10091
10092
10093 /*print results*/
10094 printf(1, "the program ran in %d minutes and %d seconds\n",tf.minute, tf.sec
10095 exit();
10096
10097 }
10098
10099
```

```
10100 #include "types.h"
10101 #include "user.h"
10102 #include "ps.h"
10103 int
10104 main(int argc, char*argv[])
10105 {
10106 int \max = 64;
10107 struct uproc table[max];
10108 int i, j;
10109 j = getprocs(max, table);
10110 if (max > j) /*fewer processes than requested*/
10111 \max = j;
10112 for(i=0; i<max; i++){
10113 printf(1 , "%d ", table[i].pid);
10114 printf(1 , "%d ", table[i].uid);
10115 printf(1 , "%d ", table[i].gid);
10116  printf(1 , "%d ", table[i].ppid);
10117 switch(table[i].state){
10118
       case 0:
10119
               printf(1, "EMBRYO");
10120
               break;
10121
       case 1:
10122 printf(1, "SLEEPING");
10123 break;
10124
         case 2:
10125 printf(1, "RUNNABLE");
10126
              break;
10127
       case 3:
10128 printf(1, "RUNNING");
10129
         break;
10130
       case 4:
10131 printf(1, "ZOMBIE");
10132
10133 printf(1 ," %d ",table[i].priority);
10134 printf(1 ," %d ",table[i].size);
10135 printf(1 , "%s\n", table[i].name);
10136 }
10137 exit();
10138 }
10139
10140
10141
10142
10143
10144
10145
10146
10147
10148
10149
```

```
10150 #include "types.h"
10151 #include "user.h"
10152 int
10153 main(int argc, char*argv[])
10154 {
10155 int uid, gid , ppid;
10156 uid = getuid();
10157 printf(1, "Current UID is : %d\n", uid );
10158 printf(1, "Setting UID to 100\n");
10159 setuid(100);
10160 uid = getuid();
10161 printf(1 , "Current UID is : %d\n",uid);
10162 gid = getgid();
10163 printf( 1, "Current GID is : %d\n",gid);
10164 printf(1, "Setting GID to 100\n");
10165 setgid(100);
10166 gid = getgid();
10167 printf(1, "Current GID is:%d\n" ,uid);
10168 ppid = getppid();
10169 printf(1 ,"My parent process is : %d\n" , ppid);
10170 printf(1 , "Done! \n" );
10171 exit();
10172 }
10173
10174
10175
10176
10177
10178
10179
10180
10181
10182
10183
10184
10185
10186
10187
10188
10189
10190
10191
10192
10193
10194
10195
10196
10197
10198
10199
```

```
10200 #include "types.h"
10201 #include "user.h"
10202
10203 int
10204 main(int argc, char*argv[])
10205 {
10206 int tmp;
10207 int pid;
10208 pid = getpid();
10209 tmp = getpriority(pid);
10210 printf(1,"process %d priority is %d\n",pid,tmp);
10211 \text{ tmp} = 2;
10212 setpriority(pid,tmp);
10213 tmp = getpriority(pid);
10214 printf(1, "process %d priority is now %d\n",pid,tmp);
10215 exit();
10216 }
10217
10218
10219
10220
10221
10222
10223
10224
10225
10226
10227
10228
10229
10230
10231
10232
10233
10234
10235
10236
10237
10238
10239
10240
10241
10242
10243
10244
10245
10246
10247
10248
10249
```

```
10250 #include "types.h"
10251 #include "user.h"
10252
10253 // We currently have 3 priority levels
10254 #define PrioCount 3
10255 #define numChildren 10
10256
10257 void
10258 countForever(int p)
10259 {
10260 int j;
       unsigned long count = 0;
10261
10262
10263 j = getpid();
10264 p = p%PrioCount;
10265 setpriority(j, p);
10266
       printf(1, "%d: start prio %d\n", j, p);
10267
10268 while (1) {
10269
       count++;
10270
       if ((count & 0xFFFFFFF) == 0) {
10271
           p = (p+1) % PrioCount;
10272
          setpriority(j, p);
10273
           printf(1, "%d: new prio %d\n", j, p);
10274
10275
10276 }
10277
10278 int
10279 main(void)
10280 {
10281 int i, rc;
10282
10283 for (i=0; i<numChildren; i++) {
       rc = fork();
10284
10285
         if (!rc) { // child
           countForever(i);
10286
10287
10288
10289
       // what the heck, let's have the parent waste time as well!
10290 countForever(1);
       exit();
10291
10292 }
10293
10294
10295
10296
10297
10298
10299
```