

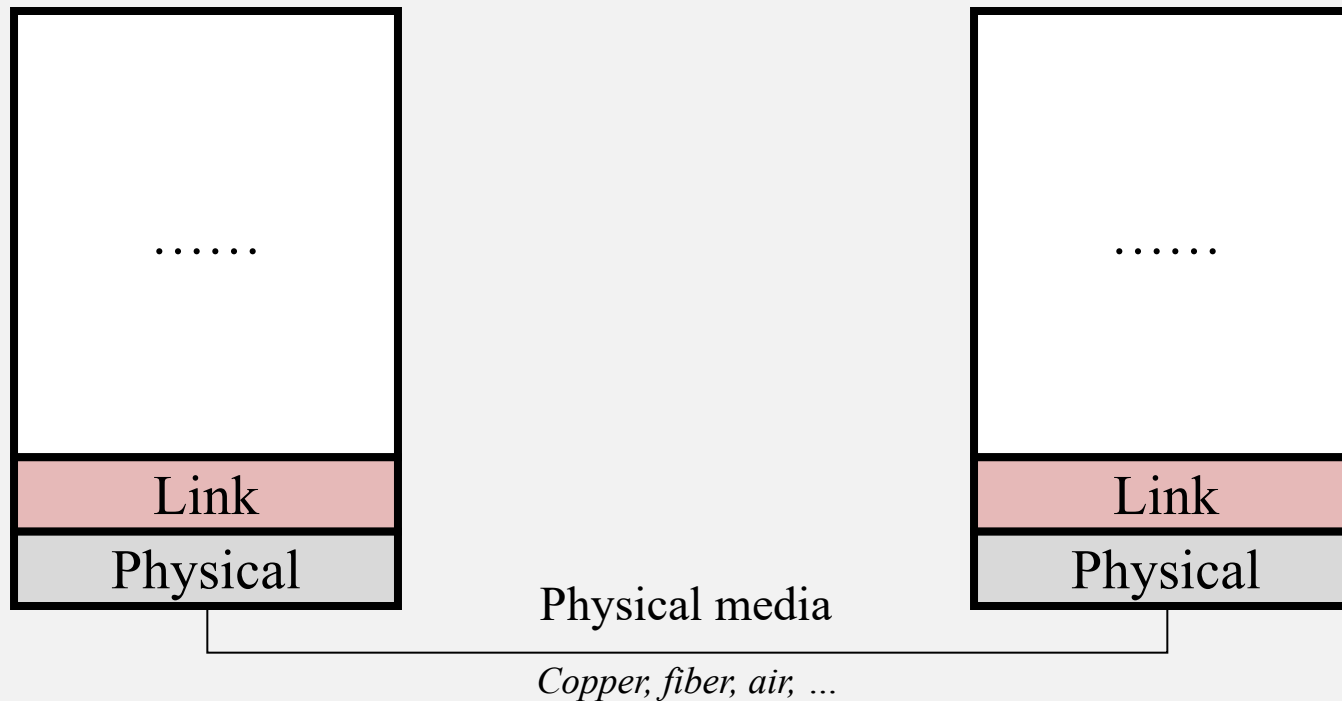


Data Link Control Layer

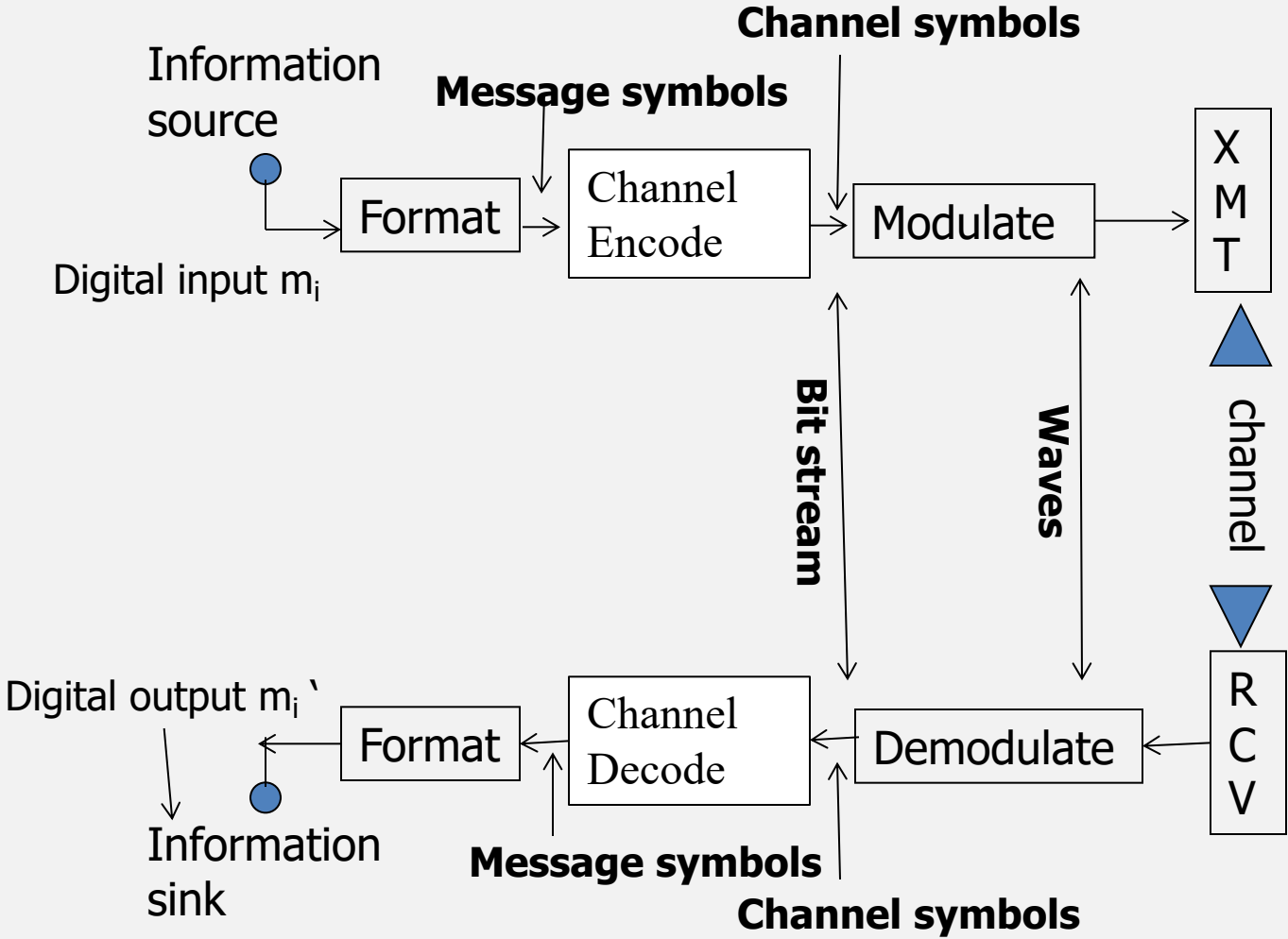
CSC/ECE 570 - Fall 2024, Section 002



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Block Diagram of a Digital Communication System





Bandwidth limited Bit rate

- Nyquist's theorem
 - Maximum bit rate = $2H \log_2 V$ bits/sec
 - H = bandwidth
 - V = number of discrete states
- Shannon's theorem
 - Maximum bit rate = $H \log_2 (1 + S/N)$ bits/sec
 - Note on application
 - SNR in Shannon's theorem - ratio of power content (P_S/P_N)
 - Usual unit of SNR - dB, a logarithmic unit
 - $\text{dB} = 10 \log_{10} (P_S/P_N)$

Question



Shannon Capacity

[Show Correct Answer](#)

Based on the Shannon capacity formula, a client device sending application data to a server via a direct wireless link can always achieve $H \log_2 (1 + S/N)$ bits/sec of application throughput, where H is the bandwidth, S is the signal strength, and N is the background noise.

A

TRUE

B

FALSE



Question (consider Shannon Capacity)

Which of the following affect the theoretical limit of information that can be transmitted via a channel?

- A Bandwidth
- B Modulation
- C Noise
- D Interference
- E Overhead
- F Protocol
- G Distance



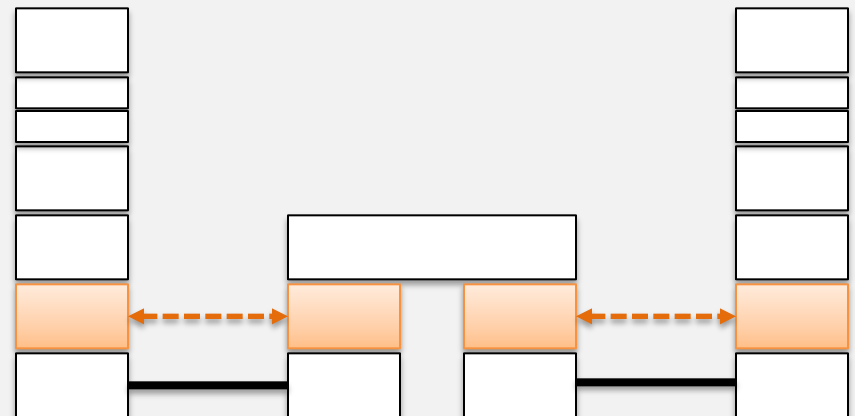
DLC - General Concepts





Data Link Layer Perspective

- Second of the OSI Model
- Utilizes (unreliable) bitpipe from PHY layer
- Provides service to Networking layer
- Equipment: switch
- PDUs: “Frames”





Data Link Layer Services

- Services Provided to the Network Layer
 - **Framing**
 - Logical bit groupings (header/trailers) – more use at higher layers
 - **Error Control**
 - Error control overlaps with physical layer (bit errors 0->1)
 - DLC – retransmission strategies
 - **Flow Control**
 - Matches dissimilar endpoint processing speeds (overwhelm)
 - Slow receiver should not be swamped by fast sender
 - Also a backward error correction mechanism
 - Optionally, mediate contention for shared medium
 - Such as Ethernet

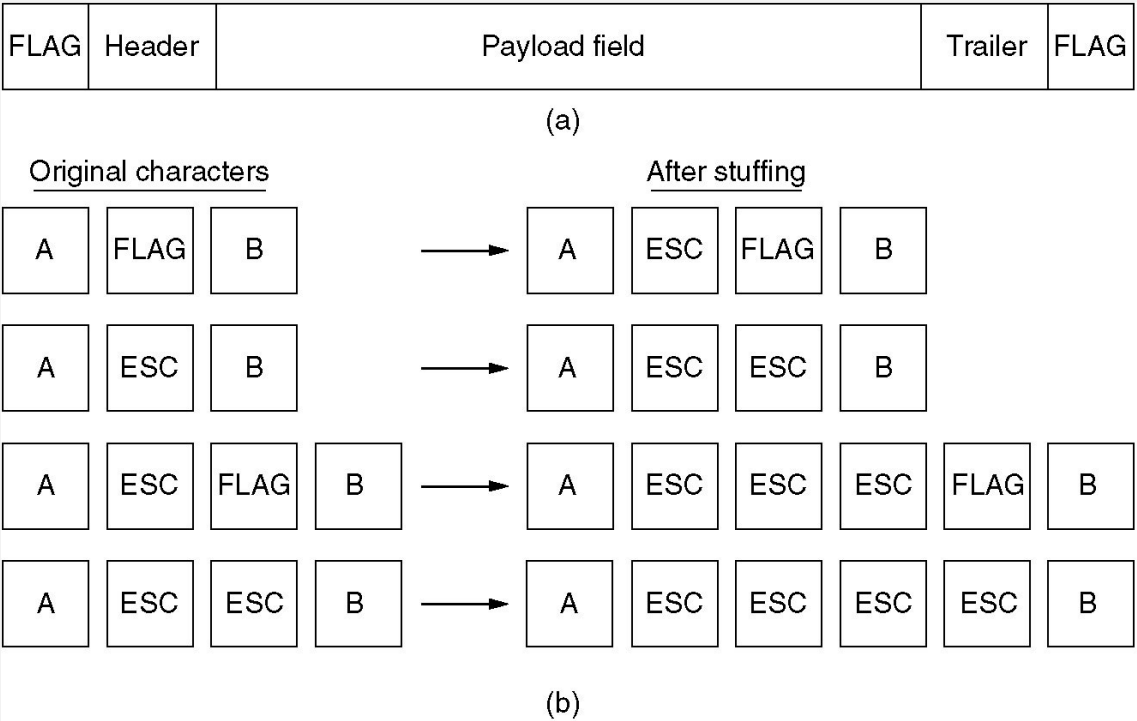


Utilizing Unreliable Bitpipe - Framing

- *“Sometimes sender sends bits, sometime not”*
- How does the receiver (receiving DLC) know when bits are being received, and when not?
 - Must create own (DLC layer) convention (protocol) about transmission of bits
 - Start with **preamble**, end with **conclusion**
 - Creates logical groups at DLC layer - “frames”
 - Also serves as logical groups to encapsulate higher layer PDUs
 - Higher layers want service in multiple-byte chunks
 - Logical units determined by the logic of the higher layers
- **Frame delineation** – 1) must start with (and end with) an easily recognizable set of bits 2) that are unlikely to arise randomly due to noise

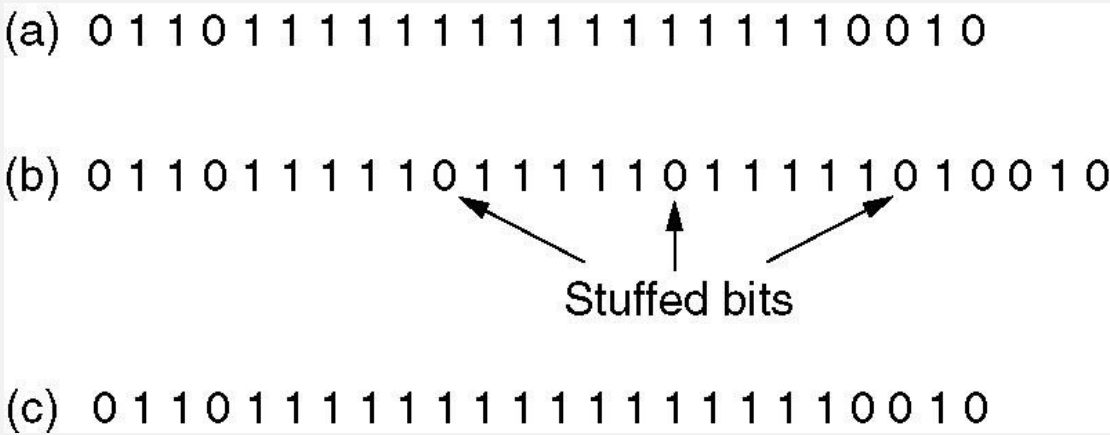
Framing with FLAGS (1st way)

- FLAG is a distinctive bit pattern
 - Such as 01111110 (01⁶0)
 - Must not appear inside data

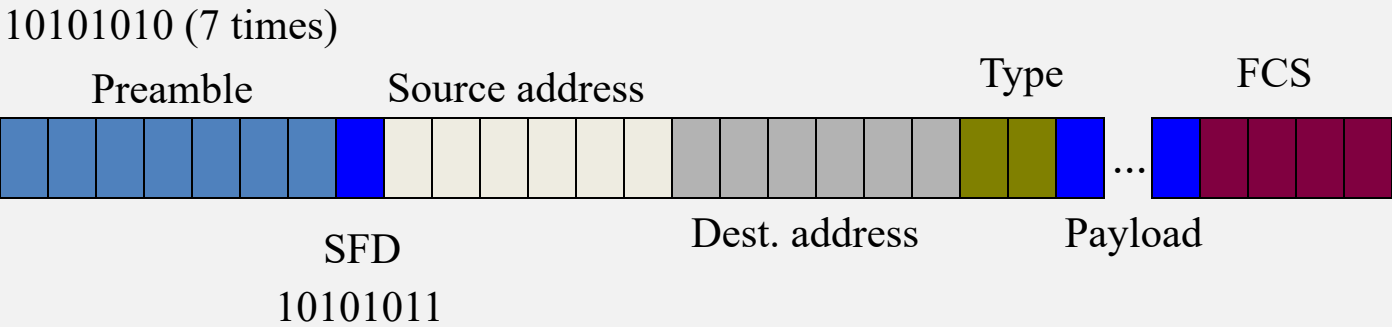


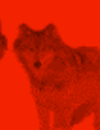
Bit Stuffing (2nd way)

- Bit stuffing method
 - Delineate by pattern of many bits
 - Prevent pattern from occurring in data by few bits
 - Must be completely reversible, *i.e.* destuffable



Ethernet Frame





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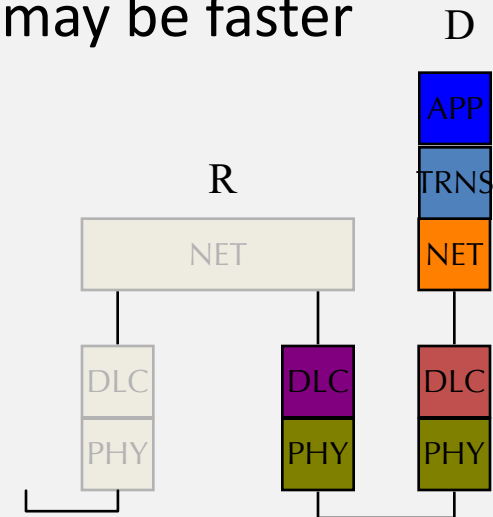
Error Control

- Errors happen
- **Detection**
 - Can at least catch errors
 - ARQ strategies may come in
- **Correction** (Forward Error Correction - FEC)
 - Better if we can
 - Sometimes essential due to link characteristics
- Involves error codes
 - Coding, not encryption



Flow Control

- Potential data generation and transmission may be faster than consumption
 - Receiver might be “overwhelmed”
- Fast sender, slow receiver (root cause)
 - Also at E2E layer (transport, or app)
- Realistically, must stop and wait during transmission
 - Channel idle for some fraction of time
 - Possible to proactively use some of this time
 - Windowed flow control





Flow Control vs. Error Control

- Flow control → if sender “gets ahead” of receiver, must retransmit
- Error control → may need to retransmit because of error in channel as well
 - Error must be detected
 - Only non-erroneous copy at the sender
 - Receiver must request a repeat
 - Automatic Repeat ReQuest (ARQ)
- But – sender needs to know
 - Requests are sometimes implicit



Error Control



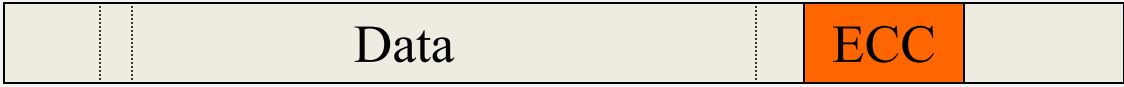


Error Control

- Errors happen
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Fundamental Concepts - Redundancy

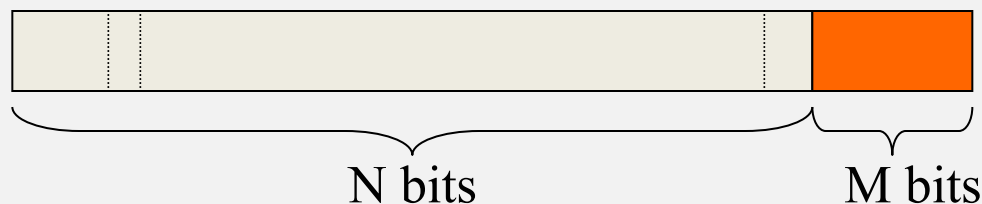
- Some data is actual data for error control
 - Transmitted data: Could be data + metadata
 - More data introduced expressly for the purpose of error control
 - “Metadata” for error control
 - Data and metadata must be distinguished
 - Usually a framing issue
 - Metadata introduces overhead
 - Only data is “useful” outside error control





Fundamental Concepts - Rate

- Overhead \rightarrow “rate” of a coding scheme
 - Rate of useful data per encoded data, < 1.0
- Higher rate is more efficient
- Unfortunately, less likely to be good coding
- Expressed as a ratio of bits
 - Over long periods, if not constant





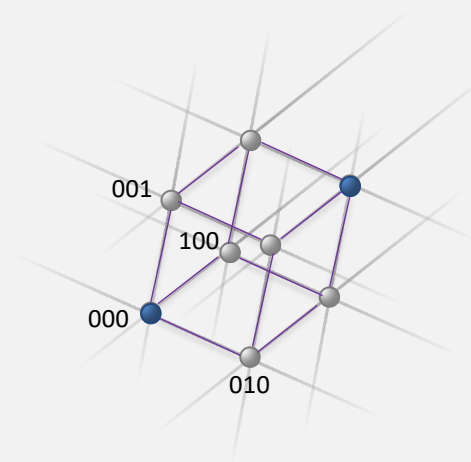
Fundamental Concepts – Block

- If a coding scheme always codes n bits to $n+m$ bits,
 - We speak of n bit blocks that are independently coded
 - The code is called a block code
- Alternative – “stream” type coding
 - Convolution



Fundamental Concepts - Distance

- Data given to error control can be anything
 - 2^n “datawords”
 - For each, a unique m bit code
 - “Codeword” (encoded data) is $n+m$ bits
- **Hamming distance** is a measure of strength
- d_{\min} = minimum Hamming distance between any two codewords
- To guarantee **detection** of t errors
 - $d_{\min} > t$
- To guarantee **correction** of t errors
 - $d_{\min} > 2t$



Goal: maximize d_{\min}

Efficient Use of Rate

- Lower rate means more overhead bits
 - But does not necessarily translate into higher distance
- Choosing appropriate codewords “smartly” can increase distance

$0 \rightarrow 00, 1 \rightarrow 10$
(does not increase robustness)

$0 \rightarrow 00, 1 \rightarrow 11$
(does)

$n = 1, m = 1$

$n = 2, m = 1$

$00 \rightarrow$
 $01 \rightarrow$
 $10 \rightarrow$
 $11 \rightarrow$

?



Simple Repeat (coding example)

- Correction is possible, but rate suffers
- “ k out of $2k-1$ ” kind of **voting process**
- E.g., if each bit is sent seven times,
 - 1 bit blocks
 - 2^1 datawords (‘0’ and ‘1’)
 - Only 2 valid codewords (‘0000000’ and ‘1111111’)
 - Hamming distance $d_{\min} = 7$
 - $t < 7$ if only detection is attempted (up to 6)
 - Up to 3 if correction is attempted from any seven-bit combination



Parity

- **XOR** all bits of the message
- Append the resultant bit to the word ($m = 1$)
- Detects *all* single bit errors
 - In fact, can detect all odd number of bits errors
 - But can *not* correct
- Unfortunately, chances of errors increases



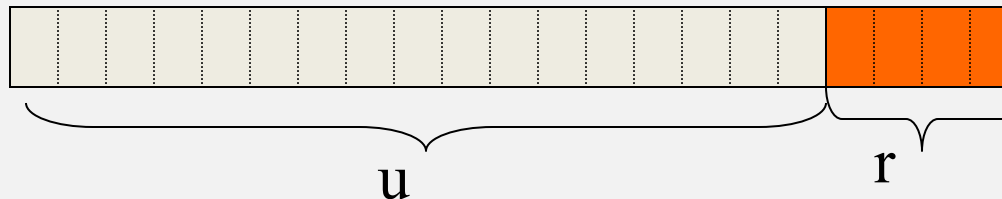
$$c = s_1 \oplus s_2 \oplus \dots \oplus s_n, \text{ the modulo-2 sum}$$

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CRC – In Plain Words

- Let dataword bits be U (viewed as a binary number)
- Define a generator bit sequence G of $t+1$ bits
Both sender and receiver knows G
- Compute a trailer R of t bits based on U and G , s.t.
 $\langle U, R \rangle$ is exactly divisible by G (using modulo-2 arithmetic)
- Can detect all **up to t bits bursty errors**...
... *but not the generator sequence itself*
=> **Bursty**: consecutive bit flippings (frequent on Internet)



want:

equivalently:

thus:

if we divide $U \cdot 2^t$ by G , the remainder is R

Long Division – but addition & subtraction are XOR

Diagram illustrating a binary tree structure for a Huffman tree. The root node is labeled $t = 3$. The tree has three main branches: G (orange), U (black), and R (green). The G branch leads to a node containing 1001 (orange) and 101110 (black). The U branch leads to a node containing 101011 (black) and 000 (blue). The R branch leads to a node containing 011 (green). The 1001 node further branches into 101 (black) and 000 (blue). The 101 node branches into 1010 (black) and 1001 (orange). The 1010 node branches into 110 (blue) and 000 (blue). The 110 node branches into 1100 (blue) and 1001 (orange). The 1100 node branches into 1010 (black) and 1001 (orange). The 1010 node branches into 1010 (black) and 1001 (orange). The 1001 node branches into 011 (green).

XOR	
1	\oplus 1 = 0
1	\oplus 0 = 1
0	\oplus 1 = 1
0	\oplus 0 = 0

$$G = 101, t = 2, U = 111011$$

CRC – Polynomial View

Message polynomial

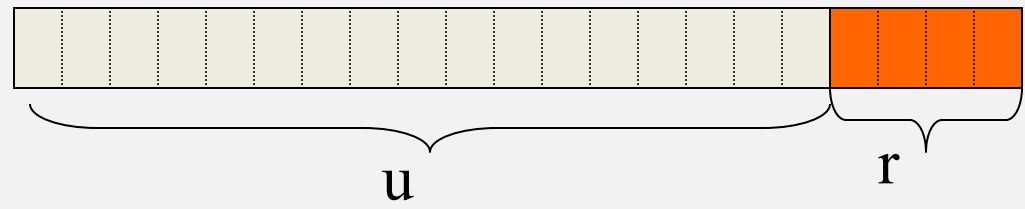
$$u(x) = u_0 + u_1x + u_2x^2 + \dots + u_{n-1} x^{n-1}$$

CRC polynomial

$$r(x) = r_0 + r_1x + r_2x^2 + \dots + r_{m-1} x^{m-1}$$

Code polynomial

$$c(x) = u(x) x^m + r(x)$$





CRC as Remainder of a Division

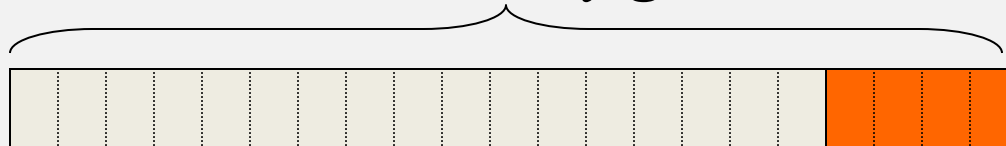
Generator polynomial :

$$g(x) = 1 + g_1x + \dots + g_{m-1}x^{m-1} + x^m$$

Encoding Rule:

$$r(x) = \text{Rem} \left\{ \frac{u(x) x^m}{g(x)} \right\}$$

Divisible by g



Long Division Example

$n=3$ and $m=3$

$g(x) = 1 + x^2 + x^3$

$u(x) = 1 + x^2$

$c(x) = ?$

$x^3 + x^2 + 1$

$x^5 + + + + + $

$x^5 + x^4 + + + + $

$x^4 + x^3 + + + $

$x^4 + x^3 + + + $

$x^2 + x$

Remainder →

Review: DLC Error Control



Error Control in DLC Layer

[Show Correct Answer](#)

TRUE or FALSE: With error control in the DLC layer, there is no need for further reliability mechanisms in the upper layers, since errors can be fully handled in the DLC layer.

A

True

B

False

Review: CRC Bits



CRC Bits

TRUE or FALSE: A CRC code with a 10-bit generator can detect all 10-bit errors.

A	True
B	False



Running Summary

- DLC - framing, flow control, error control
 - Optionally medium access
- Error control
 - Metadata must be added to enable error detection/correction
 - May be integrated with encoding of data – overlap with physical layer
 - Block codes
- Encapsulates error detection/correction largely within DLC and physical layers