# Syed Ali Shehryar

Engineer. Developer.

# Education

### June 2014 **Bachelors of Engineering, Electrical**

**Engineering** 

Military College of Signals, National University of Sciences & Technology, Pakistan.

**CGPA:** 3.11/4.00

Coursework Highlights: Data Communications & Networking, Network Security, Mobile & Wireless Communications, Telecommunication Systems Design, Digital & Analog Communications, Digital & Analog Circuit Design, Satellite Communications, Communication Systems Planning, Object Oriented

Programming Patterns, Computer Systems Architecture,

and Control Systems Design.

#### August 2010 Cambridge International A' Levels

Roots School System, Pakistan.

**CGPA:** 4.00/4.00 Grade: A

Coursework Highlights: Math (A), Physics (A\*),

Chemistry (A), Economics (A).

# Related Work Experience

#### October 2014 -**TeReSol Pvt Limited**

# Software Engineer (Embedded Linux)

- -Kernel and Driver Development, System Integration and Customization of embedded Linux distributions.
- -Modifications to and compilation of kernel images (Arago based ulmage) for AM1808 (ARM9) based custom boards.
- -Development and Testing of a Universal Parallel Port (uPP) driver module for Texas Instruments OMAP-L138.

### Summer 2014 Qwexar, Pakistan. Software Engineering Intern.

- -Acquired experience in testing and development of web based applications.
- -Assisted a company client in upgrading his rails application and improve his web presence by employing html5 and css3.

### Summer 2013 Ericsson, UAE. **Network Engineering Intern**

# -Worked with the Fixed Access Network team responsible for deploying and

- maintaining various GPON/FTTH/DSL projects for Du Telecom.
- -Worked with the LTE core packet team responsible for design, deployment, maintenance and optimization of LTE networks, and provided assistance by active participation in Drive Testing.

# Extracurricular Activities

## **NUST Community Service Club**

-Organized and lead a team of 30 students for park cleaning and maintenance campaign.

🖾 ali.shehryar@live.com

in pk.linkedin.com/in/ashehryar

(+92)320-1234121

github.com/sshehryar

# Skills

Hardware: -PCB Designing in Proteus Professional &

Electronics Workbench.

-Microwave Circuit Design in Agilent

ADS.

-Antenna Design in HFSS.

**Platforms:** Windows, Linux.

Languages: Ruby, HTML, Javascript, CSS, C, C++,

Matlab.

Frameworks: OpenCV, Rails, Pupil (MIT)

Tools: GNU-GCC, Visual Studio, Brackets IO,

GEDIT, GIT

Other: Network Optimization (GSM, 3G, LTE),

**OSP Network Configuration &** 

Maintenance.

# Significant Class Projects

### **Eye-Gesture Based Control System**

-My Group's senior year project. It uses OpenCV to track pupil based eye movements and produces serial port outputs for Arduino Due that are used to move a robotic arm.

### **Real Time GPS Tracker**

-Team leader; Created an ATMega32 based GPS tracker that was interfaced with Google maps for real time monitoring.

# **Dual Tone Multi - Frequency Based Home Monitoring System**

-End of semester project that used a DTMF IC and an AVR based circuit with and Arduino Wireless Inventors Shield that accepts wirelessly sent input from the dial pad of a cell phone and uses the sequence to drive IP cameras as well as motors.

# Other Experience/Awards

#### April 2013 Coursera

-Successfully completed Introduction to Computer Networks course offered online by University of Washington, through Coursera.

#### November 2012 Edx, BerkeleyX

-Successfully completed CS-169x: Engineering Software as a Service course offered online by University of California at Berkeley,

### **Cambridge AICE Diploma**

-Successfully cleared Cambridge International Examinations Advanced International Certificate of Education Diploma with Distinction.

#### October 2009 **IGGY Warwick**

-Inducted as an honorary member to International Gateway for Gifted Youth, University of Warwick.