

SSM INSTITUTE OF ENGINEERING AND TECHNOLOGY

Dindigul – Palani Highway, Dindigul – 624 002.

DEPARTMENT OF CIVIL ENGINEERING

Value added Course Summary (2019-2020)

Course Name : AutoCAD

Course Duration : 35 Hours

Year offered : II year students (2019-2020)

Course Outcome: The students would become familiar with the AutoCAD user interface and understand the fundamental concepts and features of AutoCAD.

Course Type : Self-Framed / Collaboration with Industry

Assessment Mode

Attendance : 35 Hours


Number of participants : 30

Scheme of Exam : MCQ offline


Course Coordinator


HoD




Dr.D.SENTHIL KUMARAN, M.B., Ph.D., (NUS)
Principal
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DEPARTMENT OF CIVIL ENGINEERING

Value added Course Summary (2019-2020)

Course Name : Revit Architecture
Course Duration : 35 Hours
Year offered : III year students (2019-2020)

Course Outcome: The students would be able to create architectural floor plans, building elevations, sections & construction details.

Course Type : Self-Framed / Collaboration with Industry

Assessment Mode

Attendance : 35 Hours
Number of participants : 86
Scheme of Exam : MCQ offline


Course Coordinator




HoD



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Department of Computer Science and Engineering

Value Added Course Summary 2019-2020

Course Name: Python with Advanced Concepts

Course Duration: 32 hours

Year Offered: 2019-2020

Course Instructors: Ms. S. Bharathi, Assistant Professor/CSE

Ms. M. Kiruthika, Assistant Professor/CSE

Course Outcome:

On completion of the course, students will be able to Create programs in Python IDLE, Implement OOPs concepts by using Arrays, and Data structures and to Implement error handling.

Course Type: Self Framed

Assessment Mode

Attendance: 32 hours

Number of Participants: 51

Scheme of Exam: Python concepts as Assignment

Course Co-ordinator

HoD

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Department of Computer Science and Engineering

Value Added Course Summary 2019-2020

Course Name: Automated Software Testing using Selenium Web Driver

Course Duration: 50 hours

Year Offered: 2019-2020

Course Instructors: Dr. V. Eswaremoorthy, Associate Professor/CSE

Course Outcome:

On completion of the course, students will be able to implement automated testing for web applications and the complete process of automated testing, from configuring the testing environment, to creating and running automated testing.

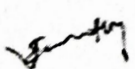
Course Type: Self Framed

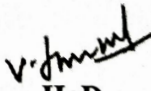
Assessment Mode

Attendance: 50 hours


Number of Participants: 106

Scheme of Exam: Software Testing concepts as Assignments


Course Co-ordinator


HoD




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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Value added Course Summary (2019-2020)

Course Name : PCB Design and Fabrication

Course Duration : 48 Hours

Year offered : IV year students -2019-2020

Course Instructors : Mr. G. Satheeshkumar
Assistant professor /EEE

Course Outcome: The student able to apply the tools and technique of PCB design and Fabrication and able to program and control.

Course Type : Self Framed / Collaboration with Industry

Assessment Mode

Attendance : 48 Hours

Number of participants : 40

Scheme of Exam : MCQ offline



Course Coordinator

HoD

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Value Added Course Summary 2019-2020

Course Name: Quantum dot Cellular Automata in Nanotechnology

Course Duration: 40 hours

Year Offered: 2019-2020

Course Instructors: Dr.S.Karthigai Lakshmi, HoD/ECE

Mrs.M.Jeyalakshmi, AP/ECE

SSMIET, Dindigul

Course Outcome:

On completion of the course, students will be able to identify different QCA devices and components and design and analyze QCA-based logic gates and circuits

Course Type: Self Framed

Assessment Mode

Attendance: 40 hours

Number of Participants: 10

Scheme of Exam: MCQ



Course Co-ordinators

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Value Added Course Summary 2019-2020

Course Name: VLSI Design tools-Verilog and VHDL

Course Duration: 30 hours

Year Offered: 2019-2020

Course Instructors: Mrs.M.Jeyalakshmi,AP/ECE
Mr.S.R.Ashok Kumar
SSMIET,Dindigul

Course Outcome:

On completion of the course, students will be able to understand the process of circuit synthesis, converting high-level descriptions into gate-level or transistor-level representations and gain hands-on experience with Register-Transfer Level (RTL) coding using Verilog and VHDL for describing the behavior of digital systems.

Course Type: Self Framed

Assessment Mode

Attendance: 30 hours

Number of Participants: 40

Scheme of Exam: MCQ



Course Co-ordinators

DR. SEMIL KUMARAN M.E., Ph.D (US)
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Value Added Course Summary 2019-2020

Course Name: C Programming

Course Duration: 30 hours

Year Offered: 2019-2020

Course Instructors: Mrs.A.Geetha,AP/ECE
Mr.J.Vetrimanikumar
SSMIET,Dindigul

Course Outcome:

On completion of the course, students will be able to understand the fundamental concepts of programming, including variables, data types, operators, and expressions and ability to handle possible errors during program execution.

Course Type: Self Framed

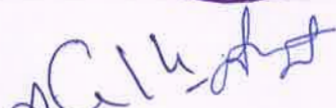
Assessment Mode

Attendance: 30 hours

Number of Participants: 75

Scheme of Exam: MCQ




Course Co-ordinators


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Value Added Course Summary 2019-2020

Course Name: Embedded Programming

Course Duration: 30 hours

Year Offered: 2019-2020

Course Instructors: Mr.S.Jayakumar,AP/ECE
Mrs.T.Leela Priyadharshini,AP/ECE
SSMIET,Dindigul

Course Outcome:

On completion of the course, students will be able to gain knowledge of the architecture and functionality of microcontrollers or microprocessors commonly used in embedded systems.

Course Type: Self Framed

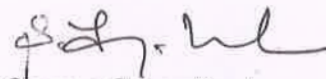
Assessment Mode


Attendance: 30 hours

Number of Participants: 10

Scheme of Exam: MCQ




Course Co-ordinators


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Value Added Course Summary 2019-2020

Course Name: Artificial Intelligence and Robotics

Course Duration: 55 hours

Year Offered: 2019-2020

Course Instructors: Angumaselvan, CEO
Base Equipments

Course Outcome:

On completion of the course, students will be able gain knowledge and skills required to understand, design, and implement intelligent systems and robotic applications

Course Type: Self Framed/ Collaboration with industry

Assessment Mode

Attendance: 55 hours

Number of Participants: 40

Scheme of Exam: MCQ




Course Co-ordinators


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DEPARTMENT OF MECHANICAL ENGINEERING

Value Added Courses Summary 2019-2020


Course Name	Heat Transfer Equipment
Course Duration	45 hours
Year offered	III year Mechanical Students
Course Instructors	Dr.M.Muthukannan & Mr.S.Srinivasan Professor/Mech. Engg, SSMIET Dindigul
Course Outcome	1. Students should be able to communicate heat transfer analyses and solutions through technical reports. 2. Students should be able to troubleshoot and address issues related to heat transfer equipment. 3. Students should be capable of understanding the environmental impact and sustainability aspects of heat transfer equipment.
Course Type	Self Framed
Assessment Mode	
Attendance	45 hours
Number of Participants	41
Scheme of Exam	Evaluation test through online mode


Course Coordinator




HoD/Mech.Engg




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DEPARTMENT OF SCIENCE AND HUMANITIES

VALUE ADDED COURSE SUMMARY 2019-2020

COURSE CODE AND NAME : ADVANCE EXCEL

COURSE DURATION : 35 HRS

YEAR OFFERED : 2019-2020

COURSE INSTRUCTORS : P.CELESTINE – CADD TECHNOLOGIES, COIMBATORE.

COURSE OUTCOME : Upon completion of the course students will be able to:

Use Microsoft Excel to create personal and business spreadsheets following current professional and industry standards.

COURSE TYPE : SELF FRAMED


ASSESSMENT MODE


ATTENDANCE : 35 HOURS


NUMBER OF PARTICIPANTS : 285

SCHEME OF EXAM : MCQ OFFLINE MODE




VAC COORDINATOR
DR. G. SHANMUGAM


S&H ACADEMIC COORDINATOR
DR. G. SHANMUGAM


PRINCIPAL

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