

# **High-Performance Approximation and Accurate Multipliers for Hardware Accelerators Based on FPGA**

**A PROJECT REPORT**

*Submitted by*

**MARUTHAMALAIAYYANRAJA.A**

**MUNIYAPPAN.P**

**MUTHUSURYA.R**

**PRANAV SRIRAM.V**

*In partial fulfilment for the award of the degree*

*of*

**BACHELOR OF ENGINEERING**

**IN**

**ELECTRONICS AND COMMUNICATION ENGINEERING**


**SSM INSTITUTE OF ENGINEERING AND TECHNOLOGY**

**DINDIGUL 624 002**

**ANNA UNIVERSITY :: CHENNAI 600 025**

**MAY 2023**



  
Dr.D.SENTHIL KUMARAN, M.E., Ph.D., (S.C.E.)  
Principal  
SSM Institute of Engineering and Technology  
Dindigul, Tamil Nadu, India

## BONAFIDE CERTIFICATE

Certified that this project report "High-Performance Approximation and Accurate Multipliers for Hardware Accelerators Based on FPGA" is the bonafide work of "MARUTHAMALAIAYYANRAJA.A (922119106055), MUNIYAPPAN.P (922119106058), MUTHU SURYA.R (922119106061) and PRANAV SRIRAM.V (922119106073)" who carried out the project work under my supervision.

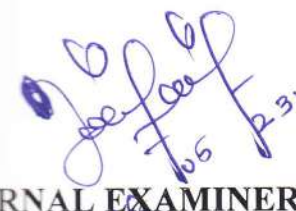
  
SIGNATURE OF HOD

Dr.S.KARTHIGAI LAKSHMI,  
Department of Electronics and  
Communication Engineering,  
SSM Institute of Engineering  
and Technology,  
Dindigul – 624002.


  
SUPERVISOR

Mr.S.KARTHIK,  
Department of Electronics and  
Communication Engineering,  
SSM Institute of Engineering  
and Technology,  
Dindigul – 624002.

Submitted for the VIVA-VOCE Examination held on 18.05.23.

  
INTERNAL EXAMINER

  
EXTERNAL EXAMINER

  
Dr.D.SENTHIL KUMARAN, M.E., Ph.D., (RUS)  
Principal  
SSM Institute of Engineering and Technology  
Dindigul (Pol.)

