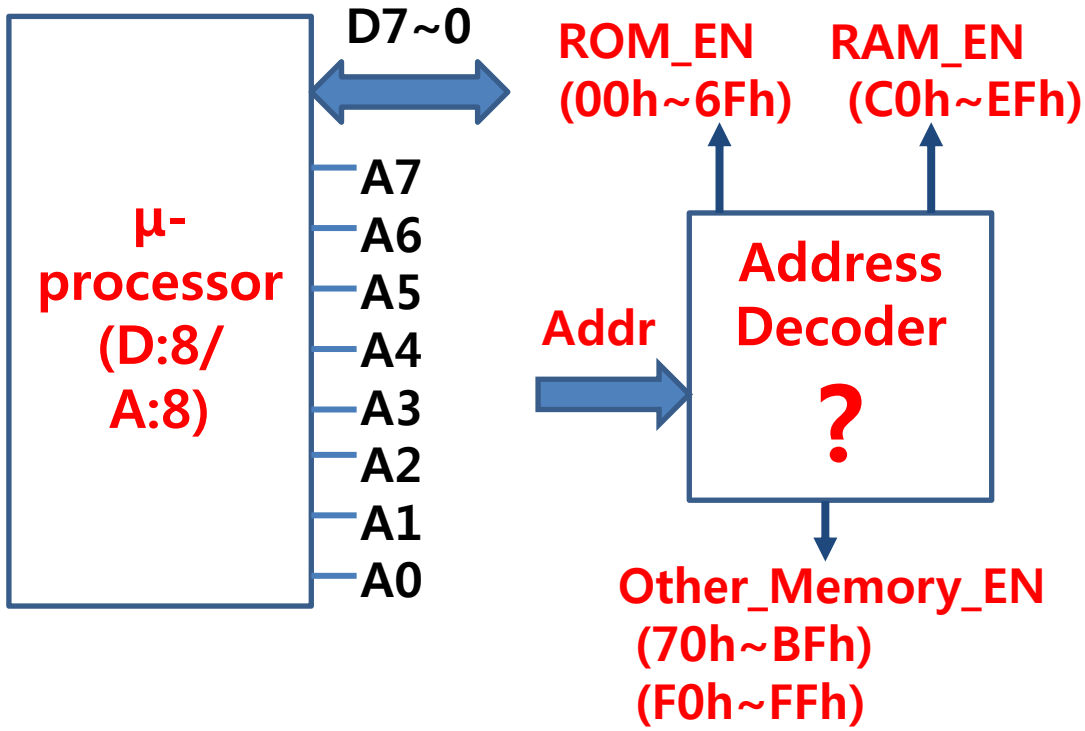
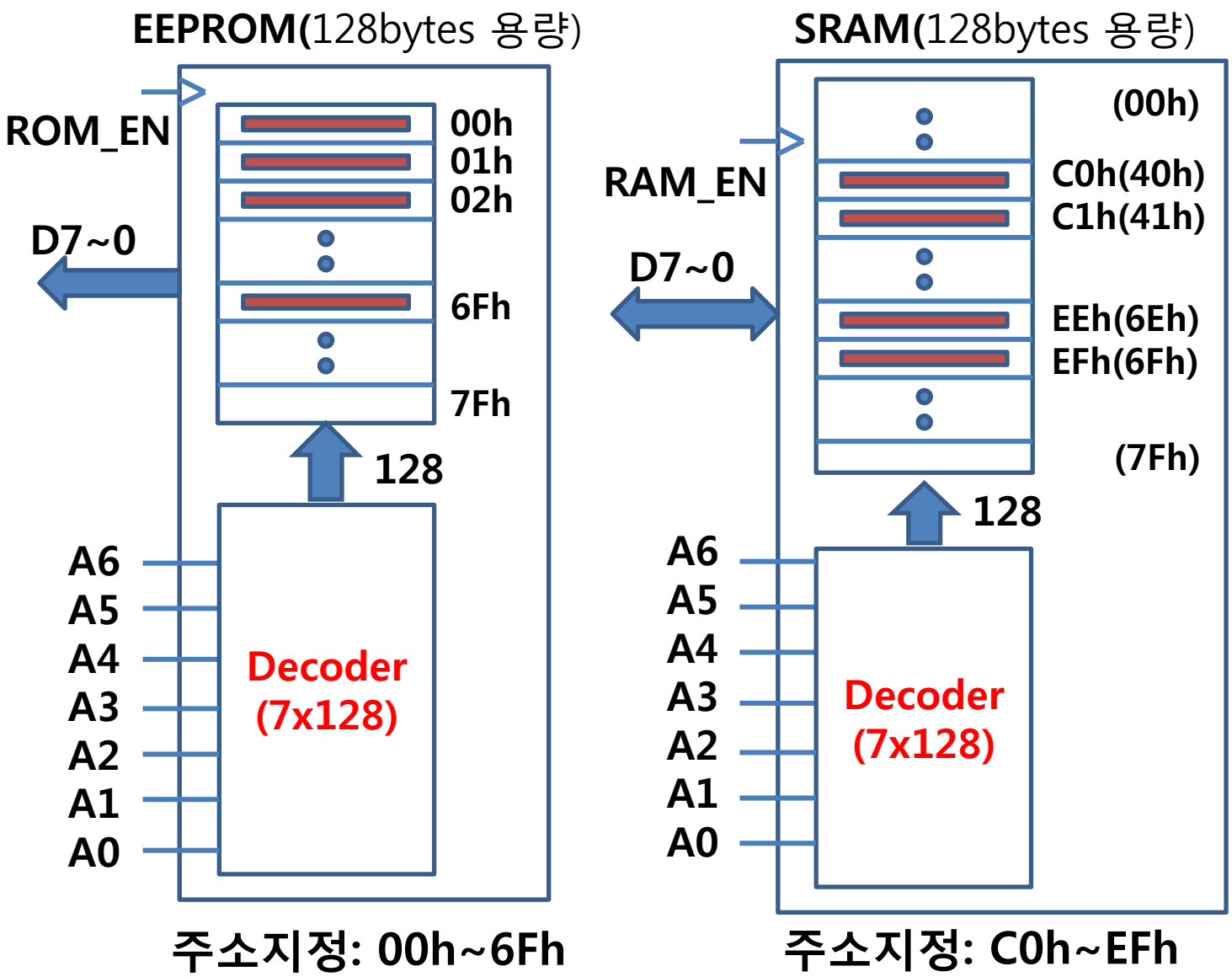
➢HW2: Address Decoder 설계(WED)




EEPROM(128bytes 용량)
ROM_EN
D7~0
00h
01h
02h
6Fh
7Fh
128
A6
A5
A4
A3
A2
A1
A0
Decoder
(7x128)
주소지정: 00h~6Fh
SRAM(128bytes 용량)
RAM_EN
D7~0
(00h)
C0h(40h)
C1h(41h)
EEh(6Eh)
EFh(6Fh)
(7Fh)
128
A6
A5
A4
A3
A2
A1
A0
Decoder
(7x128)
주소지정: C0h~EFh