Printed page: 2 Subject Cod	e: ACSE0305/AMICSE0305/AC	SEH0305
NOIDA INSTITUTE OF ENGINEERIN (An Autor Affiliated to Dr. A.P. J. Abdul Kalam T  Course: B.Tech Branch: CSE/T Semester: III Subject Name: Computer Organiz Time: 1.15Hours	comous Institute) echnical University, Uttar Pradesh, I  I/MTECH INT/CSR/CS  Sessional Examination: Se	Lucknow
General Instructions:		
<ul> <li>➤ This Question paper consists of 2 pages &amp; 5 question expected to answer them as directed.</li> <li>➤ Section A-Q.No-1 isof one 1 mark each &amp; Section B-Q. No-3 carries 5 marks each.</li> <li>➤ Section C-Q.No-4 &amp; 5 carries 6 marks each. As</li> </ul>	Q.No- 2 carries 2 markeach.	пи ате
SEC	CTION - A	[08Marks]
1. All questions are compulsor		$(4\times1=4)$
for floating point arithmet a) IEEE 260 b) IEEE 488 e) IEEE 754 d) IEEE 610	ed by almost all the computers ic	(1) CO2
b. Which of the following is a) Restoring Multiplication b) Booth's Algorithm c) Pascal's Rule d) Digit-by-digit multiplic		(1) CO2
c. The sign magnitude repre a) 0001 b) 1110 c)	sentation of -1 is 1000	(1) CO2
d. The fastest data access is a) Cache b) DRA c) SRAM d) Reg	IVI	CO3

2. Attempt all parts  a. What is divide overflow?	(2)	(2=4) CO2
b. Explain mantissa and exponent with help of an example.	(2)	CO2
SECTION - B  3. Answer any two of the following- a. What is an Array Multiplier? Explain 2x2 bit array multiplier.	(2×	Marks] 5=10) CO2
Explain floating point multiplication with the help of flowchart.	(5)	CO2
c. Explain different type ofinstructions.	(5)	CO3
SECTION - C  4 Answer any one of the following-  a. Design the 4-bit Carry Look Ahead Adder and explain its operation.	(1)	Marks] <6=6) CO2
b. Explain pipelining stages for 5-instructions with diagram.	(6)	СОЗ
5. Answer any one of the following-  Explain Division Algorithm with the help of flowchart.		(6=6) CO2
b. Show the systematic multiplication of (-7) X (3) using Booths Algorithm.	(6)	CO2