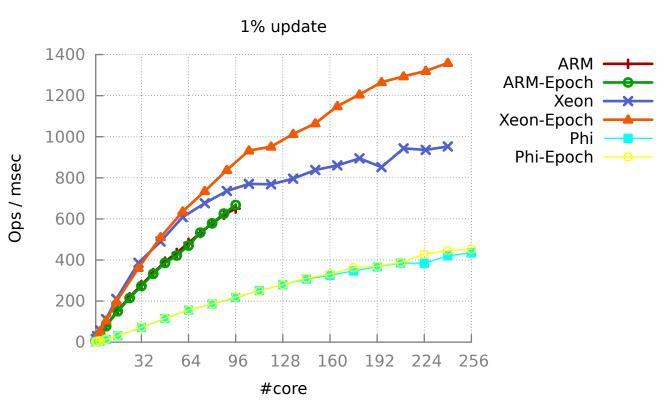
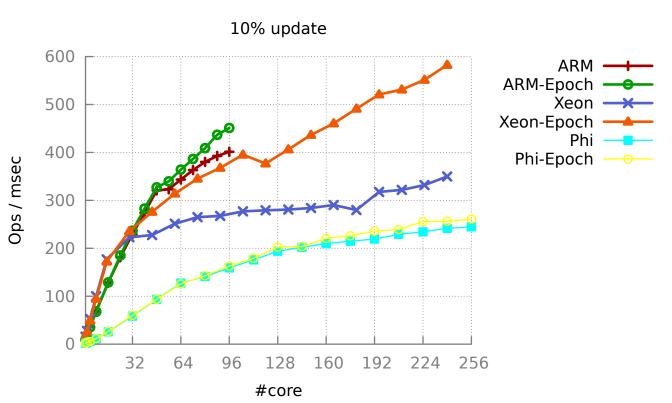
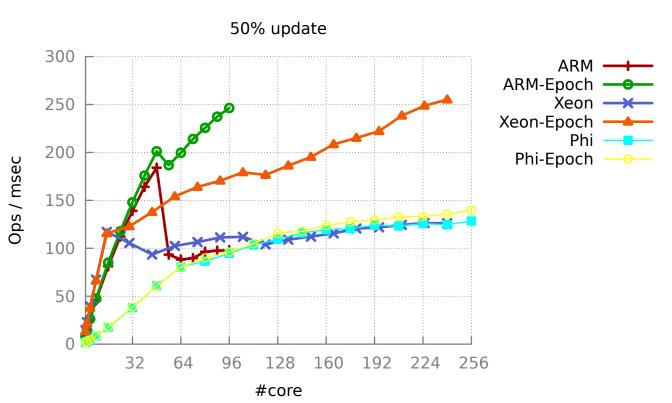
## 0.1% update 1200 ARM 1000 Xeon-Epoch Phi 800 Phi-Epoch —— Ops / msec 600 400 200 32 96 128 160 192 224 256 #core







## 100% update **ARM** ARM-Epoch Xeon Xeon-Epoch Phi Phi-Epoch —— Ops / msec 160 192 224 256

#core