# Divyeshkumar Bardoliya

M29, Hanuman Mandir Gali, Near New Delhi Laxmi Public School, Lado Sarai, New Delhi-110030.

Mobile: +91 7567917015 E-mail: divyeshbardoliya4@gmail.com

### **Summary Statement**

Dedicated dynamic **Electrical Engineer** with a **B.Tech** degree, having **1+ year** of experience in **Solar Industry**, with an undergoing training on **Solar Power Plant Design** and **Electrical System Design** courses with skills set in softwares like AutoCAD, Google Sketchup, PVsyst, Google Earth, ETap, CGLux, etc seeking an opportunity to work in an established organization where i can prove myself by utilizing my skills and knowledge and resulting organizational growth as well as individual development.

### **Professional Experience**

Sonali Energees Pvt. Ltd.
Production Engineer & Quality Engineer
Job Profile:

Surat, Gujarat December 2016 – January 2018

- ❖ I was appointed as **Trainee Engineer** at Sonali Solar.
- To make different types of **Solar Panel Modules** by using raw material like solar cell, ribbon, bus-bar, EVA, back sheet, framing material and attachment.
- **❖ IPQA: In Process Quality Assurance**

Inspection of solar module which involve visual and dimensional inspection of laminated module, all stage wise inspection activities like line-QC, EL(Electro Luminance) test.

#### **FQA: Final Quality Assurance**

Final inspection involves visual inspection of complete module and performs HI-POT test and review of IVW-CHART in SUN SIMMULATOR.

#### **Industrial Training**

- ❖ I am pursuing training program on **Solar Power Plant Design** From 03<sup>rd</sup> February 2018 at **Advanced Electrical Design & Engineering Institute**, Delhi.
- Having exposure on all **engineering calculations** like PV modules **series and parallel** calculation, LT/HT **cable sizing, earthing and lightning protection calculation**. Also **selection and sizing** of Module, cable, Battery, Inverter, ACDB, and MDB.

- Having exposure on all **engineering drawings** like **PV array layout**, **cable routing** and **trench layout**, **earthing and lightning protection layout** by using AutoCAD.
- Having exposure on preparing **shadow analysis report** by using google sketchup and **PVsyst report.**
- ❖ I am pursuing training program on **Electrical System Design** from 03<sup>rd</sup> February 2018 at **Advanced Electrical Design & Engineering Institute**, Delhi.
- Having exposure on LT/HT cable sizing calculation, earthing calculation, transformer sizing, and protection against lightning and cost estimation of industrial power system.
- Having exposure on substation design, transmission line design, earthing design, lighting design and internal electrification design.
- Having exposure on preparing all engineering drawings like **SLD**, **electrical layout** of substation, **cable trench layout** etc using AutoCAD.
- ❖ I had attended the introduction to Industrial Electrical System training program from 24<sup>th</sup> -26<sup>th</sup> august, 2015 at Larsen & Toubro Limited Switchgear Training Centre, Vadodara.
- ❖ I had undergone training from 01<sup>st</sup> June to 16<sup>th</sup> June 2015 at **Surat District Co-Operative Milk Producer's Union Ltd. Sumul Dairy, Surat.**

#### **Skills Set**

Software Skills	Interpersonal Skills
Language: C	Self confident and great patience
Operating System : Windows	motivating and goal oriented
Application System: MS Office	Positive attitude and team leader
Software: MATLAB, Keil, AutoCAD, Google Sketchup, Google Earth, Pvsyst, ETap, CGLux, etc.	Ability to work under pressure

## **Academic Project**

**Project Title: SVPWM** inverter fed speed control of BLDC motor.

**Role**: To design and implement the model of SVPWM inverter fed BLDC motor in MATLAB.

# **Academic Record**

Name of the course	Board/University	Year of passing	Percentage / CGPA
B.Tech (Electrical)	CGPIT - UTU	2012-2016	8.43/10
HSC (CLASS XII)	GSEB	2012	67.69%
SSC (CLASS X)	GSEB	2010	86%

**Declaration:** I hereby declare that the information provided above by me is **true.** 

Place: New Delhi

Date:

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