

CURRICULUM VITAE

Abhijit Biswas,

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Career Objective:

To associate myself with an organization that provides me an opportunity to use my knowledge and lead a team or to be part of it that works dynamically towards the growth of the organization.

Career Profile:

- Having more than 2 Years of rich work experience in Electrical Design and Development for Switchgear and Control Panel.
- Good understanding of Electrical SLD, GA, Foundation, Schematics and Wiring Drawings.
- Both practical as well as theoretical knowledge of Different Electrical Instruments.

Software Exposure:

- AutoCAD
- ELECDES
- SAP
- M.S. Office
- C and C++ (Basics)
- Well versed with Internet Applications.

Work and Training:

- Presently working at **Crompton Greaves Ltd.** as an Executive Design Engineer Since 15th February till now.
- Responsible for the Designing of **Control Panel & PCVCB** as per customer requirement.
- Responsible for generation of **BOM, GA, Schematics and Wiring diagram** as per customer specification on **Elecdes Software**.
- Responsible for preparation of **Instrument arrangement diagram** as per functionality of instruments maintaining aesthetics of Control Panel.
- Responsible for SAP entries required for Planning, Procurement & Dispatch.
- Responsible for release of MI (**Manufacturing Instructions**) for production of control panels.
- Technical support to Vendors about design and development of new product.
- Active participation in designing of new product such as GIS (**GAS INSULATED SWITCH**).
- Conduction of Temperature rise test on Newly developed product as per IEC.
- Previously worked as an Executive Design Engineer in the design department at **PRISTINE SWITCHGEAR (India) Pvt. Ltd., Pune** from 1st September 2014 to 10th February 2016.
- Responsible for electrical and mechanical designing of M.V. & L.T. switchgear panels.
- Preparation and study of BOM according to customer demand.
- Preparation of **Schematic drawings, Assembling Drawings, Single Line Diagram** and **Bill of Material** as per customer requirement.
- Responsible for design of 11kV, 22kV and 33kV **VCB** (Vacuum Circuit Breaker), **VCU** (Vacuum Combination Unit), **SF6 breaker** and **Earthing truck**.
- Responsible for design of **ACB & MCCB** Panels.
- Effective participation in new product development of (11 & 33kV **PCVCB** and 11kV, 22kV & 33kV **PSS**).
- Study of **L.V. switchgears** and **transformers** required for **PSS** (Package Substations).
- Providing technical support for electricians and electrical technicians
- Completed summer training at **GIMA TEX (India) Pvt. Ltd., Hinganghat**. During training period, studied various electrical drives, substations and switching devices.

Educational Qualification:

<u>EDUCATION</u>	<u>YEAR OF PASSING</u>	<u>COLLEGE/SCHOOL</u>	<u>UNIVERSITY/BOARD</u>	<u>PERCENTAGE</u>
B.E (Electrical Engineering)	2010 - 2014	YESHWANTRAO CHAVAN COLLEGE OF ENGINEERING, NAGPUR	AUTONOMOUS INSTITUTE AFFILIATED TO NAGPUR UNIVERSITY	6.78 (CGPA)
H.S.C	2009 - 2010	G.B.M.M JR COLLAGE, HINGANGHAT	MAHARASHTRA STATE BOARD	69.7%
S.S.C	2007 - 2008	MAHESH GYANPEETH HIGH SCHOOL, HINGANGHAT	MAHARASHTRA STATE BOARD	82.3%

Project:**A Photovoltaic Panel Emulator Using a DC-DC Buck Converter, a Micro-Controller and an Electronic Load.**

An Emulator is a digitally controlled buck converter circuit that replicates the V-I characteristics of a solar PV cell. In solar research laboratories, where controlled conditions are required for prolonged research, an Emulator can be efficiently used in the required mode by solely adjusting the supply voltage to the circuit and the duty cycle of the pulse used to fire the converter switch (MOSFET).

We have fabricated an Emulator circuit and a variable electronic load. The behaviour of the electronic load, when fed from the Emulator circuit was studied for different values of insolation (corresponding to the input voltage to the circuit).

Extra-Curricular Activities:

- Presented a paper titled, "A Photovoltaic Panel Emulator using a DC-DC Buck Converter, a Microcontroller and an Electronic Load" at VIT, Nagpur.
- Organizer of Chess in SOFEE (Departmental Event of the Electrical Engineering Department, YCCE, Nagpur).
- Attended workshop on Robogenesis at YCCE, Nagpur.

Strengths:

- Flexible, Adaptable to Changes.
- Systematic working.
- Learning at every stage.

Interest and Hobbies:

- To share information and be part of active discussions.
- Working with computer hardware and softwares.
- Listening music, watching movies.

Personal Information:

Father's Name	:	Jitendranathji D. Biswas.
Date of Birth	:	22nd December 1992
Sex	:	Male
Nationality	:	Indian
Marital Status	:	Unmarried
Language Known	:	English, Hindi, Bengali, Marathi.
Current Location	:	Flat-12, Gopal Krishna Apartment, Pandit Nagar, Nashik.

Declaration:

I hereby state that the above information provided by me is true to the best of my knowledge.

ABHIJIT J. BISWAS