

SHANTANU S KULKARNI

*Master of Electrical Engineering
(Power Electronics and Drives)*

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Currently working at Watt Control Techniques LLP. as R&D Engineer.

*Completed PG in July 2015 from University of Mumbai.
Graduated in June 2013 from University of Pune.*

CAREER OBJECTIVE

Interested in a technical position that allows me to contribute to the company's design and engineering excellence & offers me the opportunity to grow and demonstrate my skills as a power electronics engineer.

INDUSTRIAL EXPERIENCE

Have an exhaustive experience in **Complete Design Cycle of Power Electronic Products**, with hands on in PCB development, Schematic Design, Software Development and Sensors, User Interface, Control systems, Enclosure Design, Thermal Management of Power Devices.

R&D Engineer at Watt Control Techniques.

From July 2015 till date

Products: Static Switch (Zero Delay Load Transfer Solid State Switch)
Solar Powered DC Pump Drive
MPPT
Solar Online UPS
IGBT based Battery Charger

PROJECTS

- | | |
|---|---------------|
| • IOT Extension of UPS | June 2017 |
| • Solar Online UPS | May 2017 |
| • Single Phase Inverter | March 2017 |
| • DC Energy Meter(For Solar Power Measurement) | February 2017 |
| • IGBT based Battery Charger | December 2016 |
| • Solar MPPT based Battery Charger for Retrofit UPS systems | March 2016 |
| • Battery-Free Solar Power Supply | January 2016 |
| • Solar MPPT based DC Pump Drive with Auto On-OFF | December 2015 |

- **Battery Status Indicator** **August 2015**
- Design of **Solar Grid Tied/Interactive Inverter (3 phase, Transformerless)** & exploring possibilities on islanding (using DSP) as project for PG. **June 2015**
- **Design of Closed Loop Buck and Boost Converters.** **October 2014**

TECHNICAL SKILLS

- Digital Signal Processor (TMS320F28069)
- Microchip dsPIC33EPxx series(**DSP**)
- Microchip PIC18Fxx series
- Digital Control Systems
- Power Electronic Converters Design
- Design of Ferrite Magnetic Components
- PCB design (Double Side, PTH)
- Solar PV

CERTIFICATIONS

- Real Time Simulation (**Indian Institute of Technology, Bombay**).
- Energy Rate Structures I: Concepts and Unit Pricing (**Schneider Electric**).
- Design for Reliability in Power Electronic Based Renewables(**IEEE**).
- Basic Mountaineering course (**Nehru Institute of Mountaineering, Uttarkashi, Uttarakhand**).

EXTRA-CURRICULAR ACTIVITIES

- **Summit Expedition leader to Mt. Rudragaira(19127 ft)** near Gangotri glacier.
- Organized & led more than 50 mountaineering Expeditions in the Sahyadri Hills (Western Ghats).
- Member of 5 hiking expeditions in Himalayas.
- Regular participant in cycling events.

PERSONAL DETAILS

- Locational preference: **Open**
- Date of Birth: **15th July 1990**
- Marital status: **Single**
- Gender: **Male**
- Languages known: **English, Hindi, Marathi, French (elementary level).**

EDUCATIONAL QUALIFICATION

<i>Course</i>	<i>Institute</i>	<i>Year of Passing</i>
Master of Engineering, (M.E.) (Power Electronics and Drives)	University of Mumbai	June 2015
Bachelor of Engineering (B.E.) (Electronics)	University of Pune	August 2013
Diploma in Electronics and Telecommunication	Maharashtra State Board of Technical Education (MSBTE)	June 2009