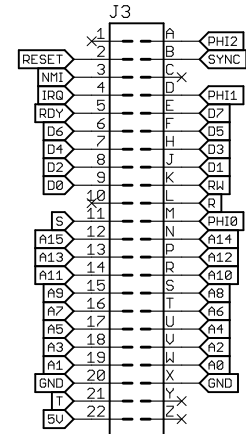
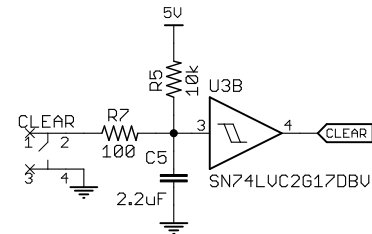
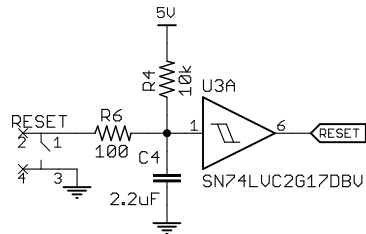
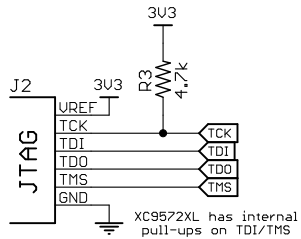
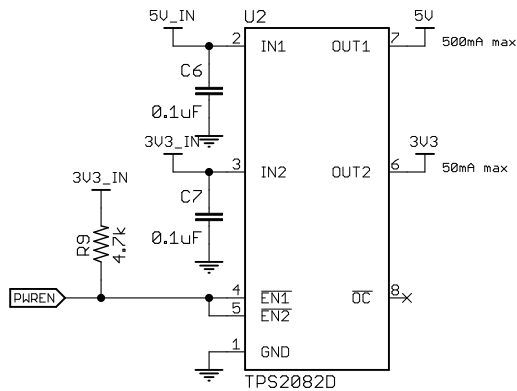


REQUIRED MTP MEMORY CONFIGURATION
 Pull down I/O Pins in USB Suspend: Enabled
 Power Source: Bus Powered
 Max Bus Power Current: 500mA
 Load VCP Driver: Enabled
 CBUS5: CLK12MHz
 CBUS6: PWREN#

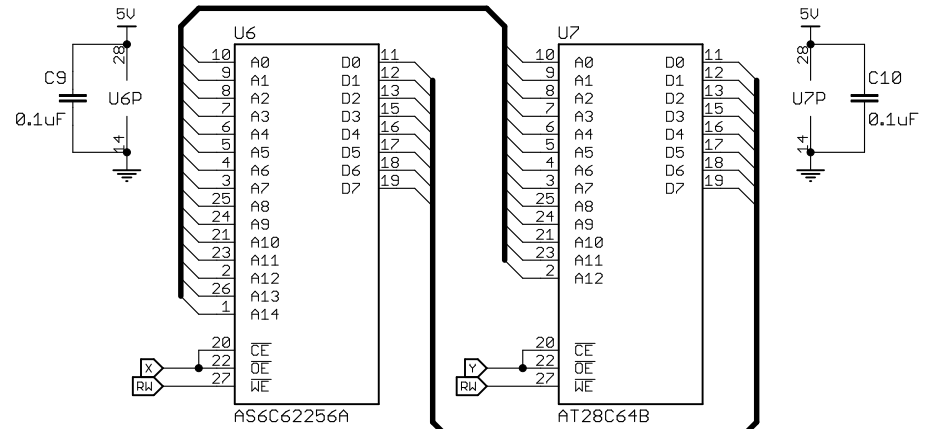
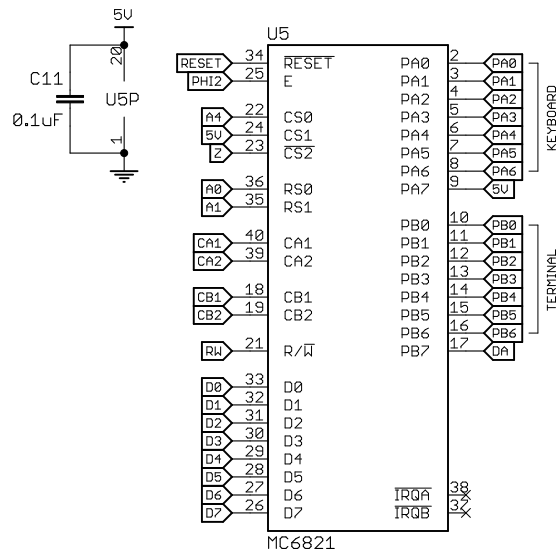
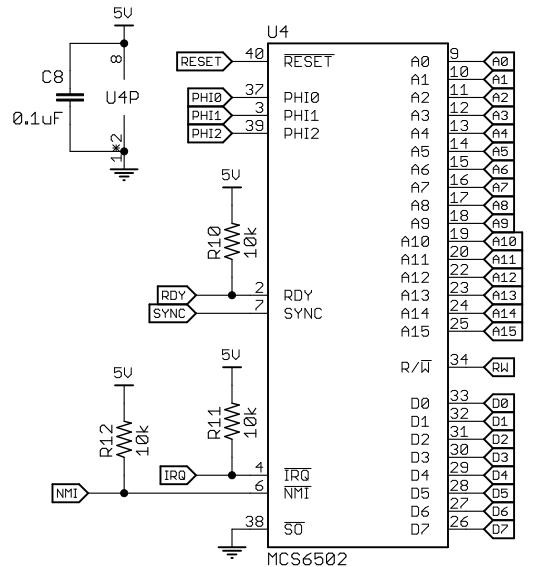


RF/DMA/UMA/+12V/-12V NC



DRAWN S. Stallion	APPLE MINI 1		
COMMENTS N/A	http://github.com/sstallion/apple-mini/		
DRAWING NAME apple-mini	DATE 7/25/15 4:39 PM	REV A	
I/O & Supply		SHEET 1/3	

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 BE FOUND IN THE LICENSE FILE

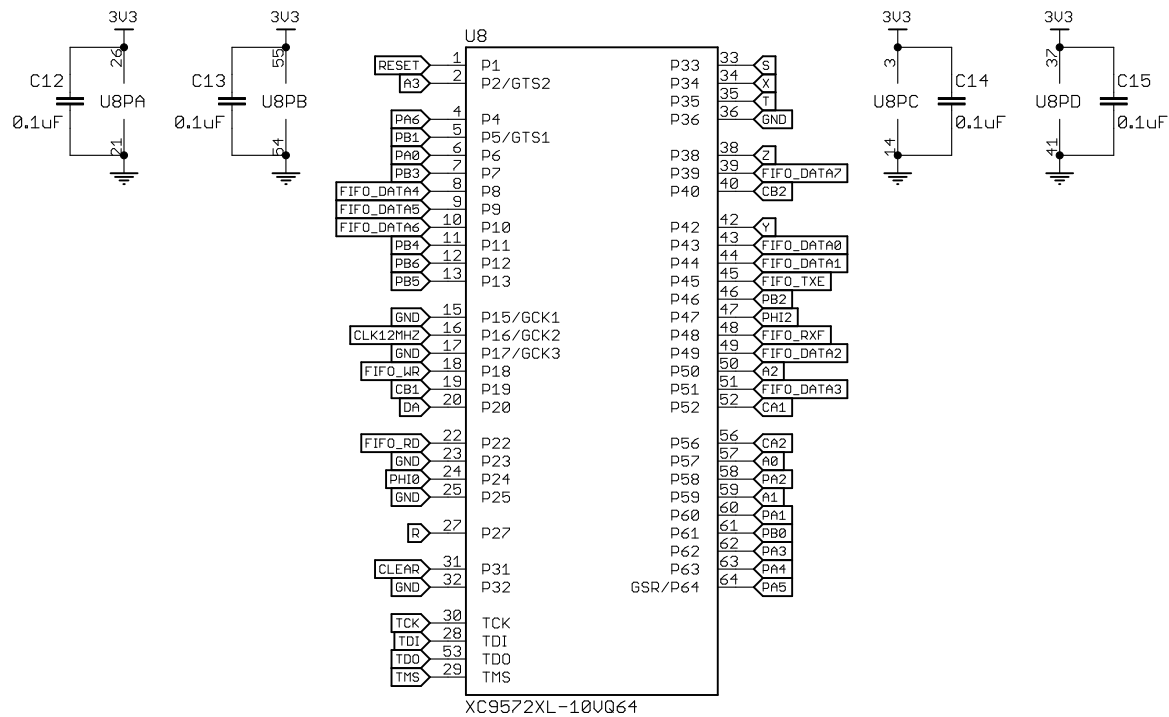


MEMORY MAP

X 0000 7FFF RAM
T 8000 AFFF
S B000 BFFF
R C000 CFFF
Z D000 DFFF PIA
Y E000 FFFF EEPROM

DRAWN S. Stallion	APPLE MINI 1		
COMMENTS N/A	http://github.com/sstallion/apple-mini/		
DRAWING NAME apple-mini	DATE 7/25/15 4:39 PM	REV A	
MPU PIA & Memory		SHEET 2/3	

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DRAWN S. Stallion	APPLE MINI 1		
	http://github.com/ssallion/apple-mini/		
COMMENTS N/A	DRAWING NAME	DATE	REV
	apple-mini	7/25/15 4:39 PM	⬆
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CPLD			SHEET 3/3

