ADC - DAC Implementation on an ARM Processor using C programming Language

EE2016 Microprocessors

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EE23B102

Abstract

ARM processors are the buzzword in the microprocessor industry now, with most devices having processors built on the ARM RISC architecture. They are particulary common in embedded and handheld devices like microcontrollers and smartphones. This experiment uses the LPC ARM chip, which is suited for low power applications. Using the C programming language to program ARM based microcontrollers is a very useful skill to be picked up from this course. The experiment involves the implementation of ADC and DAC on the LPC development kit provided in the laboratory. The ADC is implemented by converting the analog value in a potentiometer into binary numbers displayed on the LED array, while DAC is used to generate common functions like square, triangle, ramp and sine, which are observed on an oscilloscope.

1 Objectives

- 1. Use the DAC of the development kit to generate square, triangle, ramp and sine waveforms, and observe them on an oscilloscope.
- 2. Use the ADC to display the analog value (which is varied by tuning the knob of the trimpot on the development board) converted into a digital number on the LED array.

2 Programs and Results

2.1 DAC Implementation

2.1.1 Square Wave

The program for square wave was given as an example on moodle. The program is given here because the other programs simply have a change in the looping function alone.

```
/* LPC214x
  #include "LPC214x.H"
      definitions */
  #define DAC_BIAS
                                     0x00010000
   void mydelay(int);
   void DACInit( void )
   {
        /* setup the related pin to DAC output */
       PINSEL1 &= 0xFFF3FFFF;
                                    /* set p0.25 to DAC output */
       PINSEL1 | = 0 \times 000800000;
10
       return;
11
  }
12
13
   int main (void)
14
   {
15
       DACInit();
16
17
   //SQUARE WAVE
   while (1)
19
   {
20
        DACR = 0 | DAC_BIAS;
21
          mydelay(0x0F);
          DACR = (0x3FF <<6) | DAC_BIAS;
23
          mydelay(0x0f);
  }
25
       return 0;
26
27
  void mydelay(int x)
29
  {
30
  int j,k;
31
  for (j=0; j \le x; j++)
33
        for (k=0; k \le 0 xFF; k++);
34
```

```
35 }
36 }
```

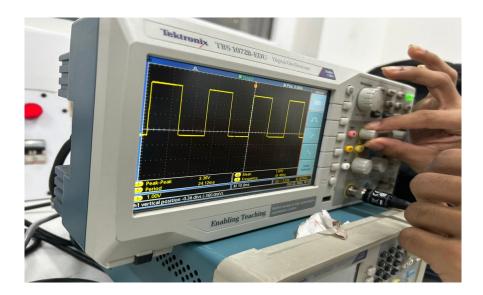


Figure 1: Square Wave generated and observed on the oscilloscope

2.1.2 Ramp Wave

The part of the code inside the main function corresponding to the wave generation is alone presented here. The remaining parts are the same as that of the program for the square wave.

```
while(1)
property

Make the state of th
```

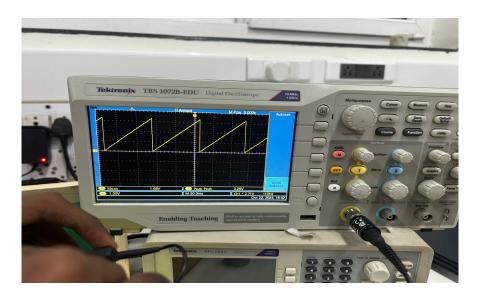


Figure 2: Oscilloscope image of the generated ramp wave

2.1.3 Triangle Wave

```
while(1)
  {
       DACR = 0 | DAC_BIAS;
3
         mydelay(0x01);
5
         for (int i = 0; i \le 0x3FF; i++){
                DACR = (i << 6) | DAC_BIAS;
                mydelay(0x01);
9
           }
10
           for (int i=0x3FF; i>=0; i--){
11
                DACR = (i << 6) \mid DAC_BIAS;
12
                mydelay(0x01);
13
           }
14
```

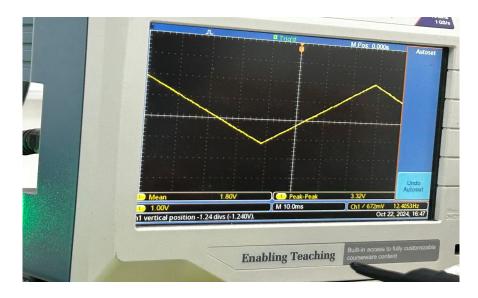


Figure 3: Oscilloscope image of the generated triangle wave

2.1.4 Sine Wave

```
while(1)
  {
       DACR = 0 | DAC_BIAS;
3
          mydelay(0x01);
5
          for (int i = 0; i < 256; i++){
7
                 DACR = ((sineLookupTable[i]) << 6) | DAC_BIAS;</pre>
9
                 mydelay(0x0F);
10
            }
11
12
13
  }
14
```

Note that for the sine function generation, we have used a lookup table that we generated using a python script and attached it to the program code before building.

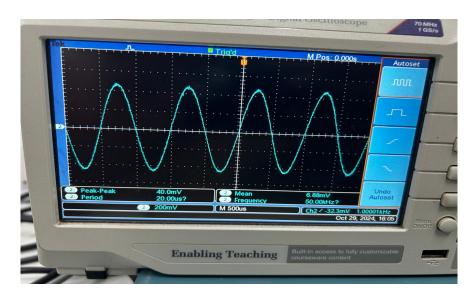


Figure 4: Oscilloscope image of the generated sine wave

2.2 ADC Conversion

The fixed code to implement the ADC on the development kit is done by adding the led turn off feature to the part of the program that triggers the corresponding leds. Appropriate definitions for the LEDOFF functions are also made.

```
#include <LPC214x.H>
                                                   /* LPC214x
      definitions */
  //#include "delay.h"
4
  unsigned long Read_ADCO(unsigned char); /*
5
  void Init_ADCO(unsigned char);
  #ifndef \__ADC_H
  \#define \_\_ADC\_H
9
10
11
  #define CHANNEL_O
                         0
12
  #define CHANNEL_1
                         1
13
  #define CHANNEL_2
                         2
14
  #define CHANNEL_3
                         3
  #define CHANNEL_4
                         4
16
  #define CHANNEL_5
                         5
17
  #define CHANNEL_6
                         6
18
  #define CHANNEL_7
20
```

```
/* Crystal frequency, 10MHz~25MHz should be the same as actual
     status. */
  #define Fosc
                        12000000
                                    /* 12 MHz is the operational
     frequency of o/p dqtlClk */
  #define ADC_CLK
                        1000000
                                    /* set to 1Mhz */
24
25
  /* A/D Converter 0 (ADO) */
26
  #define ADO_BASE_ADDR
                                0xE0034000
  #define ADC_INDEX
28
29
  #define ADC_DONE
                                0000008x0
30
  #define ADC_OVERRUN
                                0x4000000
31
32
33
  #define ADC_FullScale_Volt 3.3 // 3.3V - ADC Reference Voltage
34
  #define ADC_FullScale_Count 1024
                                         // 2^10 - 10 bit ADC
  #define LED_IOPIN
                            IOOPIN
36
  #define BIT(x)
                   (1 << x)
37
                                    // PO.10 mapping same as in
  #define LED_DO
                  (1 << 10)
39
     Exp7 switch LED
  #define LED_D1
                                    // P0.11
                   (1 << 11)
  #define LED_D2
                   (1 << 12)
                                    // P0.12
  #define LED_D3
                   (1 << 13)
                                    // P0.13
42
43
  #define LED_D4
                   (1 << 15)
                                    // P0.15
44
                   (1 << 16)
                                    // P0.16
  #define LED_D5
                                    // PO.17
  #define LED_D6
                   (1 << 17)
  #define LED_D7
                   (1 << 18)
                                    // P0.18
  #define LED_DATA_MASK
                                    ((unsigned long)((LED_D7 |
     LED_D6 | LED_D5 | LED_D4 | LED_D3 | LED_D2 | LED_D1 | LED_D0
     )))
49
                        LED_IOPIN |= (unsigned long)(LED_D0);
  #define LED1_ON
         // LED1 ON
                        LED_IOPIN |= (unsigned long)(LED_D1);
  #define LED2_ON
51
         // LED2 ON
                        LED_IOPIN |= (unsigned long)(LED_D2);
  #define LED3_ON
         // LED3 ON
                        LED_IOPIN |= (unsigned long)(LED_D3);
  #define LED4_ON
         // LED4 ON
                        LED_IOPIN |= (unsigned long)(LED_D4);
 #define LED5_ON
         // LED5 ON
```

```
LED_IOPIN |= (unsigned long)(LED_D5);
  #define LED6_ON
          // LED6 ON
  #define LED7_ON
                        LED_IOPIN |= (unsigned long)(LED_D6);
          // LED7 ON
  #define LED8_ON
                        LED_IOPIN |= (unsigned long)(LED_D7);
          // LED8 ON
58
59
  #define LED1_OFF
                           LED_IOPIN |= 0;
                                 // LED1 OFF
  #define LED2_OFF
                            LED_IOPIN |= 0;
                                 // LED2 OFF
  #define LED3_OFF
                           LED_IOPIN |= 0;
                                 // LED3 OFF
  #define LED4_OFF
                            LED_IOPIN |= 0;
                                 // LED4 OFF
  #define LED5_OFF
                            LED_IOPIN |= 0;
                                 // LED5 OFF
  #define LED6_OFF
                            LED_IOPIN |= 0;
                                 // LED6 OFF
  #define LED7_OFF
                           LED_IOPIN |= 0;
                                 // LED7 OFF
                            LED_IOPIN |= 0;
  #define LED8_OFF
                                 // LED8 OFF
68
  #endif
  #ifndef LED_DRIVER_OUTPUT_EN
                                             // P0.5
  #define LED_DRIVER_OUTPUT_EN (1 << 5)</pre>
  #endif
  //LED definitions
73
74
75
76
77
78
  int main (void)
79
  {
80
81
       unsigned long ADC_val;
82
83
       Init_ADCO(CHANNEL_1);
       Init_ADCO(CHANNEL_2);
85
86
       delay_mSec(100);
87
88
```

```
// GPIO Direction
       IOODIR |= LED_DATA_MASK;
           control -> pin is output
       IOODIR |= LED_DRIVER_OUTPUT_EN;
                                               // GPIO Direction
90
           control -> pin is output
       IOOCLR |= LED_DRIVER_OUTPUT_EN;
91
92
93
       while (1)
94
       {
95
            //ADC_val = Read_ADCO(CHANNEL_1);
96
            ADC_val = Read_ADCO(CHANNEL_2);
97
            ADC_val = (ADC_val >> 2);
98
            delay_mSec(5);
99
   LED8 = (ADC_val & BIT(0)) ? LED8_ON : LED8_OFF;
100
   LED7 = (ADC_val & BIT(1)) ? LED7_ON : LED7_OFF;
101
   LED6 = (ADC_val & BIT(2)) ? LED6_ON : LED6_OFF;
102
   LED5 = (ADC_val & BIT(3)) ? LED5_ON : LED5_OFF;
103
104
   LED4 = (ADC_val & BIT(4)) ? LED4_ON : LED4_OFF;
105
   LED3 = (ADC_val & BIT(5)) ? LED3_ON : LED3_OFF;
   LED2 = (ADC_val & BIT(6)) ? LED2_ON : LED2_OFF;
107
   LED1 = (ADC_val & BIT(7)) ? LED1_ON : LED1_OFF;
109
110
       }
111
112
113
114
       return 0;
115
   }
116
117
   void Init_ADCO(unsigned char channelNum)
118
119
       if(channelNum == CHANNEL_1)
120
            PINSEL1 = (PINSEL1 & ~(3 << 24)) | (1 << 24);
                                                                      //
121
               P0.28 -> AD0.1
122
       if(channelNum == CHANNEL_2)
123
            PINSEL1 = (PINSEL1 & ~(3 << 26)) | (1 << 26);
                                                                      //
124
               P0.29 -> AD0.2
125
       if (channelNum == CHANNEL_3)
126
            PINSEL1 = (PINSEL1 & ~(3 << 28)) | (1 << 28);
                                                                      //
127
               PO.30 -> ADO.3
```

128

```
129
       ADOCR = (0x01 << 1)
                                                        // SEL=1,
130
          select channel 0, 1 to 4 on ADCO
                (( Fosc / ADC_CLK - 1 ) << 8 ) |
                                                        // CLKDIV =
131
                   Fpclk / 1000000 - 1
                ( 0 << 16 ) |
                                                        // BURST = 0,
132
                   no BURST, software controlled
                (0 << 17)
                                                        // CLKS = 0, 11
133
                    clocks/10 bits
                (1 << 21)
                                                        // PDN = 1,
134
                   normal operation
                (0 << 22)
                                                        // TEST1:0 = 00
135
                ( 0 << 24 ) |
                                                        // START = 0 A/
136
                   D conversion stops
                (0 << 27);
                                                        /* EDGE = 0 (
137
                   CAP/MAT singal falling, trigger A/D conversion)
   }
138
   unsigned long Read_ADCO( unsigned char channelNum )
139
140
       unsigned long regVal, ADC_Data;
141
142
       /* Clear all SEL bits */
143
       ADOCR &= OxFFFFFF00;
144
       /* switch channel, start A/D convert */
145
       ADOCR |= (1 << 24) | (1 << channelNum);
146
147
148
       /* wait until end of A/D convert */
149
       while (1) {
150
151
   //
            regVal = *(volatile unsigned long *)(ADO_BASE_ADDR +
152
      ADC_INDEX);
            regVal = ADOGDR;
153
154
            if ( regVal & ADC_DONE ){
155
                break;
156
            }
157
       }
158
159
       /* stop ADC now */
160
       ADOCR &= OxF8FFFFFF;
161
       /* save data when it's not overru otherwise, return zero */
162
       if ( regVal & ADC_OVERRUN ) {
163
```

```
return (0);
164
        }
165
        ADC_Data = ( regVal >> 6 ) & 0x3FF;
166
        /* return A/D conversion value */
167
        return ( ADC_Data );
168
169
   }
170
171
                                          // pr_note:~dCnt mSec
   void delay_mSec(int dCnt)
172
173
      int j=0, i=0;
174
175
      while (dCnt --)
176
177
          for(j=0;j<1000;j++)
178
           {
179
             /* At 60Mhz, the below loop introduces
180
             delay of 10 us */
181
             for(i=0;i<10;i++);
182
          }
     }
184
   }
185
```

The above mentioned changes can be found in the fully completed code given above.

The video of us tuning the potentiometer and the led values accordingly changing can be found at this url https://drive.google.com/file/d/1XbuXvfKBSVxo4XKUrx44M7j3JxKhy15k/view?usp=drive_link

There is a question on what happens when the number of bits used in the ADC is decreased. This would mean the resolution of the ADC decreases, and the number of leds used to show the full range also decreases. This is because the same range of analog values is now captured in a lesser number of bits.

The quantisation error is related to the number of bits as 6.02 * n dB. This means, decreasing the number of error would decrease the magnitude of the quantisation error, but increases the net error as the quantisation error is usually measured below the signal.

3 Conclusion

From this experiment, I learned the key concepts related to signal conversion, and gained an understanding of their implementation on an ARM based processor. I also gained familiarity with the operation of an oscilloscope to measure generated waveforms.