Systems and Software
Verification Laboratory

MANCHESTER 1824
The University of Manchester

Software Model Checking Tools

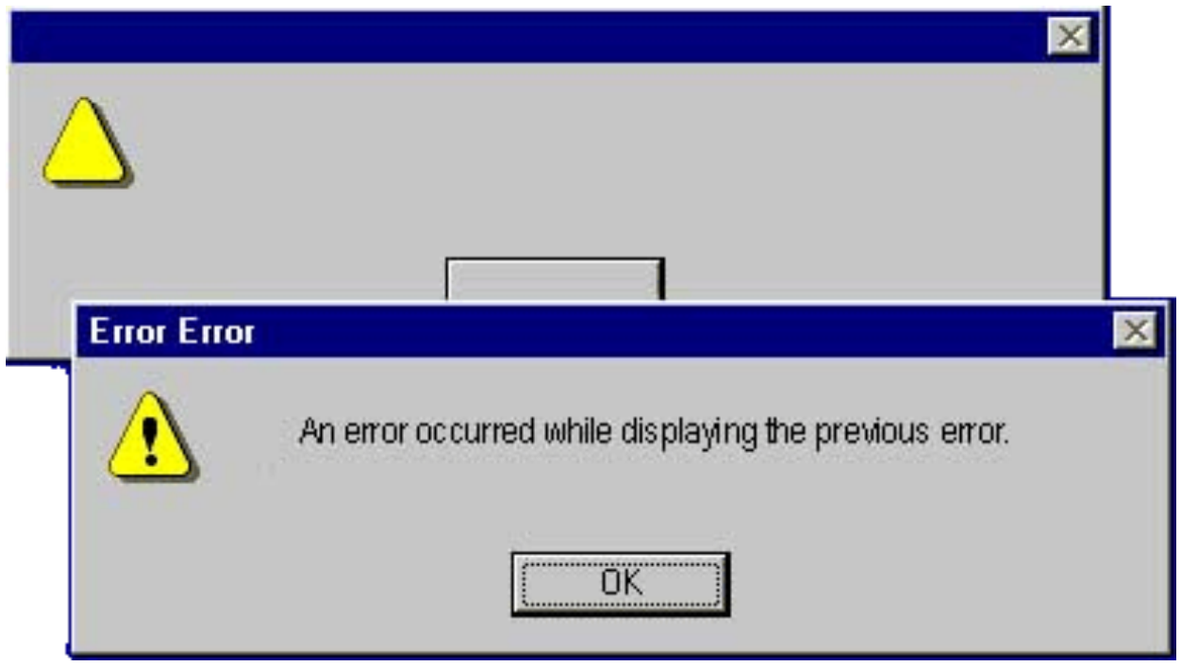
Lucas Cordeiro
Department of Computer Science
lucas.cordeiro@manchester.ac.uk

Software Model Checking Tools

- Lucas Cordeiro (Formal Methods Group)
  - *lucas.cordeiro@manchester.ac.uk*
  - Office: 2.28
  - Office hours: 15-16 Tuesday, 14-15 Wednesday
- References:
  - *Cordeiro et al.: SMT-Based Bounded Model Checking for Embedded ANSI-C Software. IEEE Trans. Software Eng. 38(4): pp. 957-974, 2012*
  - Cordeiro et al.: *JBMC: A Bounded Model Checking Tool for Verifying Java Bytecode*. CAV, pp. 183-190, 2018

Intended learning outcomes

- **Introduce** the software verifiers **ESBMC** and **JBMC**
- **Understand** how software verifiers **check safety properties** in C and Java programs
- **Explain** the main **verification strategies** and **property checks** in ESBMC and JBMC
- **Apply** ESBMC and JBMC to **test and verify** C and Java programs



ESBMC v6.0

Mikhail R. Gadelha
Felipe R. Monteiro
Lucas C. Cordeiro
Denis A. Nicole

25th Intl. Conference on Tools and Algorithms for the Construction and Analysis of Systems

8th Intl. Competition on Software Verification

ESBMC *v6.0*
Verifying C Programs Using *k*-Induction and Invariant Inference (Competition Contribution)

Mikhail R. Gadelha, **Felipe R. Monteiro**, Lucas C. Cordeiro, and Denis A. Nicole




UNIVERSITY OF Southampton

UNIVERSIDADE FEDERAL DO AMAZONAS — IN UNIVERSA SCIENTIA VERITAS

MANCHESTER 1824

ESBMC Gadelha *et al.*, ASE'18

- SMT-based bounded model checker of single- and multi-threaded C/C++ programs

  turned 10 years old in 2018 🎉

- Combines BMC, *k*-induction and abstract interpretation:

  path towards correctness proof

  bug hunting

- Exploits SMT solvers and their background theories

  optimized encodings for pointers, bit operations, unions, arithmetic over- and underflow, and floating-points

ESBMC

Gadelha *et al.*, ASE'18

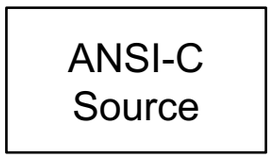
- SMT-based bounded model checker of single- and multi-threaded C/C++ programs

arithmetic under- and overflow
pointer safety
array bounds
division by zero
memory leaks
atomicity and order violations
deadlock
data race
user-specified assertions

built-in properties

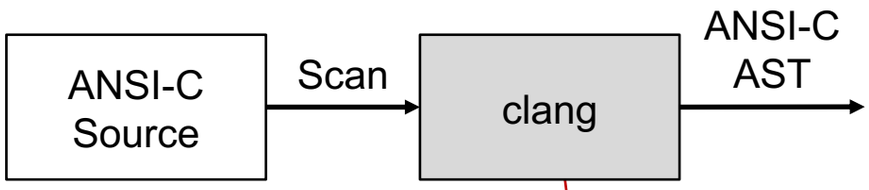
ESBMC Architecture

- ESBMC uses a *k*-induction proof rule to verify and falsify properties over C programs



ESBMC Architecture

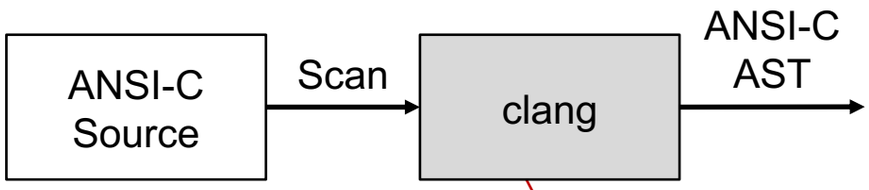
- ESBMC uses a *k*-induction proof rule to verify and falsify properties over C programs



ESBMC uses clang's API **to access and traverse the program AST**, not LLVM bytecode.

ESBMC Architecture

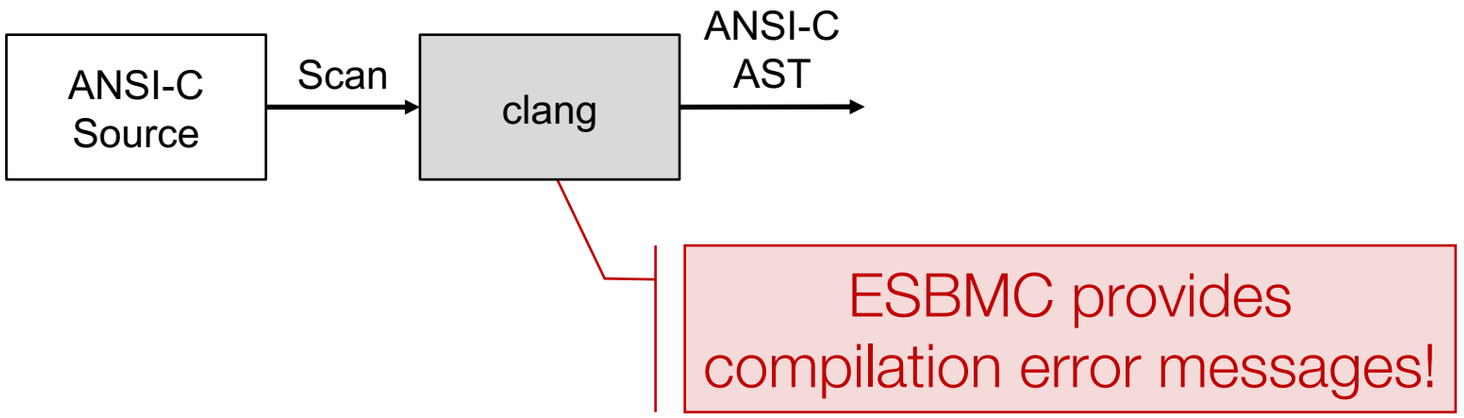
- ESBMC uses a *k*-induction proof rule to verify and falsify properties over C programs



clang can **simplify** the input program (*e.g.*, calculate `sizeof` expressions, evaluate static asserts)

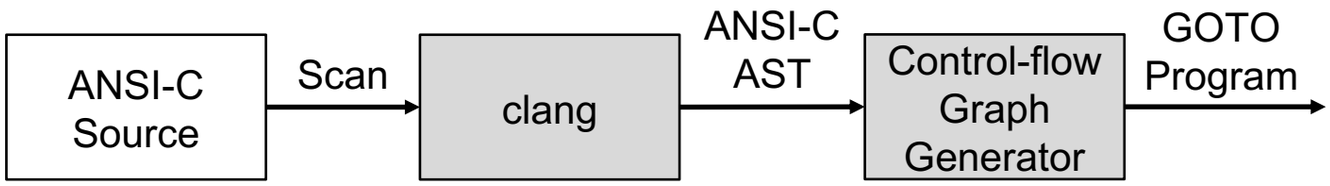
ESBMC Architecture

- ESBMC uses a *k*-induction proof rule to verify and falsify properties over C programs



ESBMC Architecture

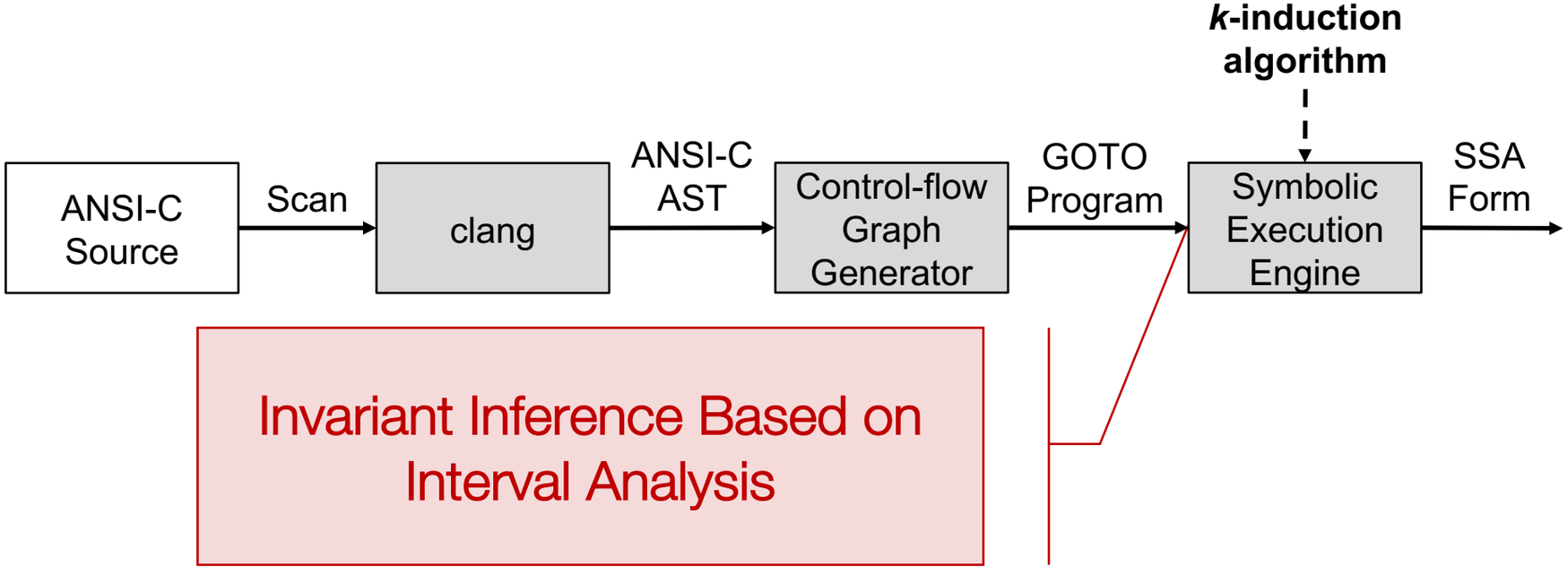
- The CFG generator takes the program AST and transforms it into an equivalent GOTO program
  - only of assignments, conditional and unconditional branches, assumes, and assertions.



ESBMC Architecture

- ESBMC perform a static analysis prior to loop unwinding and (over-)estimate the range that a variable can assume

  "rectangular" invariant generation based on interval analysis (*e.g.*, a ≤ x ≤ b)

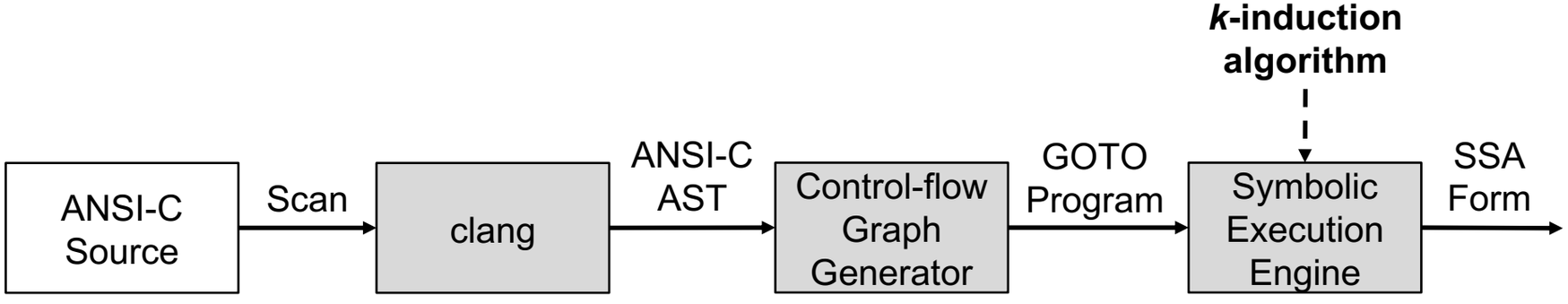


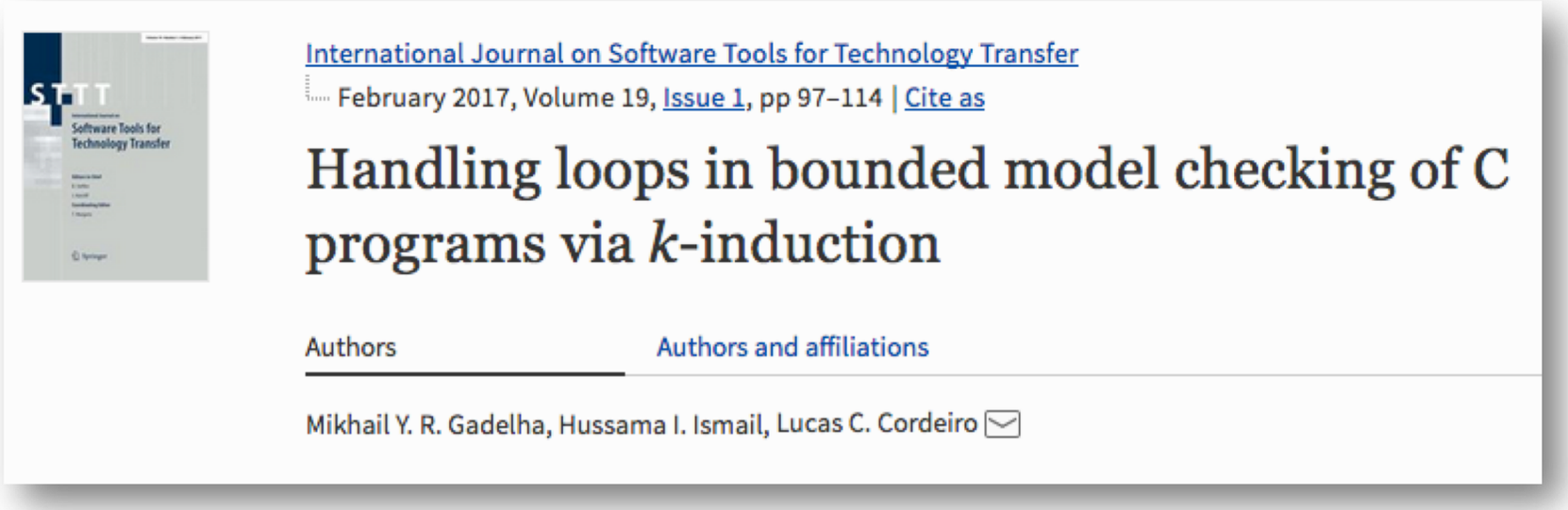
- Abstract-interpretation component from CPROVER
- Only for **integer** variables

ESBMC Architecture

- ESBMC perform a static analysis prior to loop unwinding and (over-)estimate the range that a variable can assume

  “rectangular” invariant generation based on interval analysis (*e.g.*, a ≤ x ≤ b)

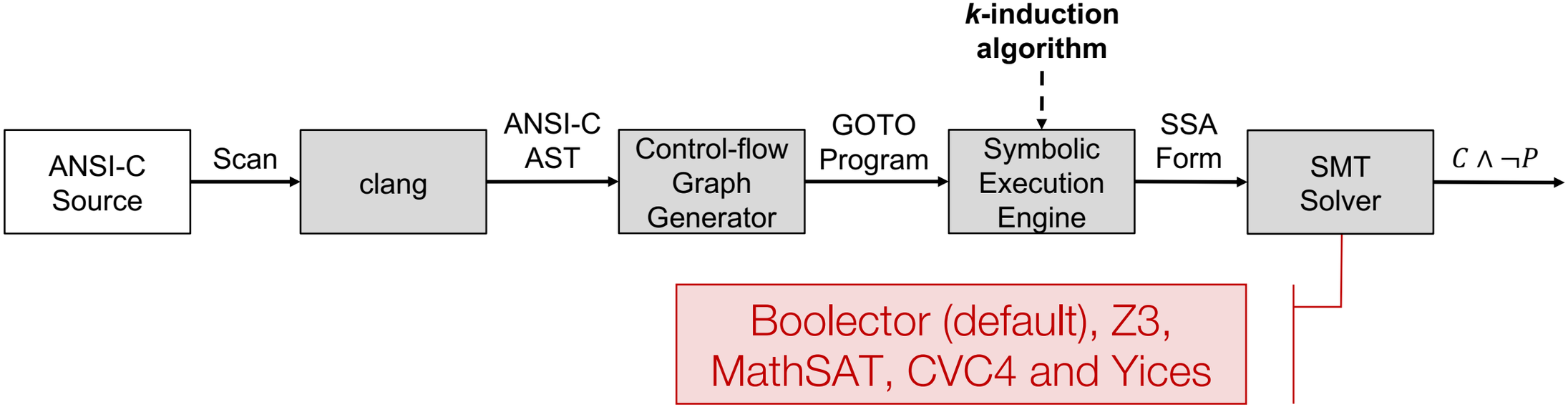




ESBMC Architecture

- The back-end is highly configurable and allows the encoding of quantifier-free formulas

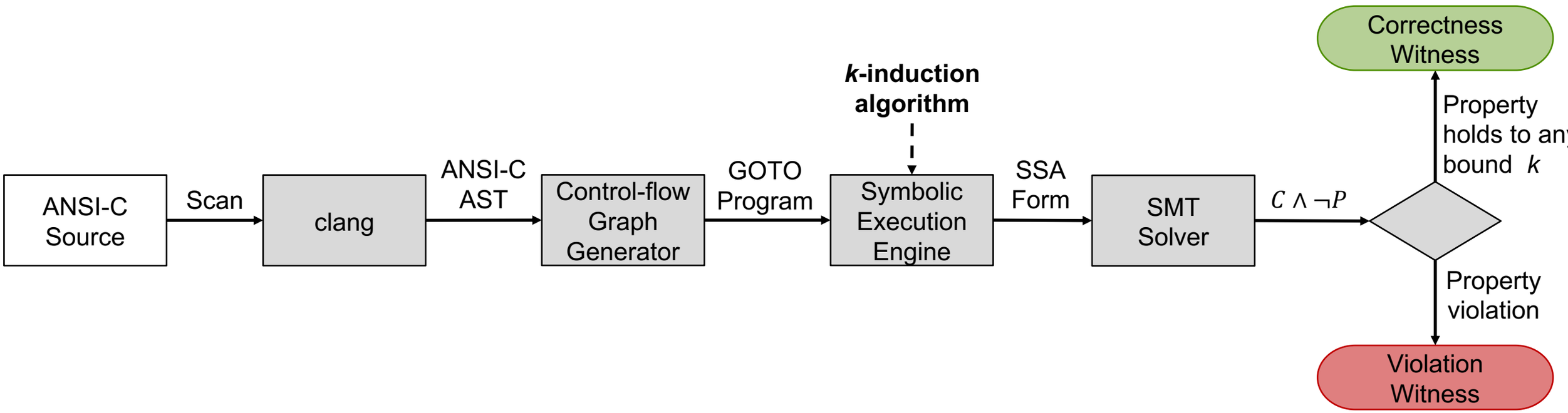
  bitvectors, arrays, tuple, fixed-point and floating-point arithmetic (all solvers), and linear integer and real arithmetic (all solvers but Boolector).



ESBMC Architecture

- The back-end is highly configurable and allows the encoding of quantifier-free formulas

  bitvectors, arrays, tuple, fixed-point and floating-point arithmetic (all solvers), and linear integer and real arithmetic (all solvers but Boolector).



Strengths & Weaknesses

- Strengths

  Reach-Safety

    ECA (score: 1113)

    Floats (score: 790)

    Heap (score: 300)

    Product Lines (score: 787)

  🥉 Falsification (score: 1916)

  Arithmetic, floating point arithmetic

  User-specified assertions

  the use of invariants increases the number of correct proofs in ESBMC by about 7%

- Weaknesses

  need relational analysis that can keep track of relationship between variables

```c
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

Simplified safe program extracted from SV-COMP 2018

```c
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

Program under verification

Enable *k*-induction instead of plain BMC

Enable interval analysis

```
esbmc main.c --k-induction --interval-analysis
```

without interval analysis

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int
    if (input > 5
      return 0;
    } else if (in
      s = 2;
    } else if (in
      s = 3;
    } else if (in
      s = 4;
    } else if (in
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

```
Unwinding loop 1 iteration 49 file svcomp2018.c line 17 function main
Not unwinding loop 1 iteration 50 file svcomp2018.c line 17 function main
Symex completed in: 0.111s (3563 assignments)
Slicing time: 0.008s (removed 2716 assignments)
Generated 1 VCC(s), 1 remaining after simplification (847 assignments)
No solver specified; defaulting to Boolector
Encoding remaining VCC(s) using bit-vector arithmetic
Encoding to solver time: 0.006s
Solving with solver Boolector 3.0.0
Encoding to solver time: 0.006s
Runtime decision procedure: 0.219s
The inductive step is unable to prove the property
Unable to prove or falsify the program, giving up.
VERIFICATION UNKNOWN
```

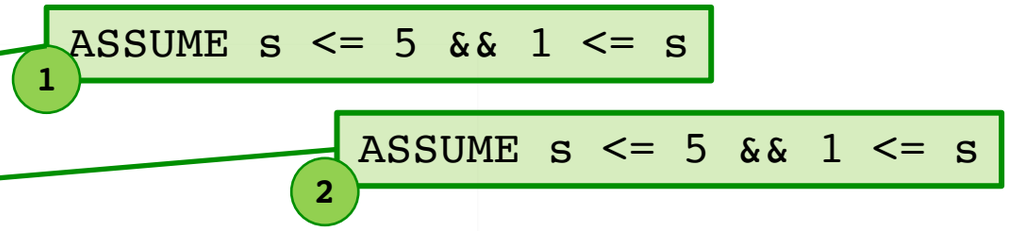
with interval analysis

```
ASSUME s <= 5 && 1 <= s
```

1

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

with interval analysis




```c
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

with interval analysis

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

1 ASSUME s <= 5 && 1 <= s

2 ASSUME s <= 5 && 1 <= s

3 ASSUME s <= 5 && 1 <= s && 6 <= input

with interval analysis

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

1 ASSUME s <= 5 && 1 <= s

2 ASSUME s <= 5 && 1 <= s

3 ASSUME s <= 5 && 1 <= s && 6 <= input

4 ASSUME s == 1 && input == 1

with interval analysis

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

1 ASSUME s <= 5 && 1 <= s

2 ASSUME s <= 5 && 1 <= s

3 ASSUME s <= 5 && 1 <= s && 6 <= input

4 ASSUME s == 1 && input == 1

5 ASSUME s == 2 && input == 2

with interval analysis

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

1 ASSUME s <= 5 && 1 <= s

2 ASSUME s <= 5 && 1 <= s

3 ASSUME s <= 5 && 1 <= s && 6 <= input

4 ASSUME s == 1 && input == 1

5 ASSUME s == 2 && input == 2

6 ASSUME s == 3 && input == 3

with interval analysis

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

1 ASSUME s <= 5 && 1 <= s

2 ASSUME s <= 5 && 1 <= s

3 ASSUME s <= 5 && 1 <= s && 6 <= input

4 ASSUME s == 1 && input == 1

5 ASSUME s == 2 && input == 2

6 ASSUME s == 3 && input == 3

7 ASSUME s == 4 && input == 4

with interval analysis

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return 0;
    } else if (input == 1 && s == 1) {
      s = 2;
    } else if (input == 2 && s == 2) {
      s = 3;
    } else if (input == 3 && s == 3) {
      s = 4;
    } else if (input == 4 && s == 4) {
      s = 5;
    } else if (input == 5 && s > 5) { // unsatisfiable
      __VERIFIER_error(); // property violation
    }
  }
}
```

1 ASSUME s <= 5 && 1 <= s

2 ASSUME s <= 5 && 1 <= s

3 ASSUME s <= 5 && 1 <= s && 6 <= input

4 ASSUME s == 1 && input == 1

5 ASSUME s == 2 && input == 2

6 ASSUME s == 3 && input == 3

7 ASSUME s == 4 && input == 4

8 ASSUME s <= 5 && 1 <= s && input <= 5

with interval analysis

```
unsigned int s = 1;
int main() {
  while (1) {
    unsigned int input = __VERIFIER_nondet_int();
    if (input > 5) {
      return
    } else i
      s = 2;
    } else i
      s = 3;
    } else i
      s = 4;
    } else i
      s = 5;
    } else i
      __VERI
    }
  }
}
```

1 ASSUME s <= 5 && 1 <= s

2 ASSUME s <= 5 && 1 <= s

3 ASSUME s <= 5 && 1 <= s && 6 <= input

ut == 1

ut == 2

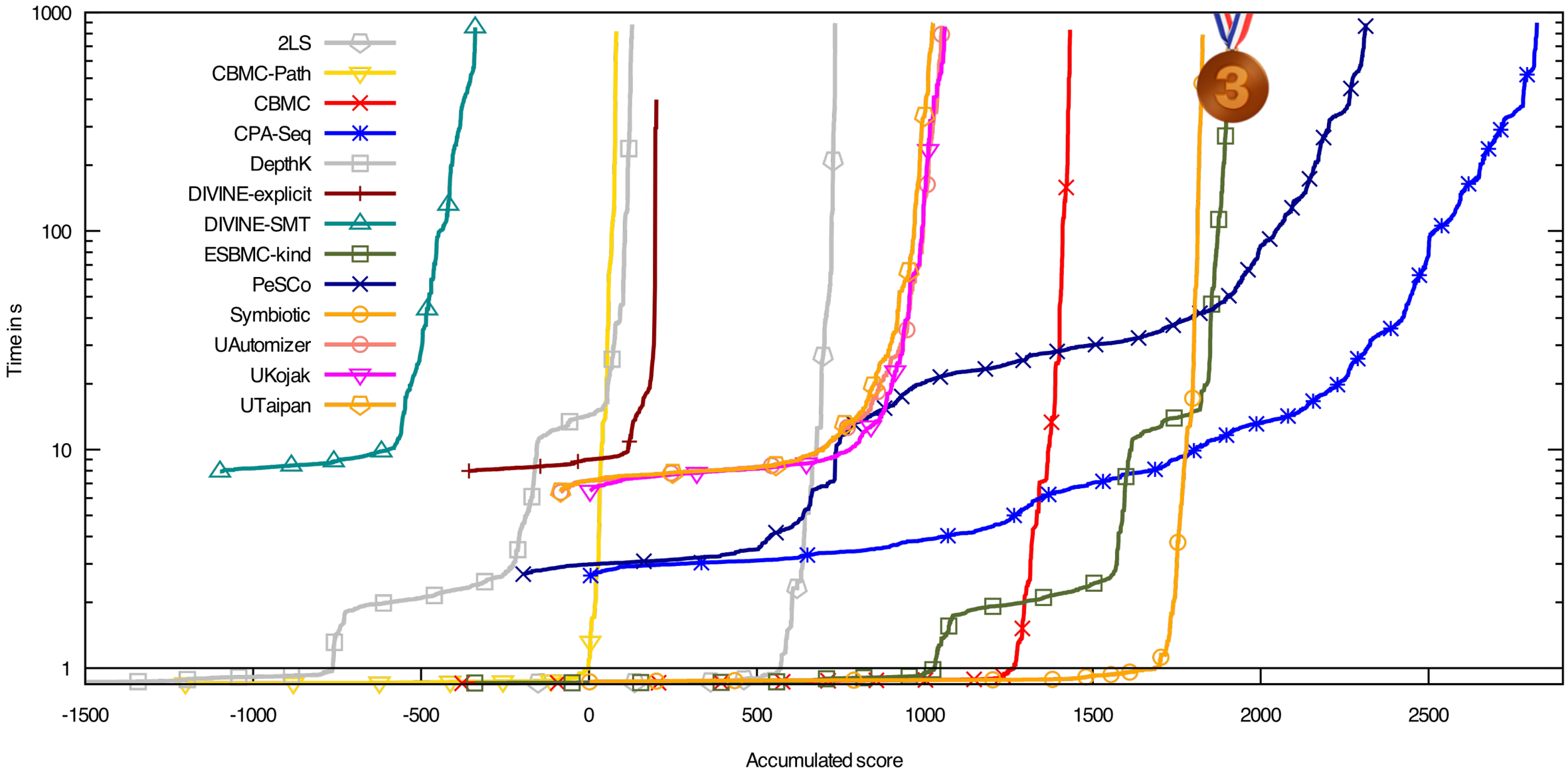
ut == 3

ut == 4

nput <= 5

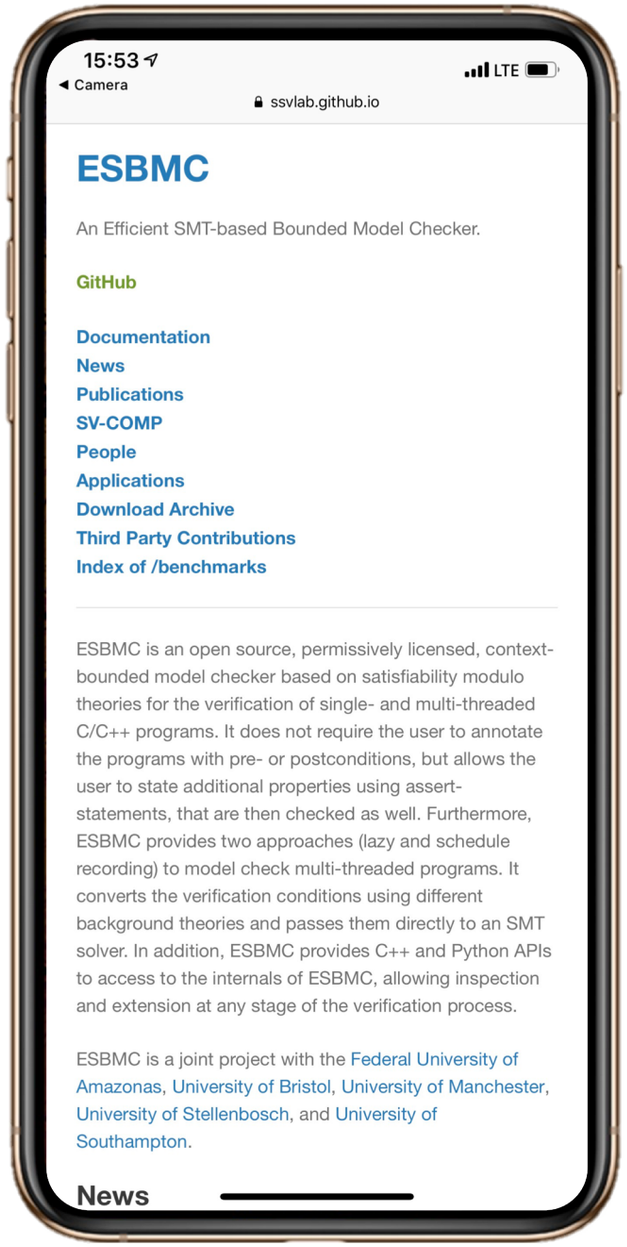
```
*** Checking inductive step
Starting Bounded Model Checking
Unwinding loop 1 iteration 1 file svcomp2018.c line 17 function main
Not unwinding loop 1 iteration 2 file svcomp2018.c line 17 function main
Symex completed in: 0.001s (82 assignments)
Slicing time: 0.000s (removed 22 assignments)
Generated 1 VCC(s), 1 remaining after simplification (60 assignments)
No solver specified; defaulting to Boolector
Encoding remaining VCC(s) using bit-vector arithmetic
Encoding to solver time: 0.000s
Solving with solver Boolector 3.0.0
Encoding to solver time: 0.000s
Runtime decision procedure: 0.001s
BMC program time: 0.003s


VERIFICATION SUCCESSFUL


Solution found by the inductive step (k = 2)
```

Falsification



Thank you!

More information available at http://esbmc.org/



Scan me

JBMC: A Bounded Model Checking Tool for Verifying Java Bytecode

Lucas Cordeiro
Pascal Kesseli
Daniel Kroening
Peter Schrammel
Marek Trtik

diffblue
AI for Code

Computer Aided Verification 2018

Why?

Java and JVM languages:

- Most widely used
- Established software development culture

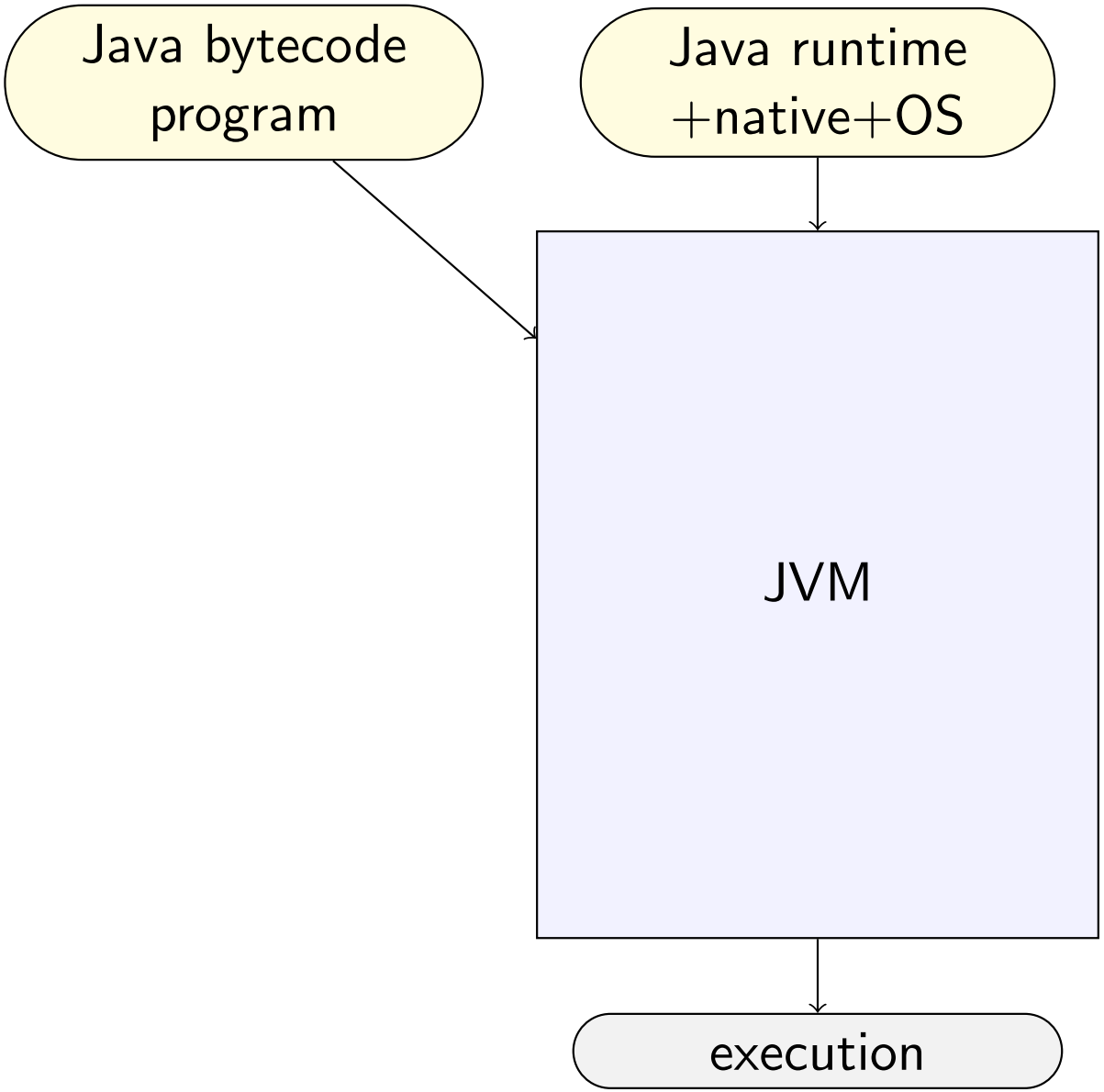
Only few model checking tools available:

- Symbolic JPF (Anand et al, TACAS'07)
- JayHorn (Kahsai et al, CAV'16)

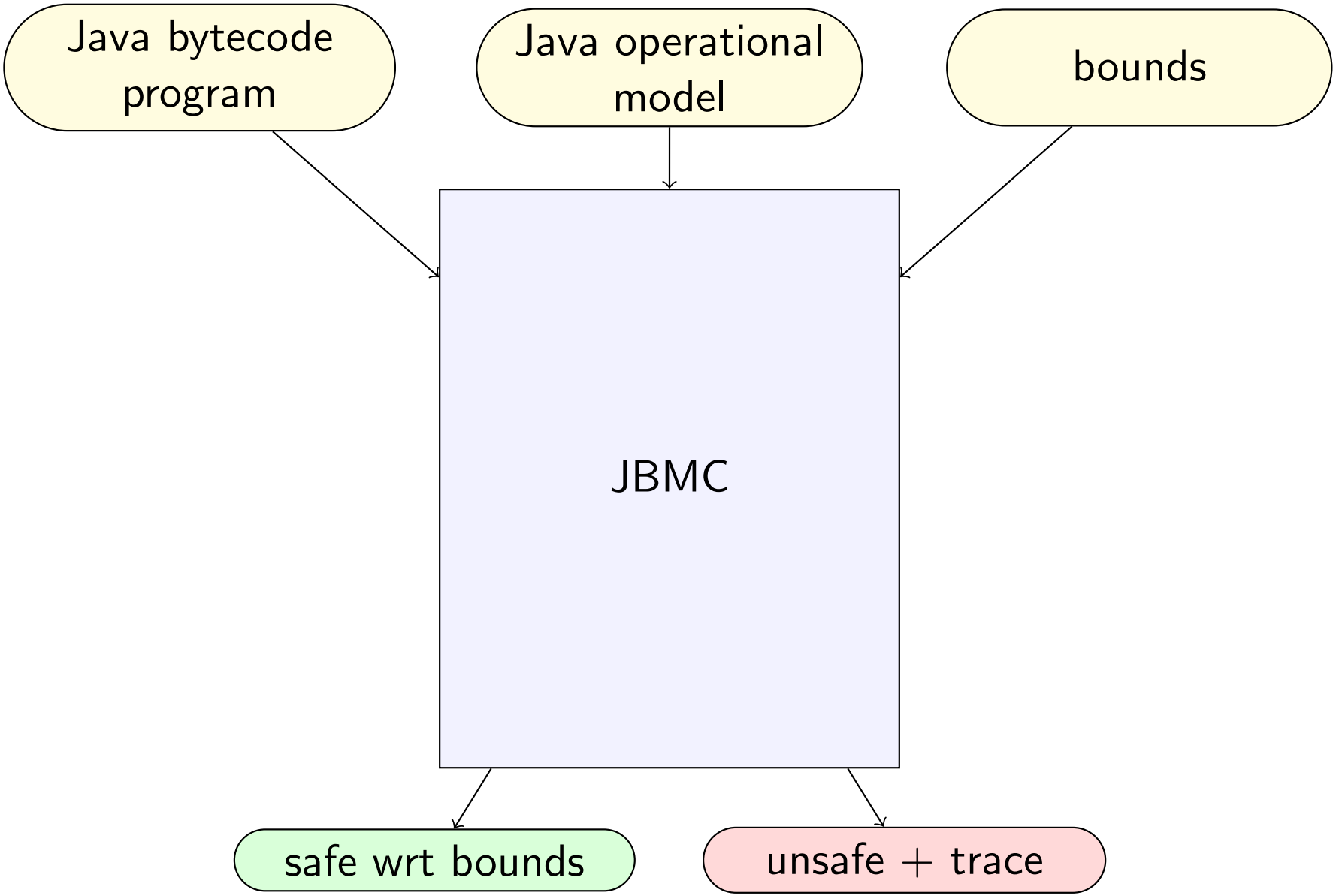
Many applications of BMC:

- Bug finding
- Program synthesis
- Test generation
- ...

JVM vs JBMC

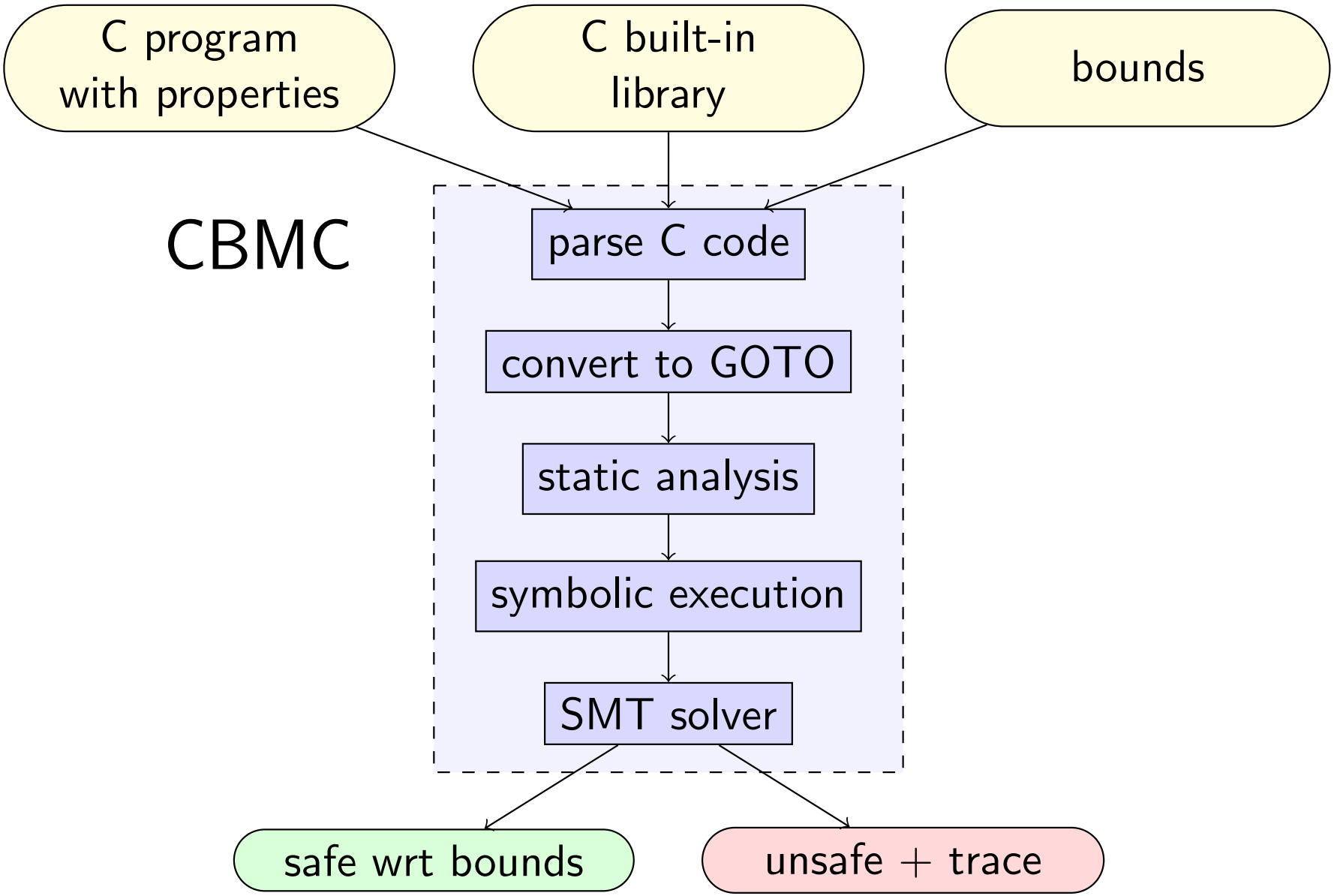

Java bytecode program
Java runtime +native+OS
JVM
execution

JVM vs JBMC


Java bytecode program
Java operational model
bounds
JBMC
safe wrt bounds
unsafe + trace

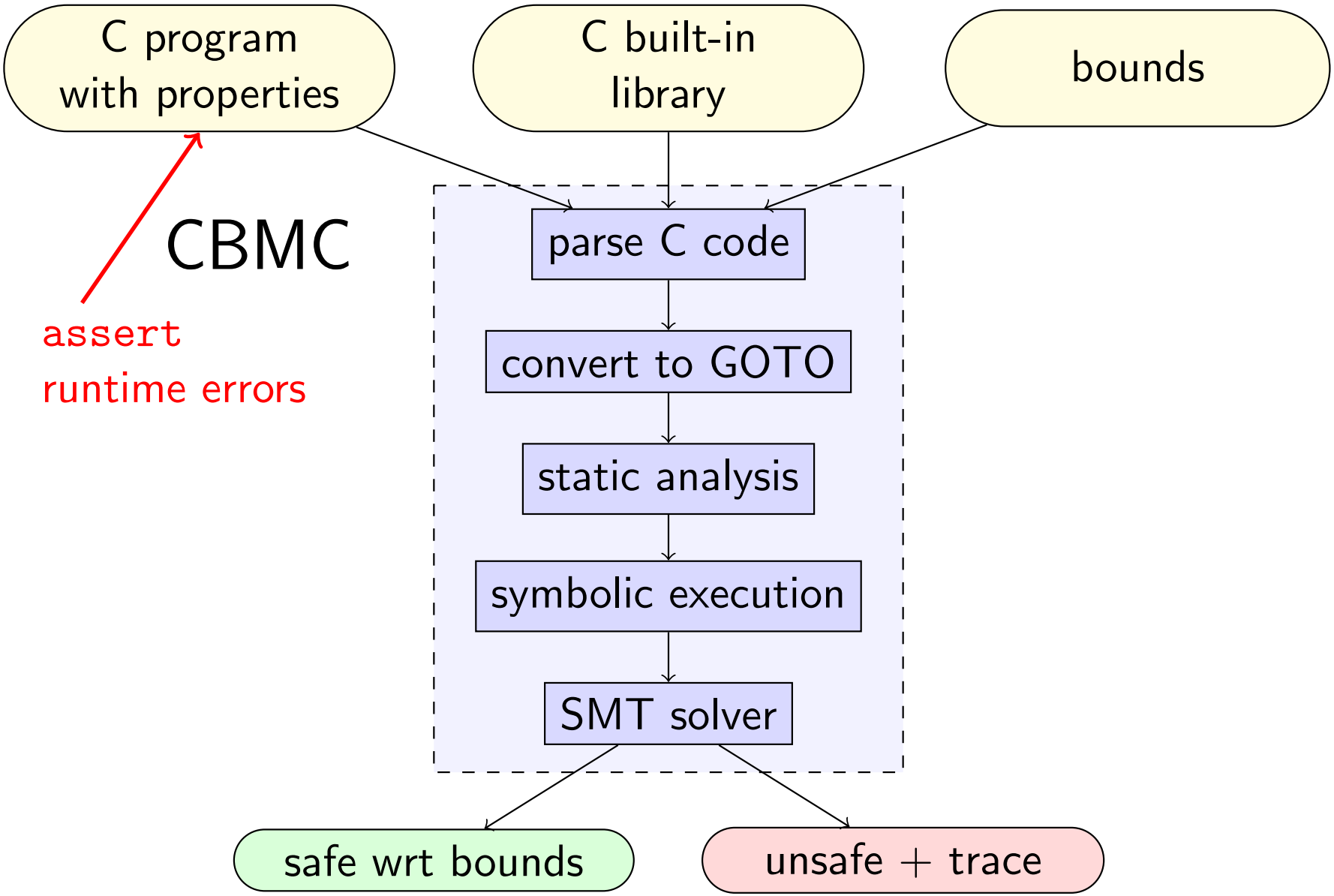
CBMC

Clarke, Kroening & Lerda, TACAS'04



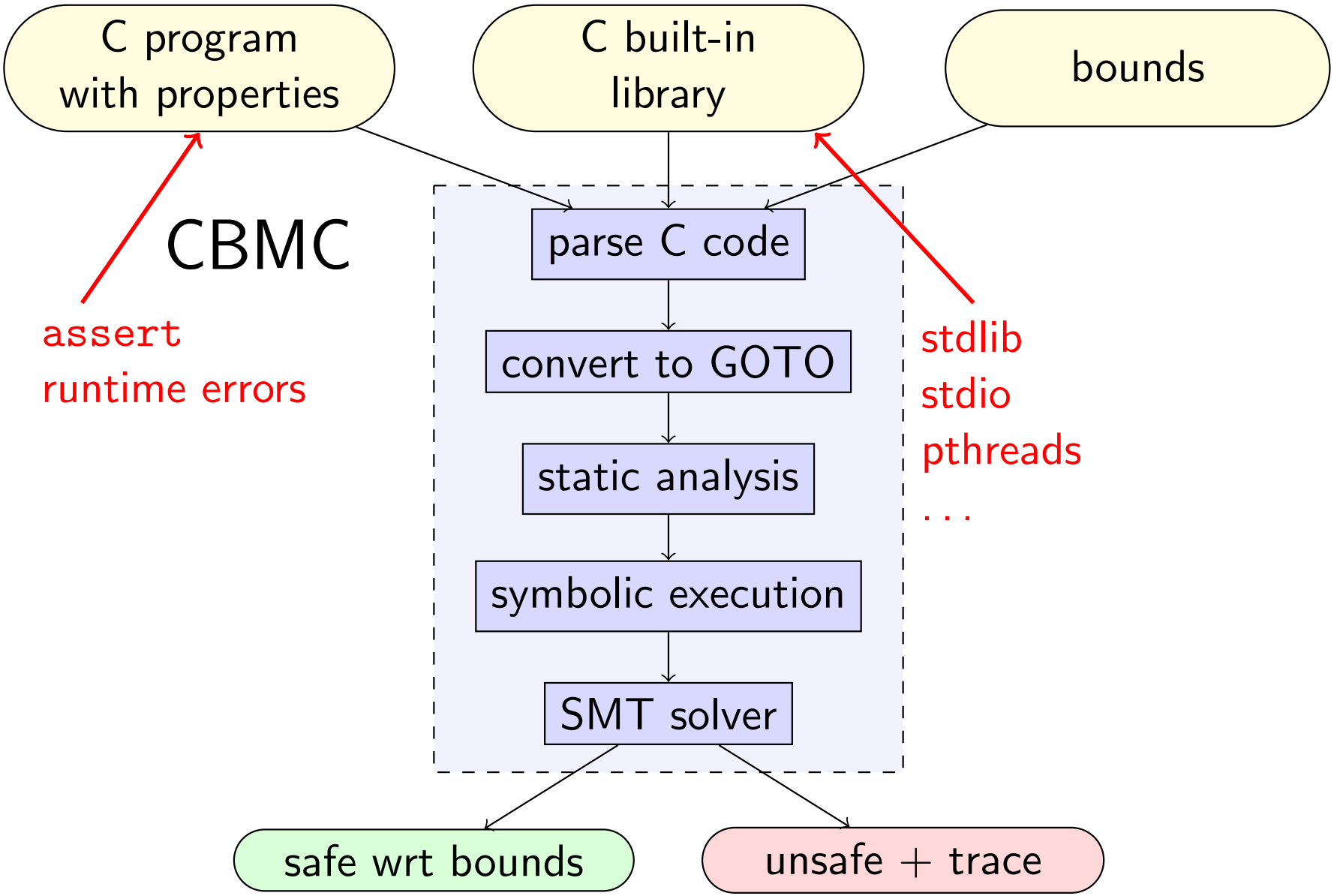
CBMC

Clarke, Kroening & Lerda, TACAS'04



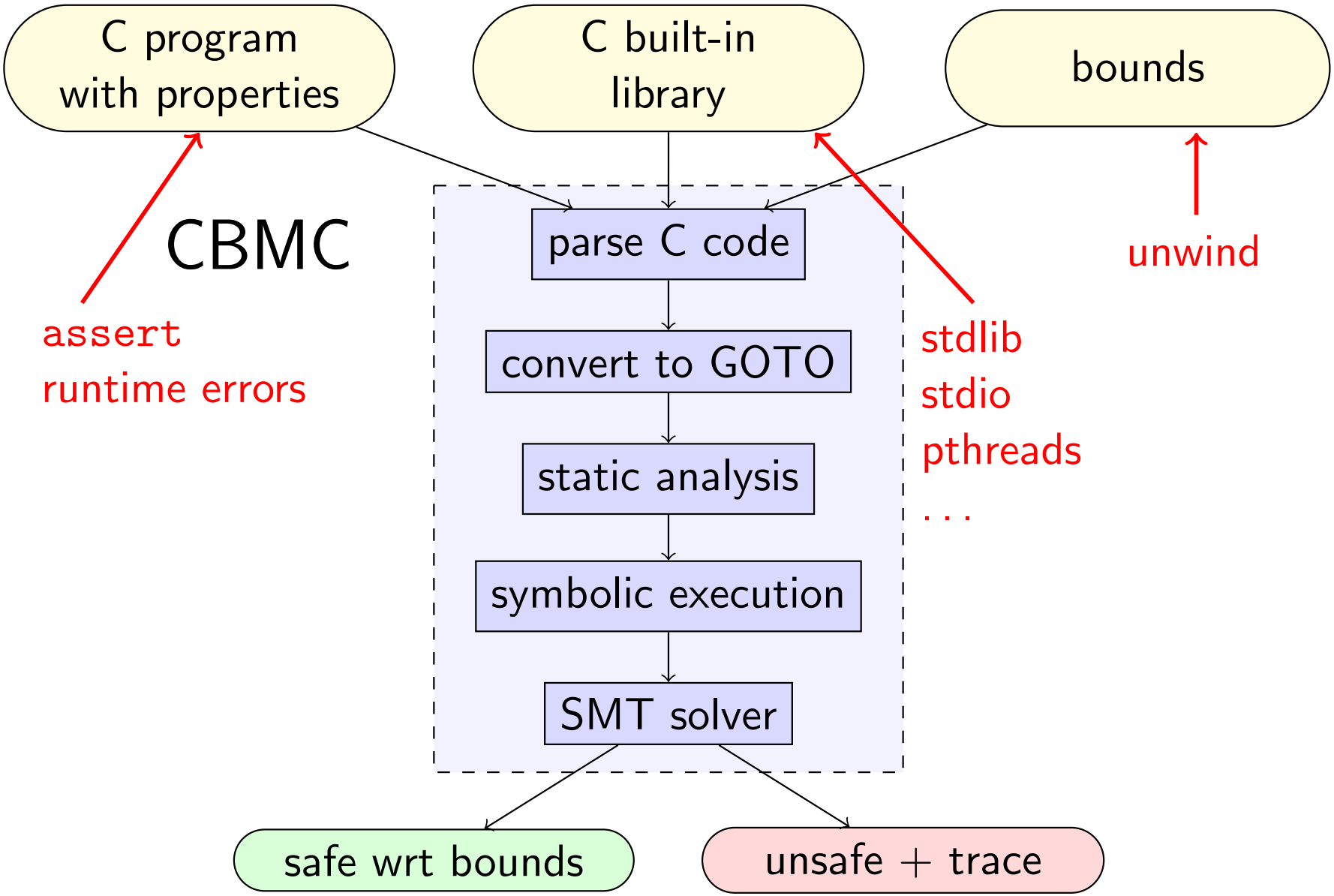
CBMC

Clarke, Kroening & Lerda, TACAS'04



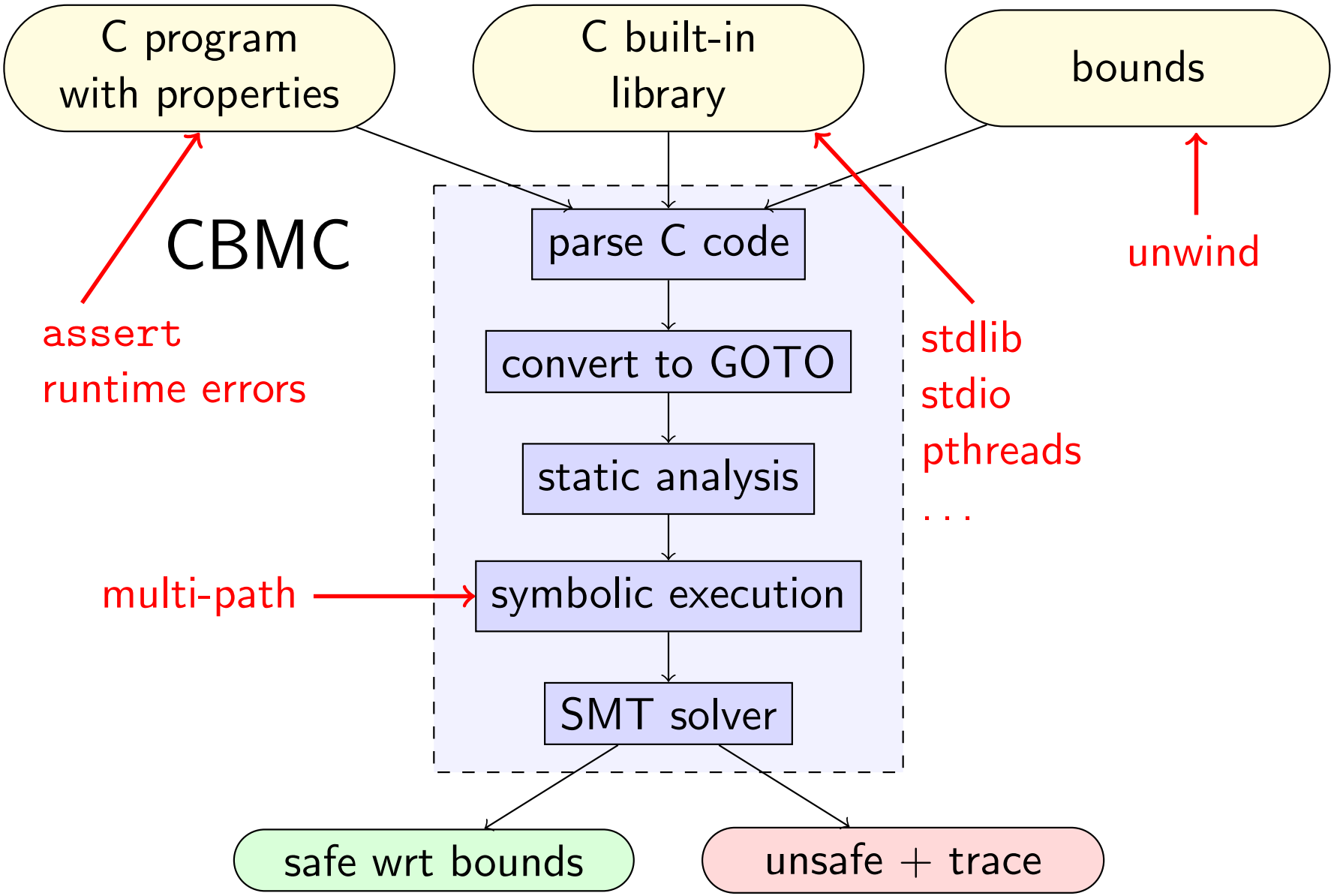
CBMC

Clarke, Kroening & Lerda, TACAS'04



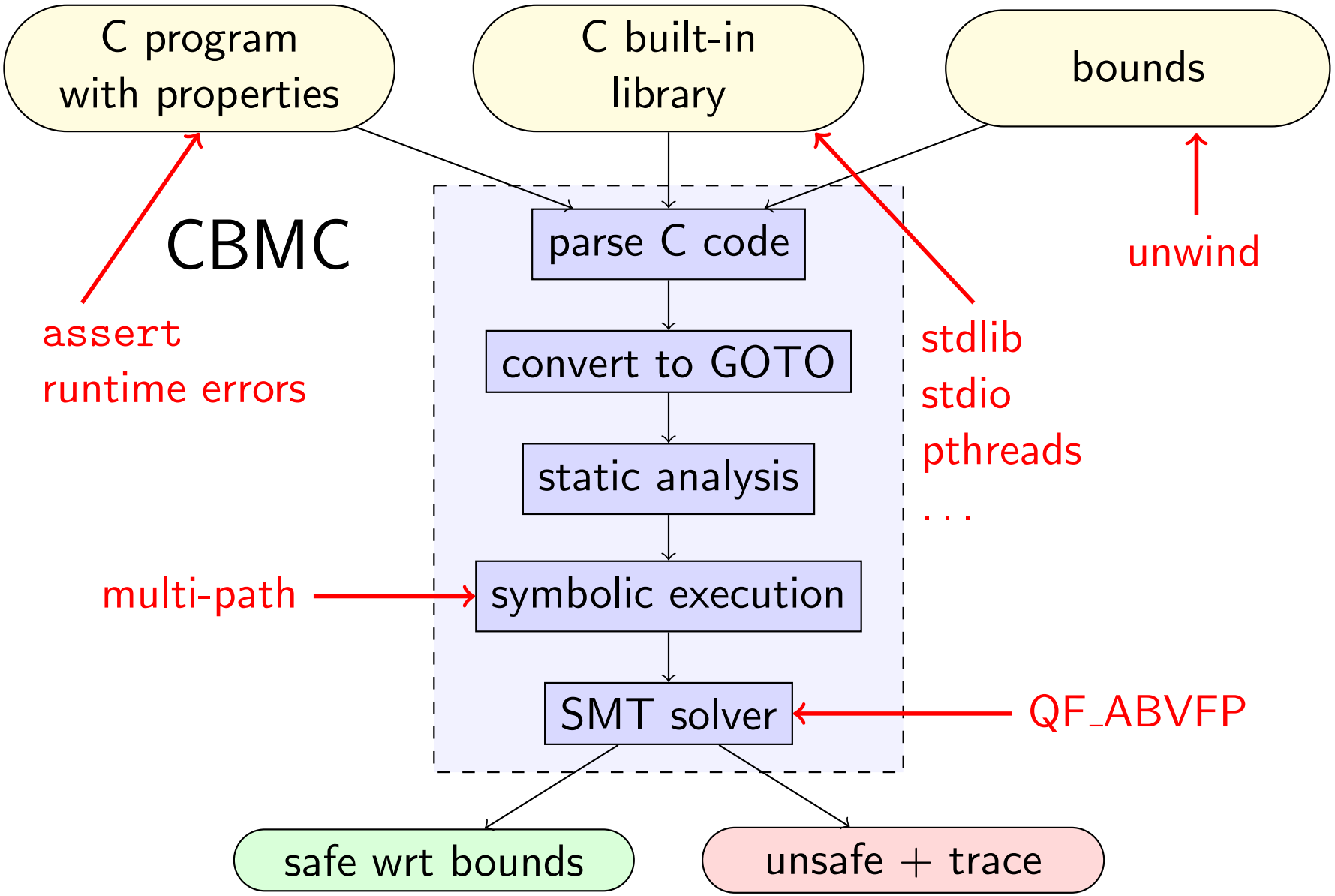
CBMC

Clarke, Kroening & Lerda, TACAS'04

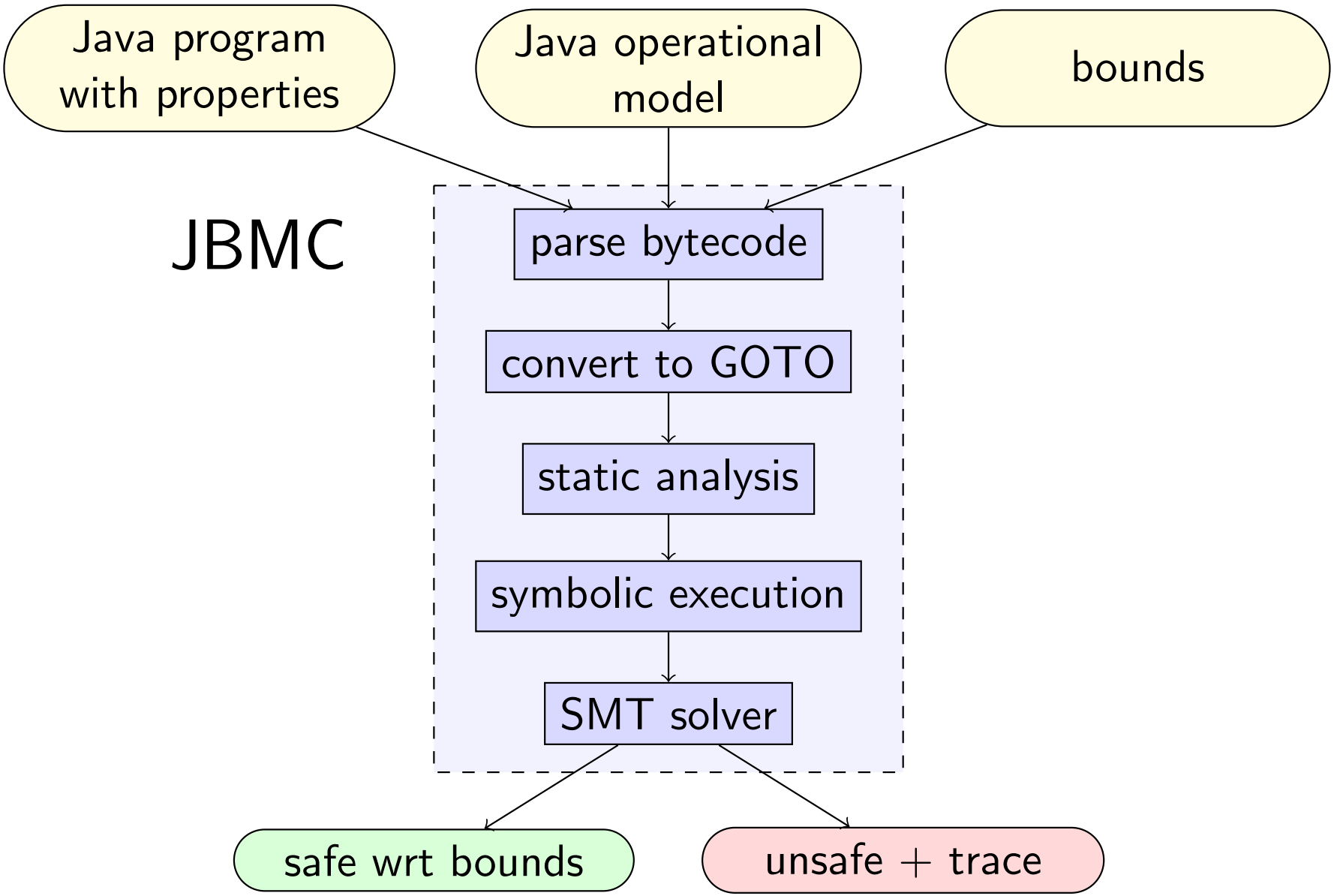


CBMC

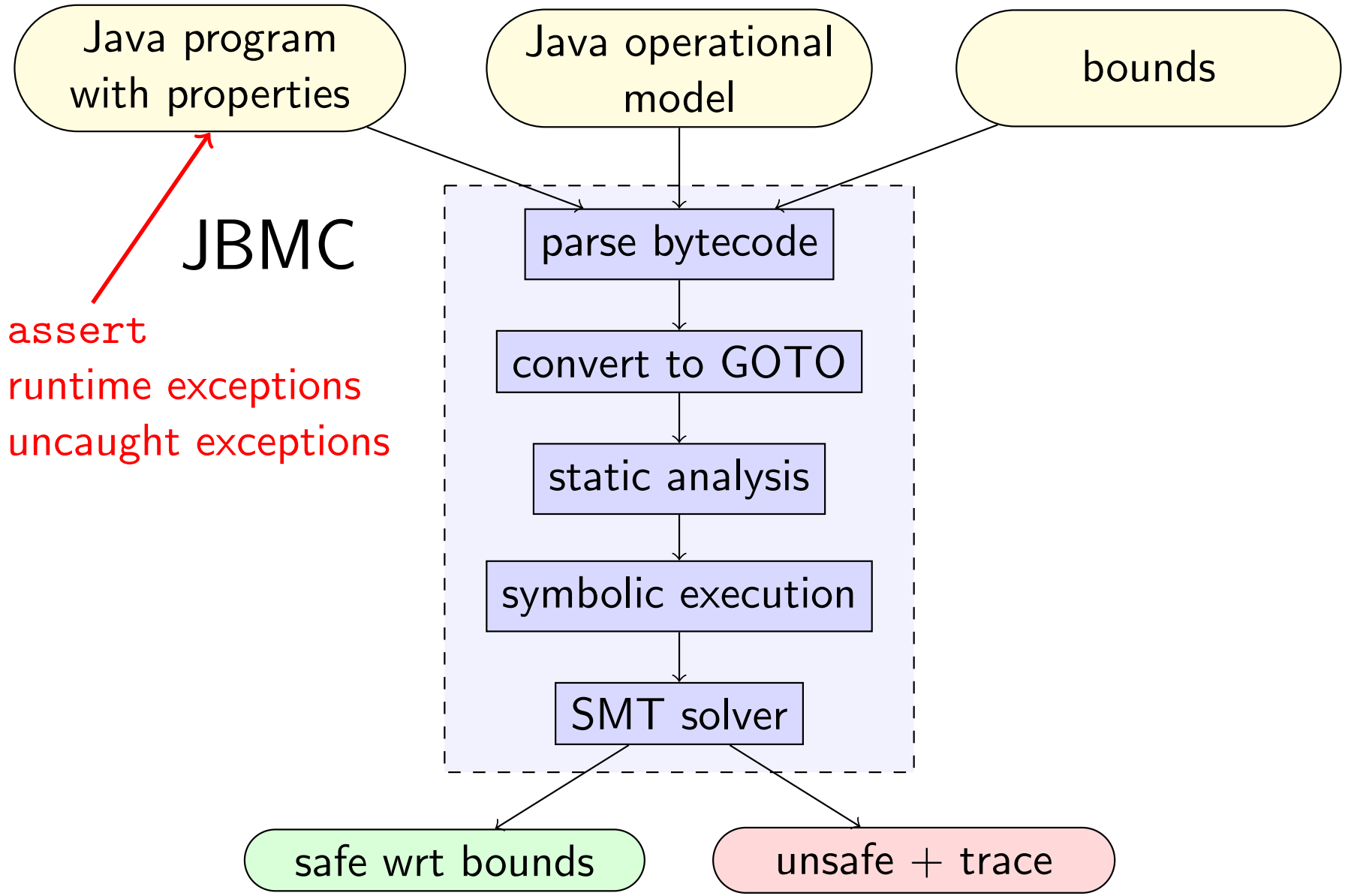
Clarke, Kroening & Lerda, TACAS'04



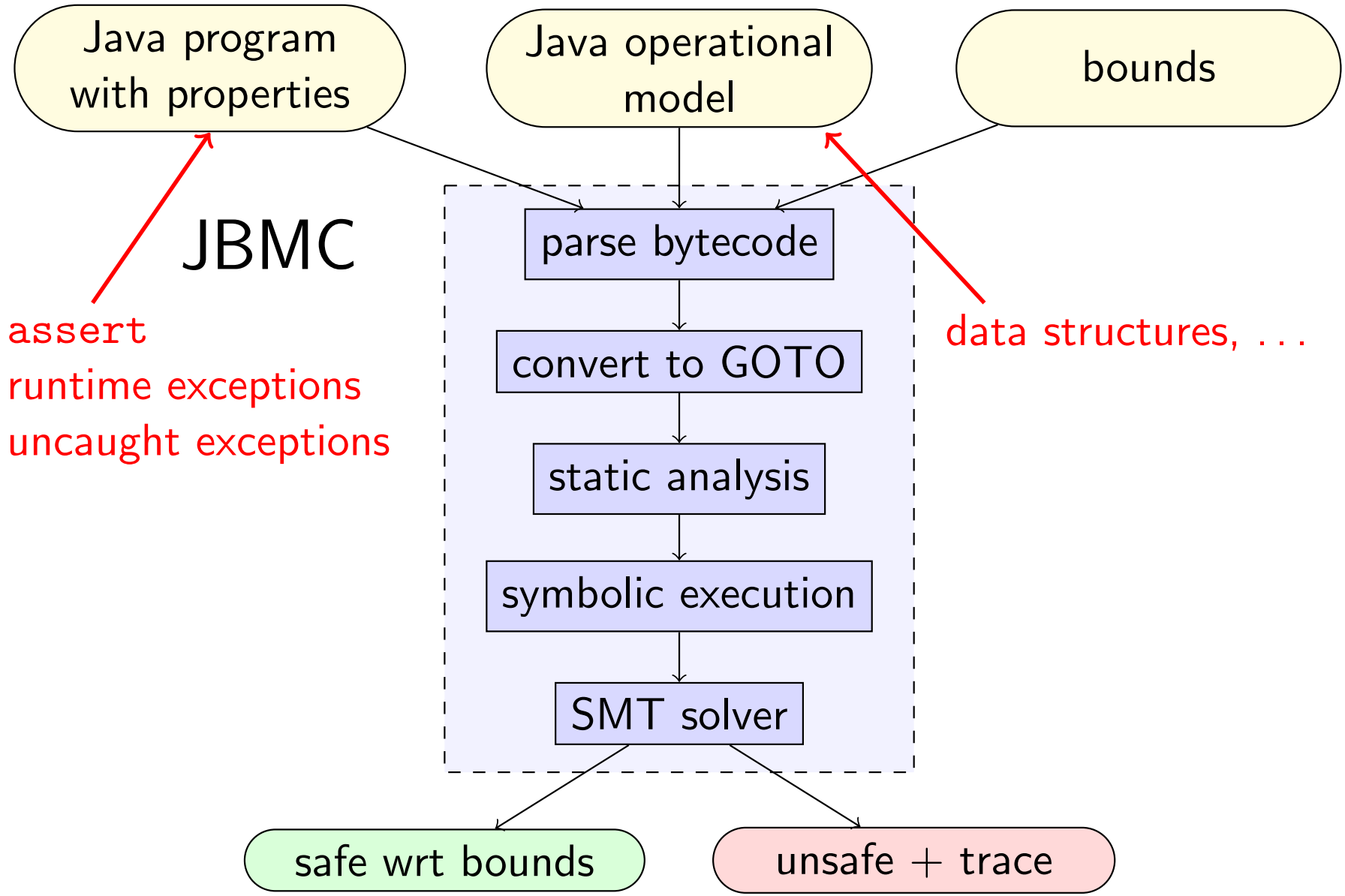
JBMC


Java program with properties
Java operational model
bounds
JBMC
parse bytecode
convert to GOTO
static analysis
symbolic execution
SMT solver
safe wrt bounds
unsafe + trace

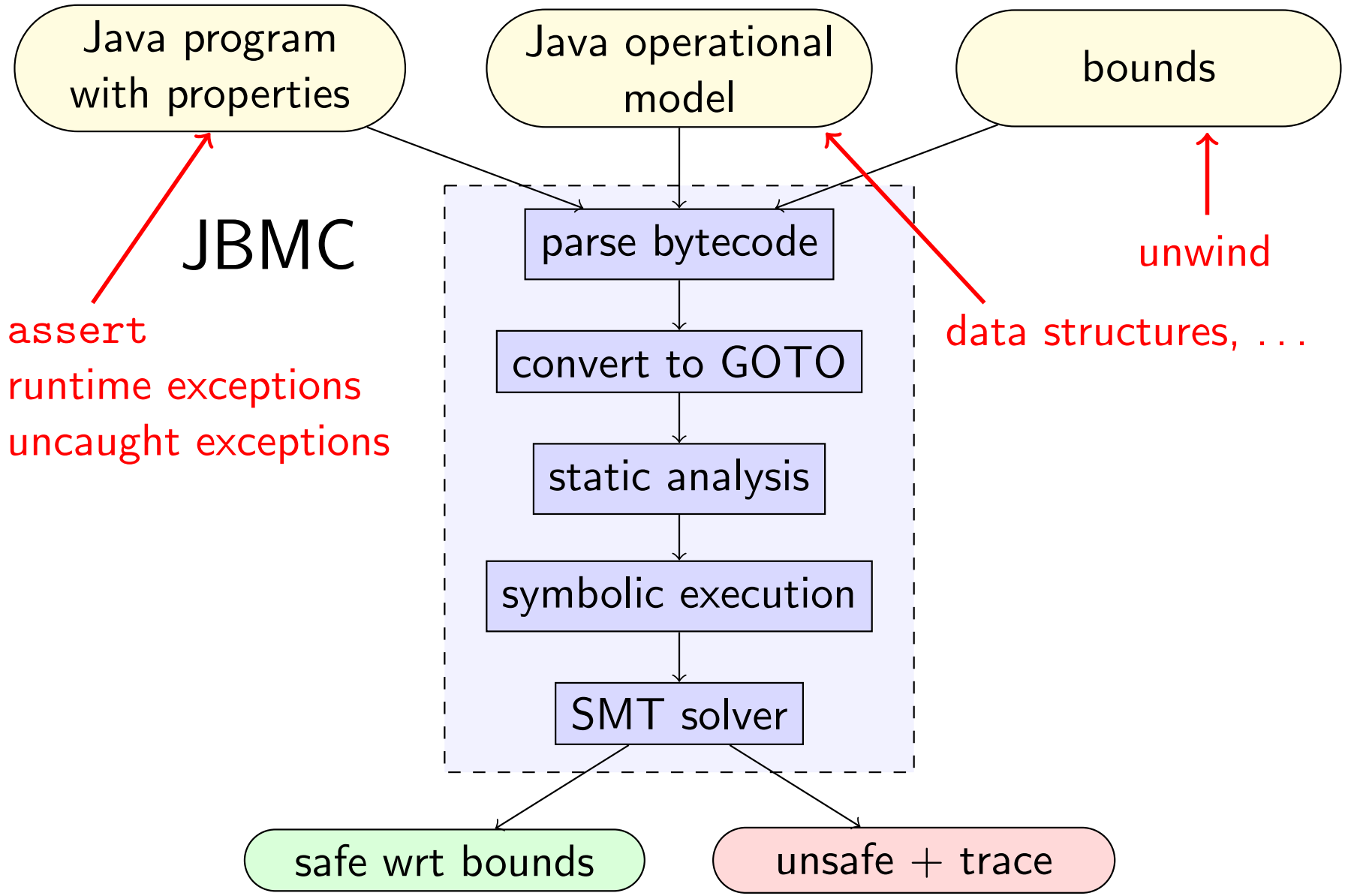
JBMC


Java program with properties
Java operational model
bounds
JBMC
assert
runtime exceptions
uncaught exceptions
parse bytecode
convert to GOTO
static analysis
symbolic execution
SMT solver
safe wrt bounds
unsafe + trace

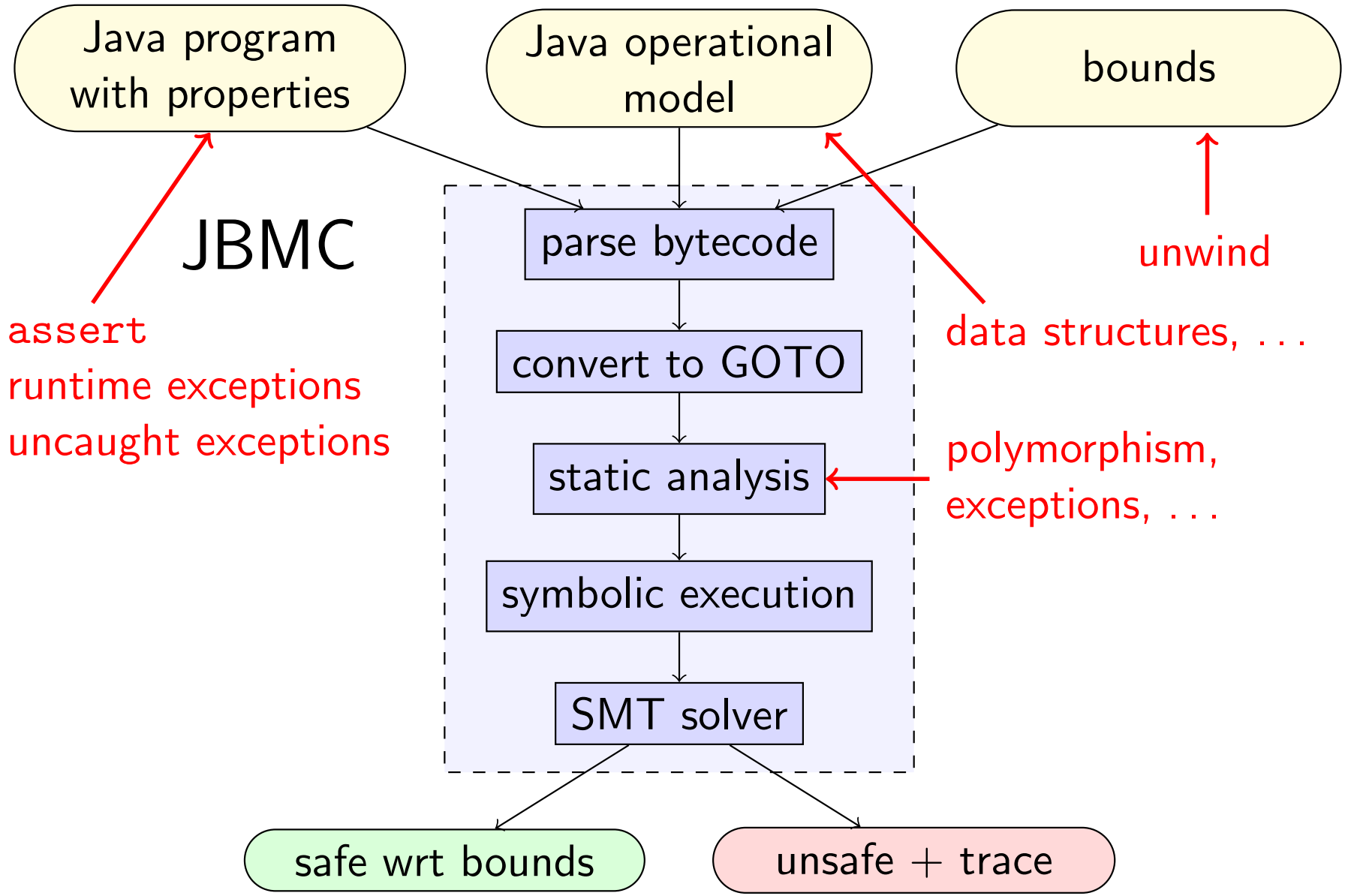
JBMC


Java program with properties
Java operational model
bounds
JBMC
parse bytecode
convert to GOTO
static analysis
symbolic execution
SMT solver
assert
runtime exceptions
uncaught exceptions
data structures, . . .
safe wrt bounds
unsafe + trace

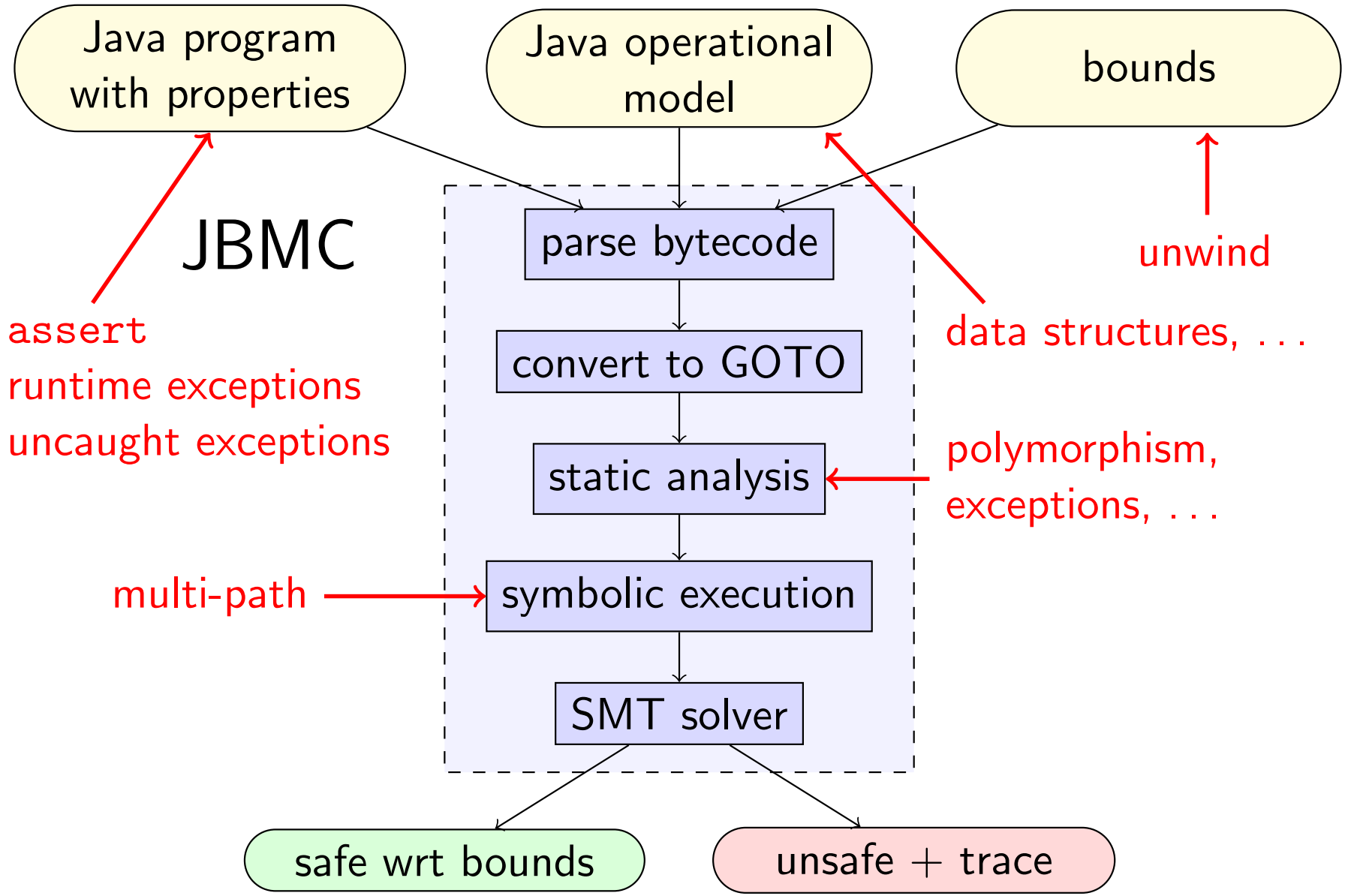
JBMC


Java program with properties
Java operational model
bounds
JBMC
parse bytecode
convert to GOTO
static analysis
symbolic execution
SMT solver
unwind
assert
runtime exceptions
uncaught exceptions
data structures, . . .
safe wrt bounds
unsafe + trace

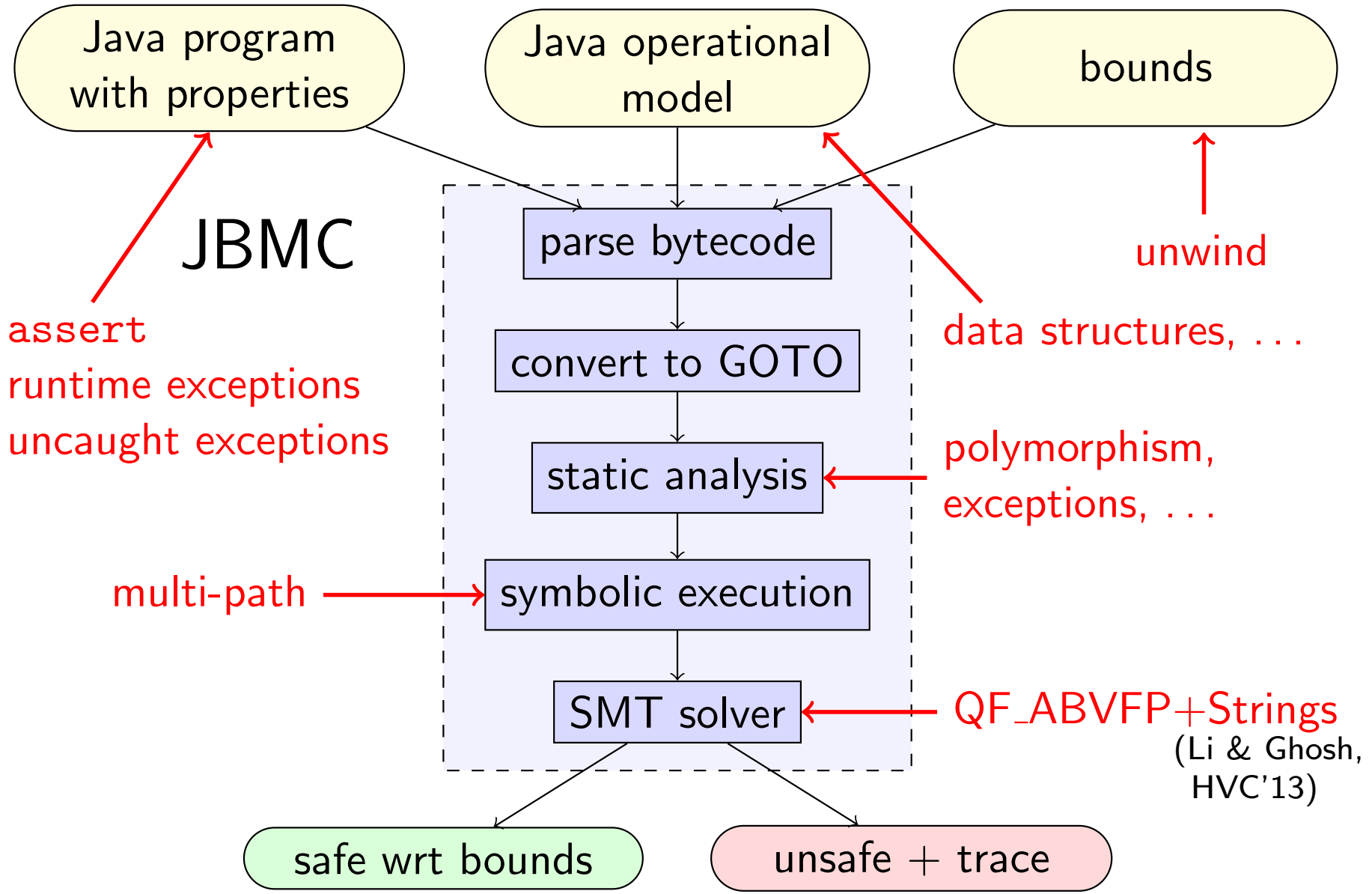
JBMC


Java program with properties
Java operational model
bounds
JBMC
parse bytecode
convert to GOTO
static analysis
symbolic execution
SMT solver
assert
runtime exceptions
uncaught exceptions
data structures, . . .
unwind
polymorphism, exceptions, . . .
safe wrt bounds
unsafe + trace

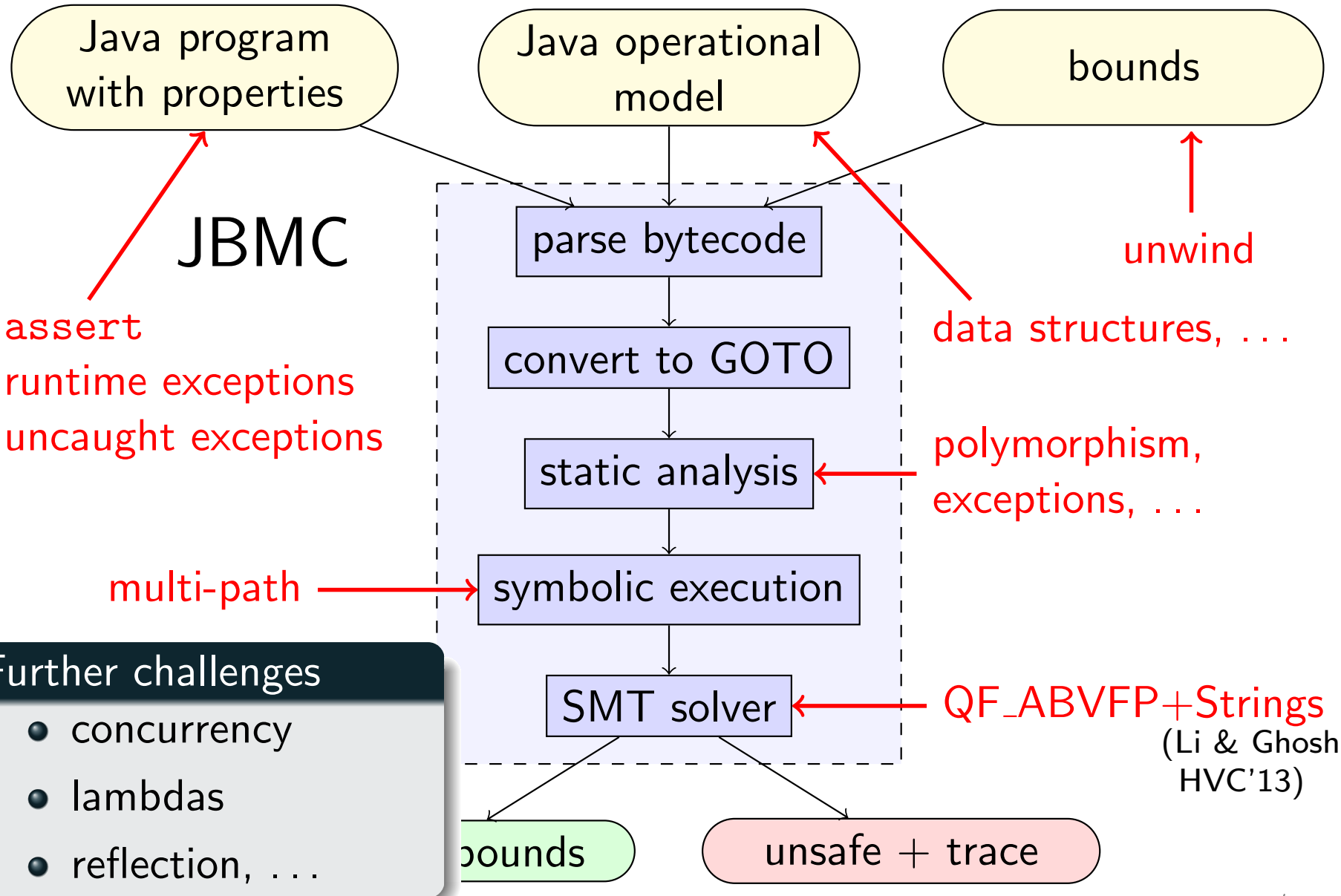
JBMC


Java program with properties
Java operational model
bounds
JBMC
parse bytecode
convert to GOTO
static analysis
symbolic execution
SMT solver
unwind
assert
runtime exceptions
uncaught exceptions
data structures, . . .
polymorphism, exceptions, . . .
multi-path
safe wrt bounds
unsafe + trace

JBMC


Java program with properties
Java operational model
bounds
JBMC
parse bytecode
convert to GOTO
static analysis
symbolic execution
SMT solver
assert
runtime exceptions
uncaught exceptions
data structures, . . .
unwind
polymorphism, exceptions, . . .
multi-path
QF_ABVFP+Strings
(Li & Ghosh, HVC'13)
safe wrt bounds
unsafe + trace

JBMC



Further challenges

- concurrency
- lambdas
- reflection, . . .

JBMC in Action

```
public class Calculator {
  public static void main(String[] args) {
    if(args.length != 3 || args[1].length() != 1) {
      System.err.println("invalid input");
      return;
    }

    int a = Integer.parseInt(args[0]);
    char op = args[1].charAt(0);
    int b = Integer.parseInt(args[2]);

    int result = 0;
    switch(op) {
    case '+': result = a + b; break;
    case '-': result = a - b; break;
    case '*': result = a * b; break;
    case '/': result = a / b; break;
    }

    System.out.println("Result: "+result);
  }
}
```

JBMC in Action

```
$ jbmc Calculator.class --classpath core-models.jar:. --trace
    --throw-runtime-exceptions

...
Runtime decision procedure: 0.980s
...
dynamic_object4={ '-', '0', '4' } // args[0].data
...
dynamic_object6={ '/' }           // args[1].data
...
dynamic_object8={ '0' }           // args[2].data
...
Calculator.java line 17 function Calculator.main
dynamic_object36={... .@class_identifier="java.lang.ArithmeticException" ...}
...
uncaught_exception=&dynamic_object36

Violated property:
  file Calculator.java line 3 function Calculator.main
  no uncaught exception
  uncaught_exception == null
```

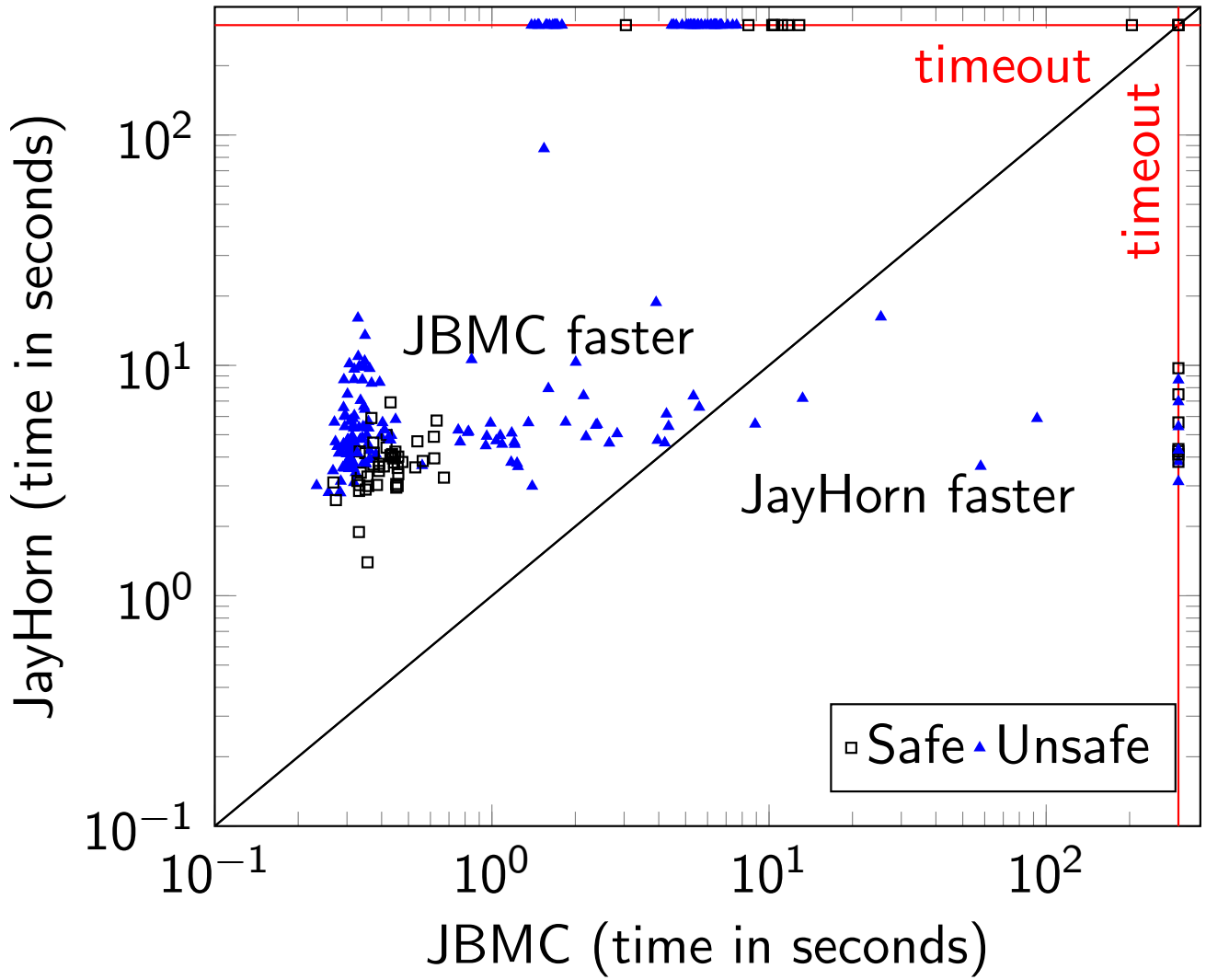
JBMC in Action

```
public class MyTranslator {
  static abstract class Translator {
    abstract String translate(String text);
    static Translator build(String language) {
      if("Chinese".equals(language))
        return new ChineseTranslator();
      return null;
    }
  }
  static class ChineseTranslator extends Translator {
    String translate(String text) {
      if(text.toLowerCase().contains("welcome to oxford"))
        return "欢迎来到牛津";
      return "I don't understand";
    }
  }
  public static void main(String[] args) {
    if(args.length < 2)
      return;
    Translator translator = Translator.build(args[0]);
    if(translator == null)
      return;
    String translatedText = translator.translate(args[1].trim());
    assert(!"欢迎来到牛津".equals(translatedText));
  }
}
```

JBMC in Action

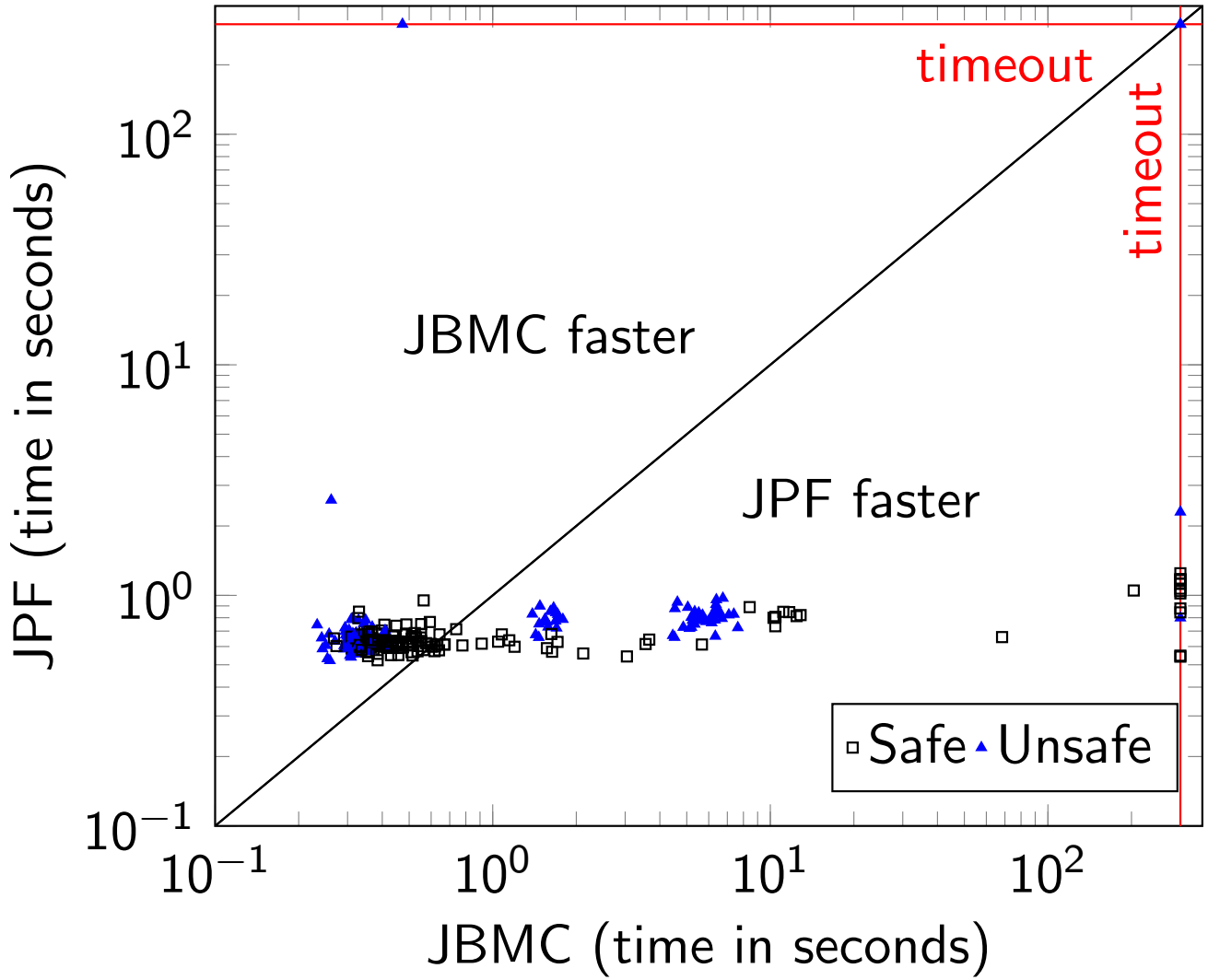
```
$ jbmc MyTranslator.class --classpath core-models.jar:. --trace
    --max-nondet-string-length 50

...
Runtime decision procedure: 0.786s
...
dynamic_object4={ 'C', 'h', 'i', 'n', 'e', 's', 'e' } // args[0].data
...
dynamic_object6={ '\u0010', '\u0017', 'w', 'e',
                  'w', 'E', 'L', 'C', 'O', 'M', 'E', ' ',
                  'T', 'o', ' ',
                  'o', 'x', 'f', 'o', 'r', 'D',
                  'x', 'x', 'x', 'x', 'x', 'x', 'x', 'x', 'x', 'x', 'x',
                  'x', ' ', ' ' }                    // args[1].data
...

Violated property:
  assertion at file MyTranslator.java line 31 function MyTranslator.main
```

Comparison with JayHorn 0.5.1



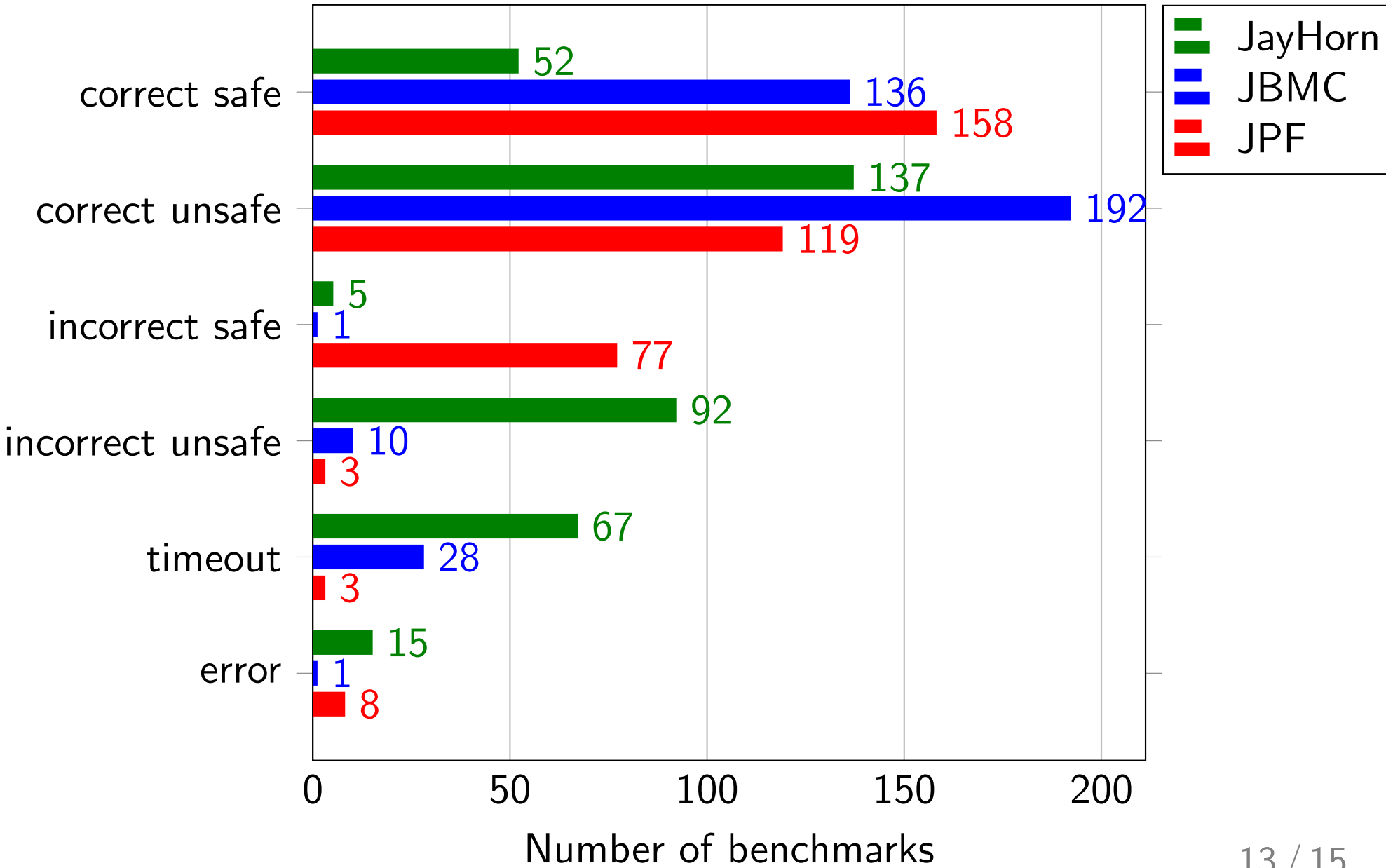
timeout 300s, 15GB

Comparison with JPF v32

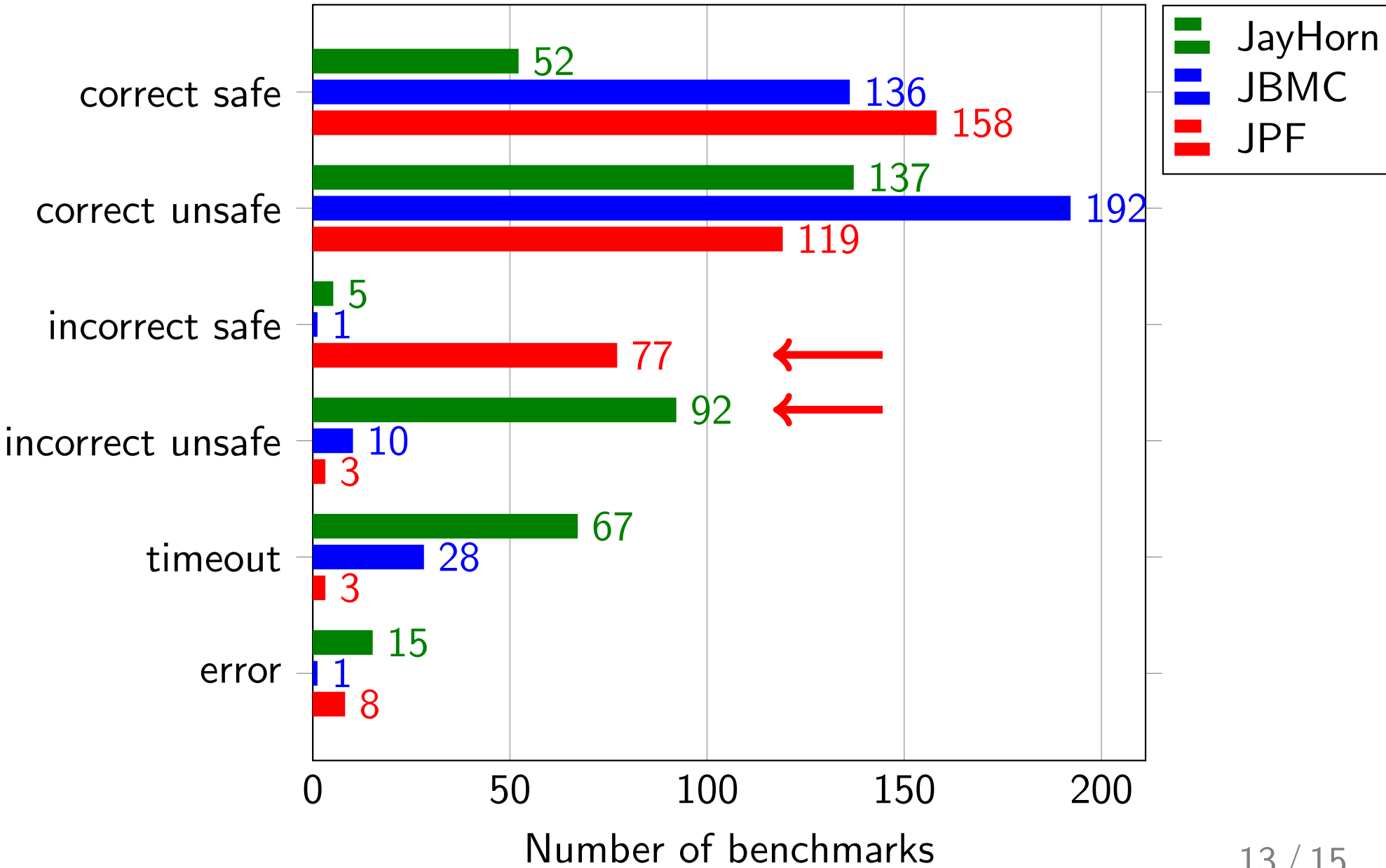


timeout 300s, 15GB

Comparison


JayHorn
JBMC
JPF
correct safe
52
136
158
correct unsafe
137
192
119
incorrect safe
5
1
77
incorrect unsafe
92
10
3
timeout
67
28
3
error
15
1
8
0
50
100
150
200
Number of benchmarks

Comparison


JayHorn
JBMC
JPF
correct safe
52
136
158
correct unsafe
137
192
119
incorrect safe
5
1
77
incorrect unsafe
92
10
3
timeout
67
28
3
error
15
1
8
0
50
100
150
200
Number of benchmarks

Java @ TACAS SV-COMP 2019

Objectives:

- More languages in SV-COMP
- Standard benchmark set
- Comparability
- Reproducibility
- Re-use existing benchmarking infrastructure
  3 tools already integrated: JPF, Jayhorn, JBMC

Tool	JPF 32				JayHorn 5.1				JBMC 5.8			
Limits	timelimit: 180 s, memlimit: 15000 MB, CPU core limit: 8											
Host	localhost											
OS	Linux 4.4.0-116-generic x86_64											
System	CPU: Intel Core i7-6700HQ CPU @ 2.60GHz with 8 cores, frequency: 3500 MHz, Turbo Boost enabled: RAM: 16014056 kB											
Date of execution	2018-03-10 14:09:57 GMT				2018-03-10 11:39:56 GMT				2018-03-10 16:54:00 GMT			
Run set	jpf.sv-comp18				jayhorn.sv-comp18				jbmc.sv-comp18			
../git-sv-benchmarks/java/	status	cputime	walltime	memUsage	status	cputime	walltime	memUsage	status	cputime	walltime	memUsage
jbmc-regression/ArithmeticException1_false-assert.jar	false(reach)	1.252s	0.492s	86929408	true	3.748s	1.352s	203776000	false(reach)	0.207s	0.224s	17719296
jbmc-regression/ArithmeticException5_true-assert.jar	false(reach)	1.208s	0.479s	85815296	true	3.564s	1.169s	184410112	true	0.212s	0.226s	12709888
jbmc-regression/ArithmeticException6_false-assert.jar	true	1.172s	0.452s	82345984	true	3.448s	1.164s	174071808	false(reach)	0.251s	0.264s	17477632
jbmc-regression/ArrayIndexOutOfBoundsException1_false-assert.jar	true	1.164s	0.475s	83734528	false(reach)	6.516s	1.934s	228200448	false(reach)	0.256s	0.268s	16728064
jbmc-regression/ArrayIndexOutOfBoundsException2_false-assert.jar	true	1.140s	0.443s	81854464	false(reach)	6.748s	2.030s	230465536	false(reach)	0.250s	0.257s	18628608
jbmc-regression/ArrayIndexOutOfBoundsException3_false-assert.jar	true	1.256s	0.501s	82014208	false(reach)	7.204s	2.023s	233783296	false(reach)	0.223s	0.236s	18378752
jbmc-regression/BufferedReaderReadLine_false-assert.jar	true	1.208s	0.464s	85073920	false(reach)	11.484s	2.992s	264642560	false(reach)	0.373s	0.381s	29315072
jbmc-regression/CharSequenceBug_false-assert.jar	true	1.136s	0.444s	79372288	false(reach)	7.336s	2.191s	233238528	false(reach)	1.407s	1.422s	26890240
jbmc-regression/CharSequenceToString_true-assert.jar	true	1.136s	0.501s	80220160	false(reach)	7.412s	2.186s	235905024	true	1.047s	1.059s	36552704
jbmc-regression/ClassCastException1_false-assert.jar	false(reach)	1.172s	0.462s	86487040	false(reach)	6.124s	1.898s	230752256	false(reach)	0.305s	0.319s	18665472
jbmc-regression/ClassCastException2_true-assert.jar	true	1.216s	0.506s	77561856	true	6.512s	1.907s	233218048	true	0.233s	0.253s	13275136
jbmc-regression/ClassCastException3_false-assert.jar	false(reach)	1.148s	0.441s	84291584	false(reach)	6.852s	2.080s	233496576	false(reach)	0.235s	0.242s	17874944
jbmc-regression/Class_method1_true-assert.jar	true	1.160s	0.457s	80973824	true	5.148s	1.699s	236470272	true	0.236s	0.256s	13553664
jbmc-regression/Inheritance1_true-assert.jar	true	1.188s	0.482s	83697664	true	7.240s	2.046s	230694912	true	0.269s	0.276s	13492224
jbmc-regression/NegativeArraySizeException1_false-assert.jar	false(reach)	1.236s	0.492s	87400448	true	3.720s	1.230s	185139200	false(reach)	0.213s	0.225s	16687104
jbmc-regression/NegativeArraySizeException2_false-assert.jar	false(reach)	1.176s	0.472s	83542016	true	3.908s	1.245s	185753600	false(reach)	0.241s	0.247s	18546688
jbmc-regression/NullPointerException1_false-assert.jar	false(reach)	1.152s	0.440s	84705280	unknown	1.864s	0.686s	136036352	false(reach)	0.243s	0.245s	16834560
jbmc-regression/NullPointerException2_false-assert.jar	false(reach)	1.212s	0.466s	85893120	unknown	1.740s	0.676s	134901760	false(reach)	0.244s	0.251s	16654336
jbmc-regression/NullPointerException3_false-assert.jar	false(reach)	1.368s	0.532s	88522752	unknown	1.668s	0.616s	133578752	false(reach)	0.243s	0.255s	16982016
jbmc-regression/NullPointerException4_false-assert.jar	false(reach)	1.272s	0.535s	77479936	unknown	1.636s	0.626s	128970752	false(reach)	0.240s	0.248s	18771968
jbmc-regression/RegexMatches01_true-assert.jar	true	1.308s	0.537s	80244736	false(reach)	8.344s	2.393s	246411264	true	1.280s	1.301s	39464960
jbmc-regression/RegexMatches02_false-assert.jar	true	1.192s	0.481s	85598208	false(reach)	9.824s	2.621s	254140416	false(reach)	1.033s	1.044s	64978944
jbmc-regression/RegexSubstitution01_true-assert.jar	true	1.252s	0.503s	83034112	false(reach)	14.268s	3.535s	394878976	true	72.774s	72.858s	757309440
jbmc-regression/RegexSubstitution02_false-assert.jar	true	1.116s	0.442s	81616896	false(reach)	17.136s	4.284s	390799360	true	40.973s	41.011s	526553088
jbmc-regression/RegexSubstitution03_true-assert.jar	true	1.232s	0.465s	86216704	false(reach)	12.680s	3.158s	338685952	true	1.754s	1.766s	46804992
jbmc-regression/StaticCharMethods01_true-assert.jar	true	1.176s	0.502s	82903040	false(reach)	6.744s	2.041s	228970496	true	0.261s	0.276s	14376960
jbmc-regression/StaticCharMethods02_false-assert.jar	true	1.196s	0.484s	83034112	false(reach)	8.524s	2.476s	237215744	false(reach)	0.829s	0.854s	50974720
jbmc-regression/StaticCharMethods03_false-assert.jar	true	1.192s	0.486s	82206720	false(reach)	10.188s	2.888s	243417088	false(reach)	0.847s	0.858s	50806784
jbmc-regression/StaticCharMethods04_false-assert.jar	true	1.196s	0.487s	84496384	false(reach)	8.664s	2.529s	231280640	false(reach)	0.821s	0.829s	51412992

Timeline:

- September 2018: Contribution of benchmarks
- October 2018: Tool submission

Watch out on `sv-comp.sosy-lab.org`
Subscribe to `sv-comp@googlegroups.com`

Download JBMC and contribute!

`www.cprover.org/jbmc`

Java @ TACAS SV-COMP 2019

- Contribute and participate!
- `sv-comp.sosy-lab.org`

`www.diffblue.com`

- Jobs in program analysis, verification and machine learning!