

Design and Implementation [30%]														
Level	System Modelling (10%)				Level	System Development (10%)				Level	Use of literature to support development (10%)			
Fail	Section missing, or unable to meet the next rubric level.				Fail	Section missing, or unable to meet the next rubric level.				Fail	No use of literature, or unable to meet the next rubric level.			
Unsatisfactory	Methods used maybe overly simplistic and/or not suitable for the intended application.				Unsatisfactory	Development is significantly lacking, and it is clear that little actual effort has been expended on the project.				Unsatisfactory	There is some attempt to reference where supporting material is used but it is unclear what aspects of the project are original work and which are from external material.			
40% (Pass)	Student demonstrates the use of simulation and/or analytical methods to model/design their project and makes an attempt at discussing the limitations. Analysis may be simplistic in application and demonstrate little to no original thought beyond that of reference materials (e.g. software tutorials). LO8. Select and apply appropriate engineering tools to model and solve complex electrical and electronics engineering problems. C3. Select and apply appropriate computational and analytical techniques to model complex problems, recognising the limitations of the techniques employed.				40% (Pass)	Student demonstrates ability to take designed and modelled system into a ‘real world’ environment (note this may be through an actual build or a further refined simulation). Expected challenges from this process are discussed and compensated for. LO9. Apply a set of engineering principles to solve complex problems, where some of the knowledge is at the forefront of the topic. C1. Apply knowledge of mathematics, statistics, natural science and engineering principles to the solution of complex problems. Some of the knowledge will be at the forefront of the particular subject of study.				40% (Pass)	Student makes clear referenced use of sources to support their project development. May be from poor sources or focussing on a single type (e.g. website). IEEE/IET style is, by-in-large, followed correctly. O1. Identify and evaluate the literature to address a complex electrical and electronic engineering problem. C4. Select and evaluate technical literature and other sources of information to address complex problems.			
+++	Clear discussion of the limitations and/or trade-offs of the techniques with reference to the project specification, application area, and wider context.				+++	The development makes use of bespoke methods, created by the student, to meet the project specifications..				++++	Student clearly identifies how they have adopted and/or adapted the referenced material to add value to their project.			
	Not present	Some	Most	Fully met		Exceeded								
+++	The methods / tools / techniques that are used are justified with respect to the project specification, application area, resources, and wider context.				+++	Student has demonstrated competency with a range of tools throughout the development of their project.				++	All materials are from high quality sources (e.g. manufacturer, peer reviewed, textbook) and a range of types (datasheets, app notes, tutorials, code samples, etc).			
+++++	Demonstrate application of the methods / tools / techniques to successfully address the project specifications.				+++	The development shows a clear progression of their methodology adhering to engineering principles. All changes from the original methodology are both explained and justified well.				++	Limitations of the referenced sources are considered with respect to the project needs, application area, and specification requirements.			
					+++	Clear evidence of sub-system testing to validate individual components before integration.				++++	Referencing is used consistently throughout the implementation section to support all aspects of project design and development.			