

Q1.

<p>(a)</p> <p># assumed signed</p> <p>sra \$14, \$12, 7</p>	<p>(b)</p> <p>#initialise 16 to be used</p> <p>addi \$8, \$0, 16</p> <p>#move to FP register</p> <p>mtc1 \$8, \$f8</p> <p>#convert to double</p> <p>cvt.d.w \$f8, \$f8</p> <p>#multiply and put to register</p> <p>mul.d \$f10, \$f12, \$f8</p>
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