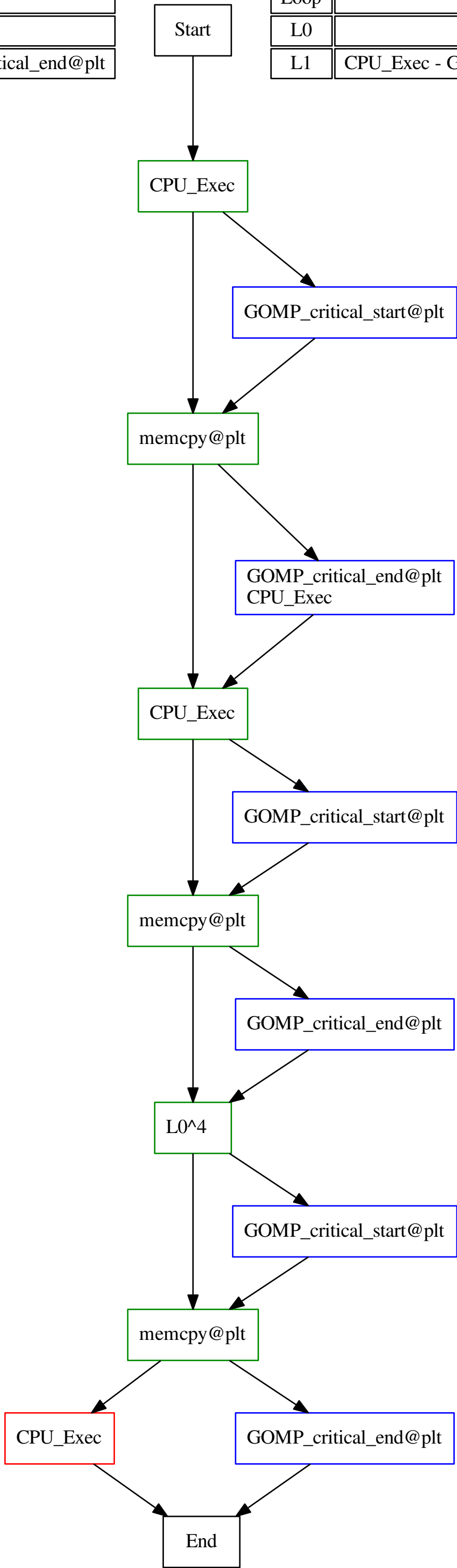


Loop	Body
L0	CPU_Exec
L1	CPU_Exec - GOMP_critical_start@plt - memcpy@plt - GOMP_critical_end@plt



Loop	Body
L0	CPU_Exec
L1	CPU_Exec - GOMP_critical_start@plt - memcpy@plt - GOMP_critical_end@plt

