cdio
Silkscreen top (refs)
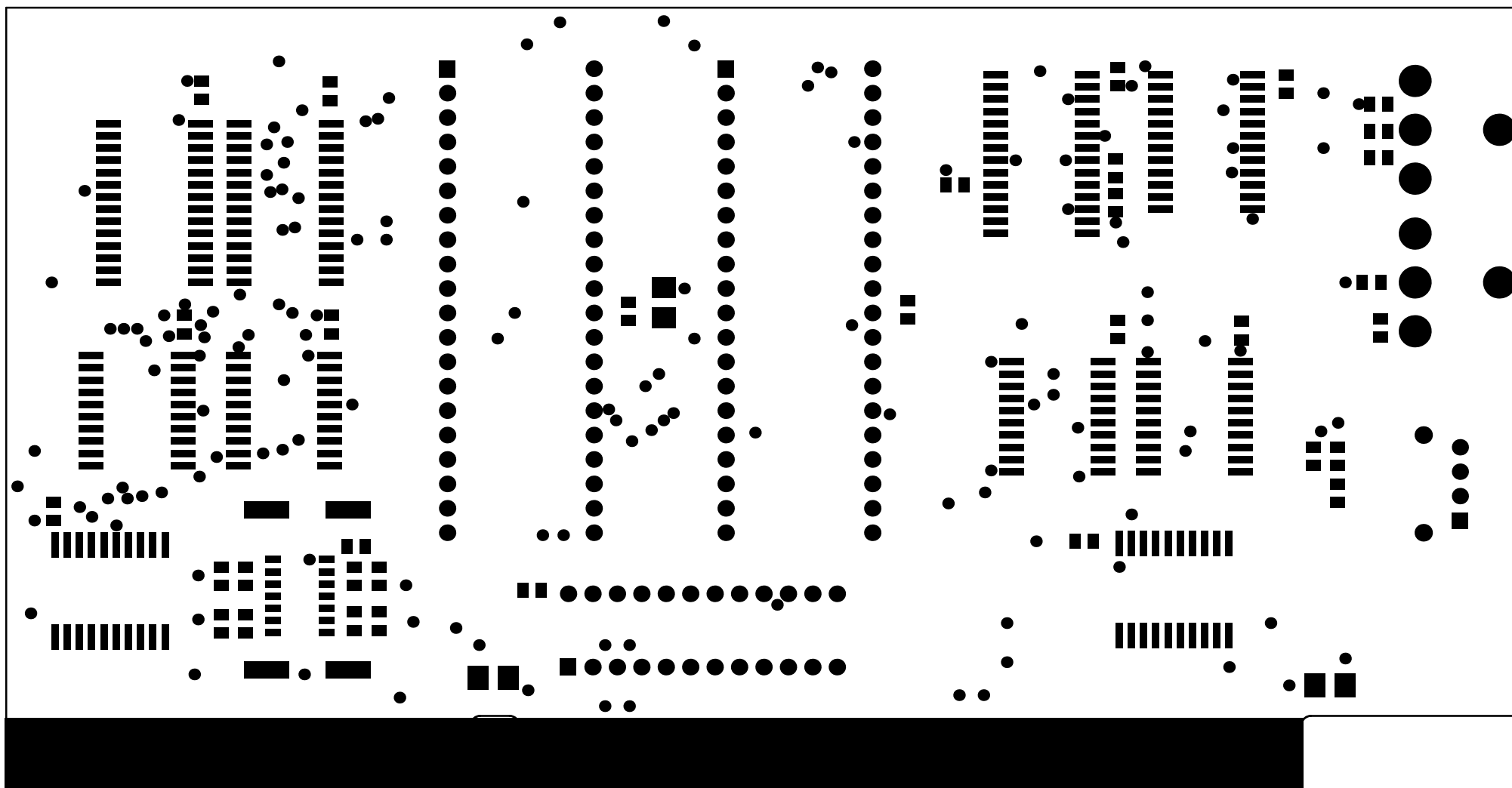Apr 29, 2012   14:38:01
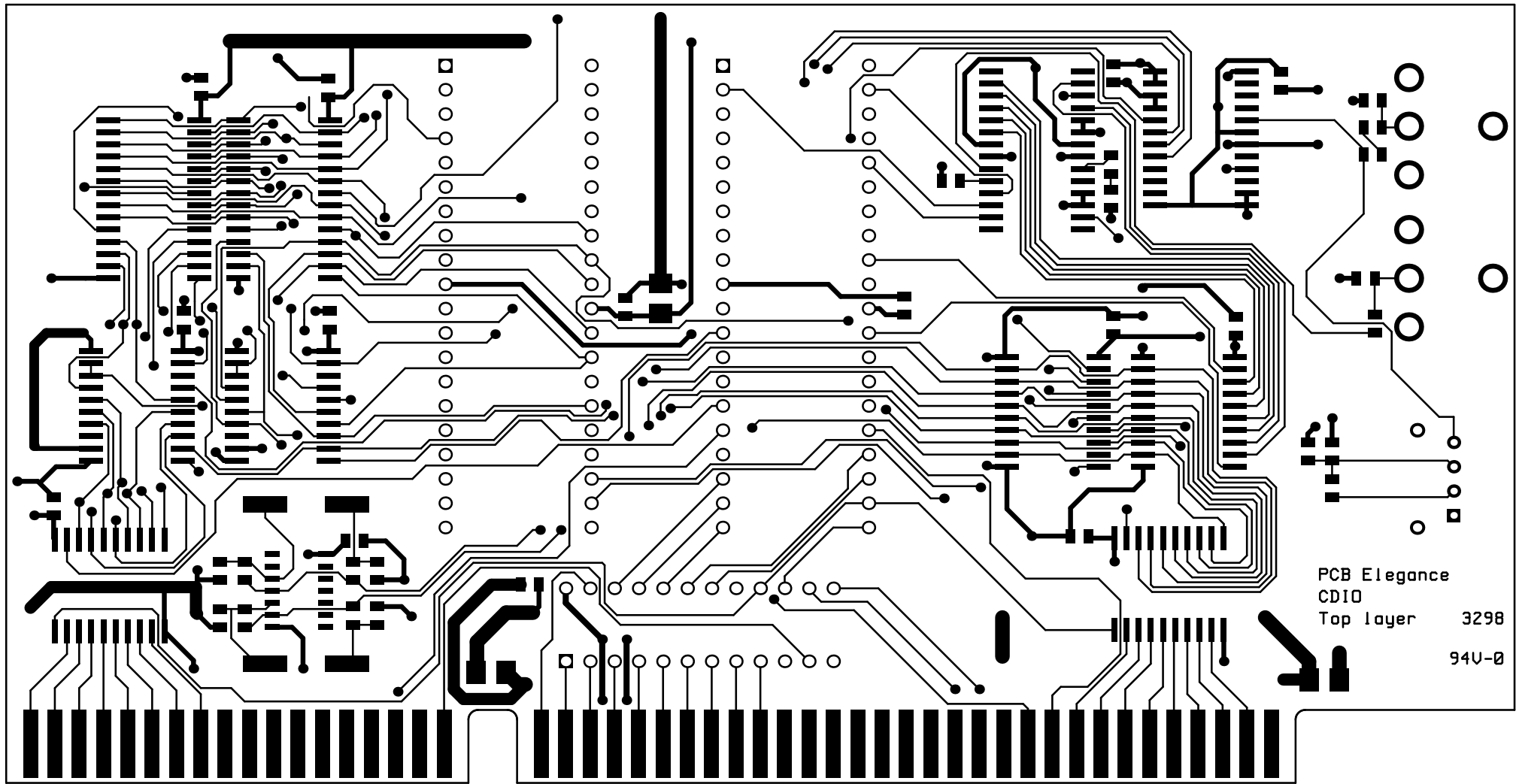
cdio
Paste mask top
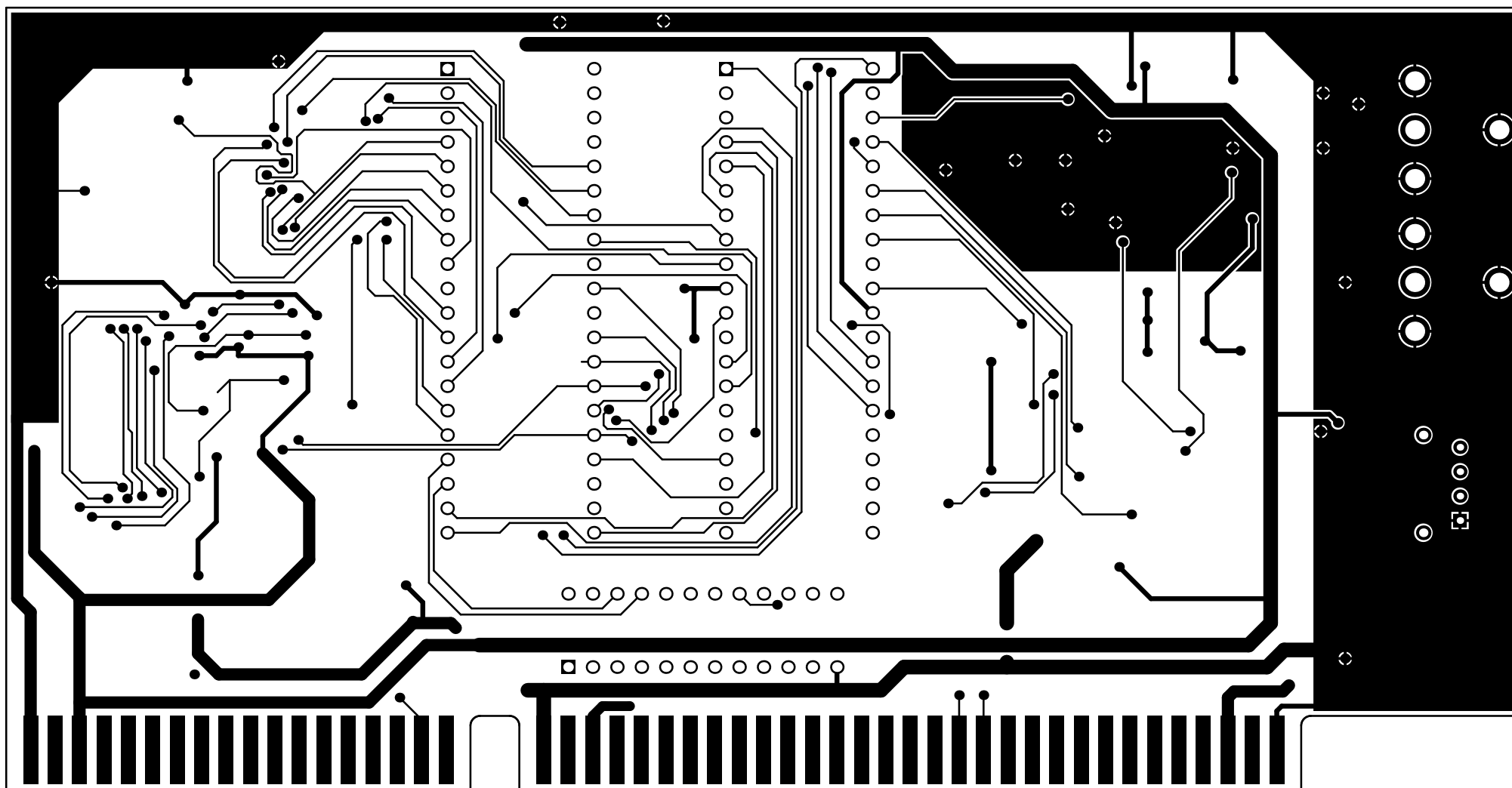Apr 29, 2012   14:38:01

cdio
Solder mask top
Apr 29, 2012   14:38:01

PCB Elegance
CDIO
Top layer   3298

94V-0

cdio
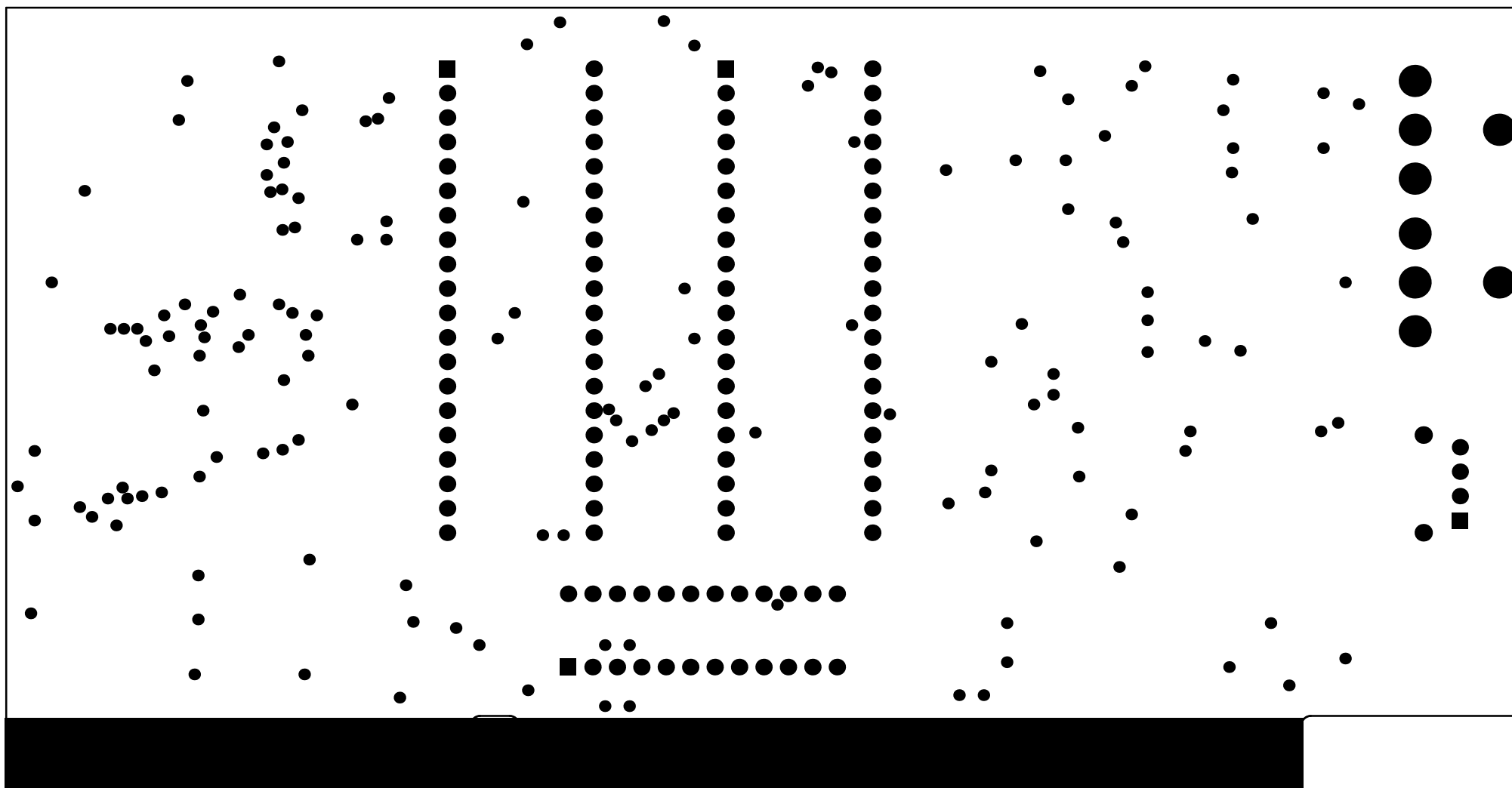Layer 1 (Top)
Apr 29, 2012   14:38:01
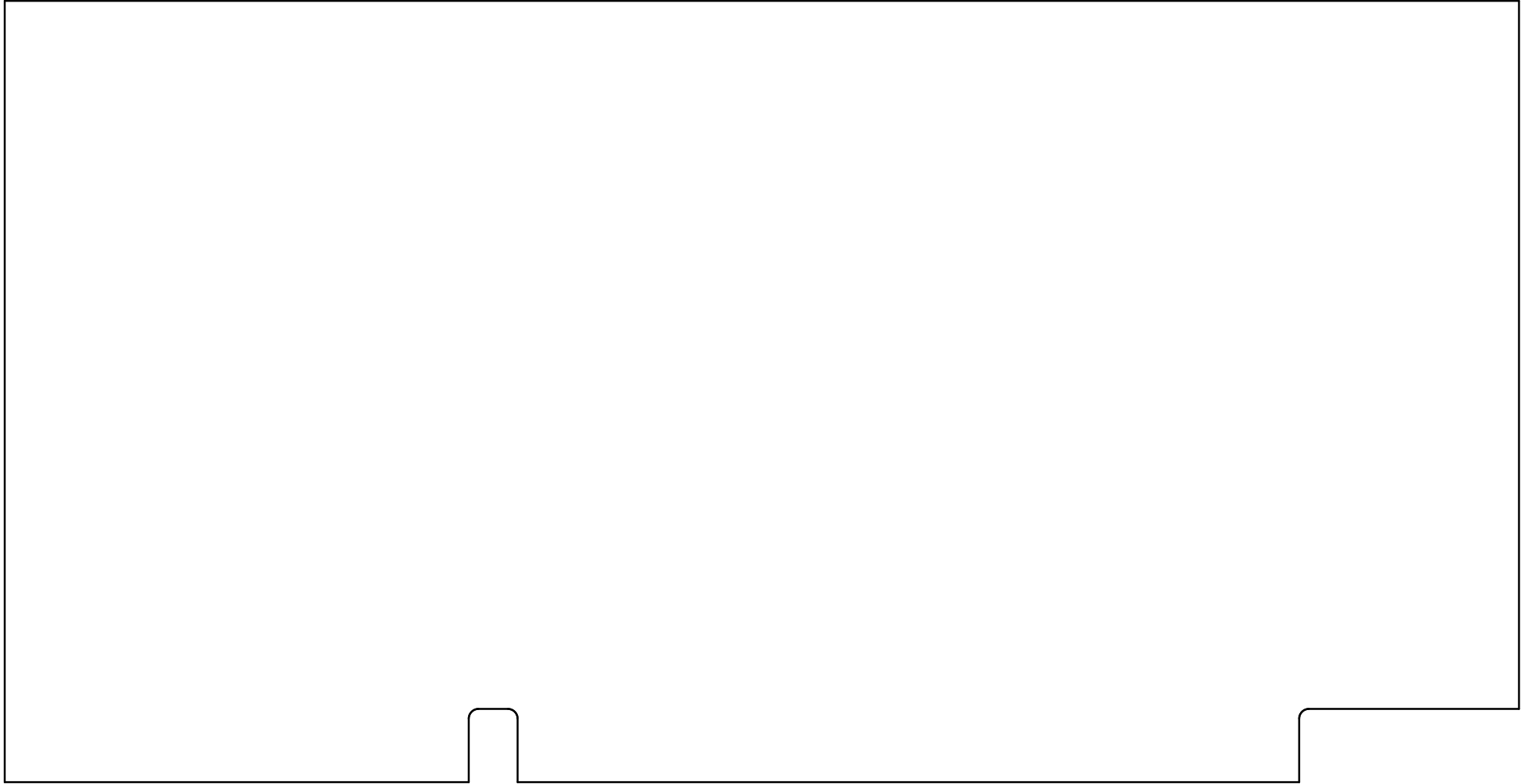
cdio
Layer 2 (Bottom)
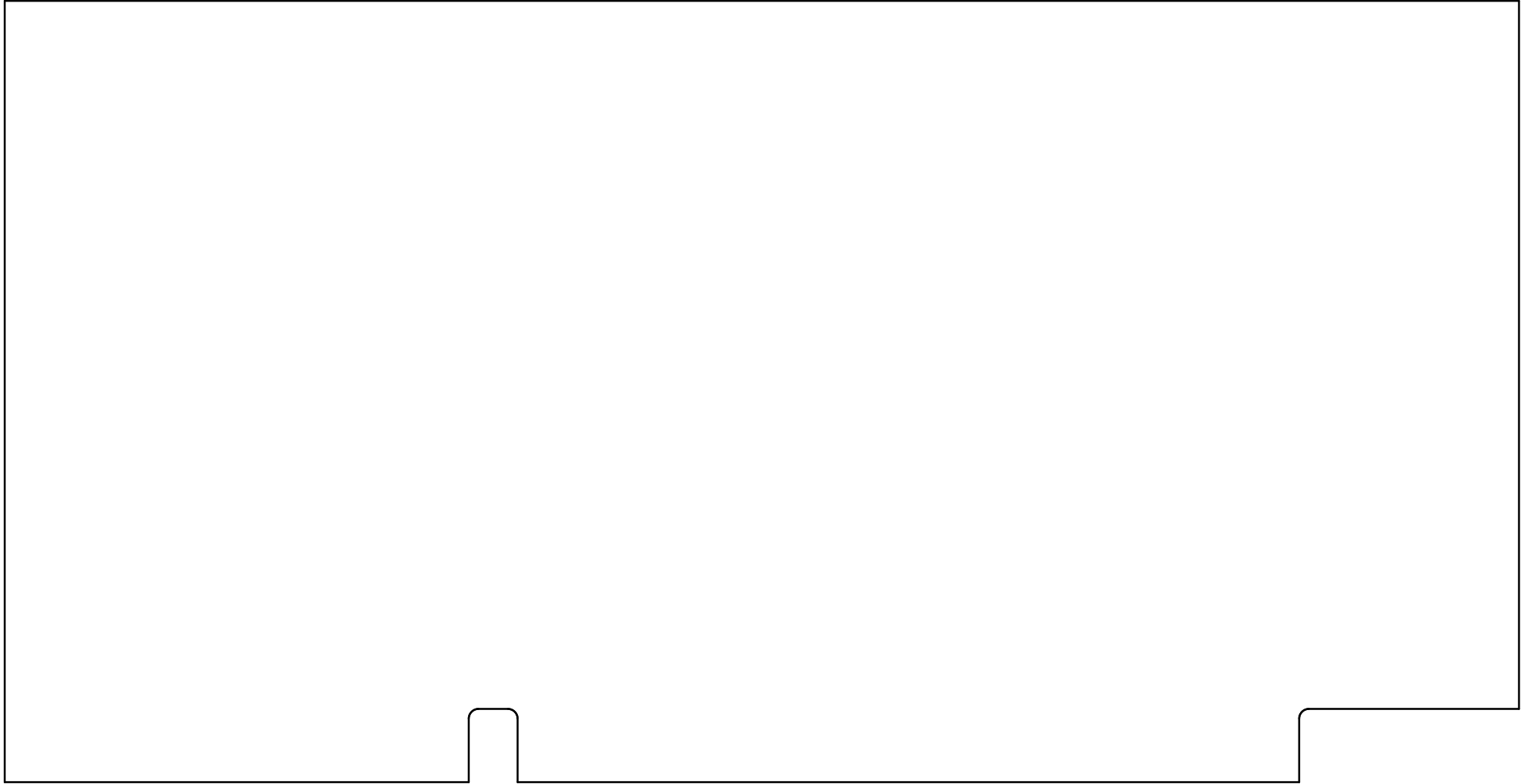Apr 29, 2012   14:38:01

cdio
Solder mask bottom
Apr 29, 2012   14:38:01

cdio
Paste mask bottom
Apr 29, 2012   14:38:01

cdio
Silkscreen bottom (refs)
Apr 29, 2012   14:38:01

cdio
Drills
Apr 29, 2012   14:38:01