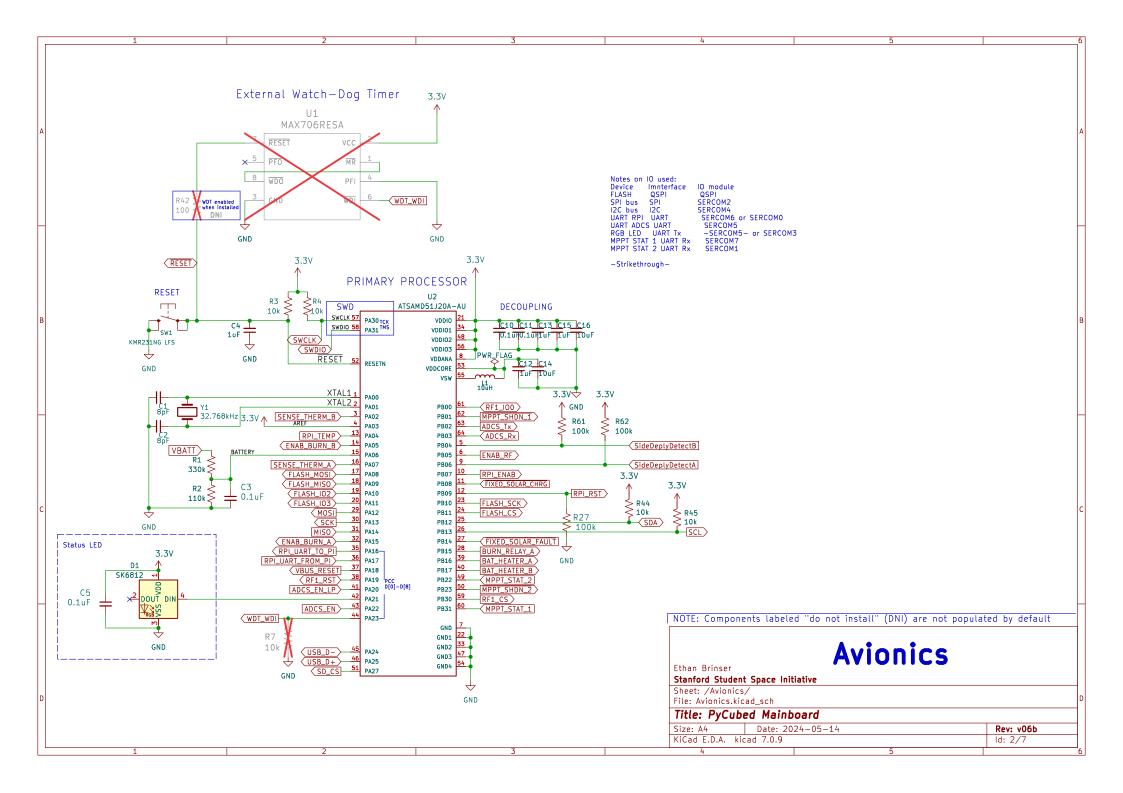
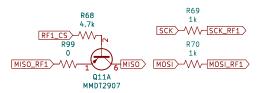
1	2	3	4	5		
Avionics	Connectors	Power	Burn Wires	RF		
File: Avionic	s.kicad_sch File: Connectors.	kicad_schFile: Power.kicad	_sch File: Burn_Wires.I	kicad_sdfile: RF_and_GPS.kicad_sc		
Bus Protecti	0 n					
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File: Bus_Pr	otection.kicad_sch					
				Ethan Brinser Stanford Student Space Initiative		
			Ethan Brinser			
			Sheet: / File: mainboard.kicad_sch	lactive		
			Title: PyCubed Mainb	oard		
			Size: A4 Date: 202 KiCad E.D.A. kicad 7.0.9	24-05-14 Rev: v06b Id: 1/7		
1	2	3	4 Kicad E.B.A. Kicad 7.0.3	5		



SPI Bus Protection — SD Card and Payloads

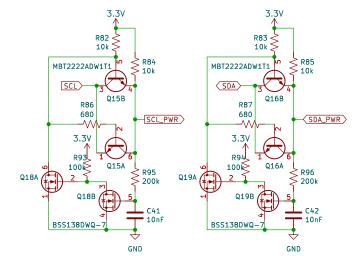


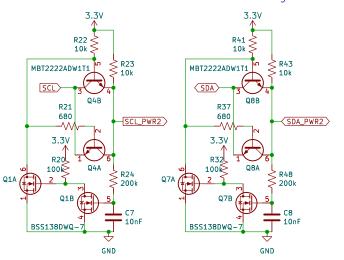
SPI Bus Protection - Radio 1



12C Bus Protection - Power Monitor

I2C Bus Protection — MPPT Status & USB Charger





NOTE

These novel bus protection circuits prevent traditional I2C/SPI failure modes where a single slave failure can disable the entire bus.

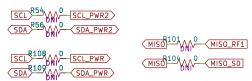
Learn more: https://doi.org/10.36227/techrxiv.15166620

By default, slave clock and/or data lines can be held low and the Master (SAMD51) will still be able to communicate with the remainder of the bus.

They can individually be bypassed by removing the transistor(s) and soldering the Oohm the jumpers below.

NOTE: Components labeled "do not install" (DNI) are not populated by default

Bus Protection — Bypass Jumpers



Bus Protection

Stanford Student Space Initiative

Ethan Brinser

Sheet: /Bus Protection/ File: Bus_Protection.kicad_sch

Title:	PyCu.	bed l	4ain	board
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 Size: A4
 Date: 2024-05-14
 Rev: v06b

 KiCad E.D.A. kicad 7.0.9
 Id: 3/7

