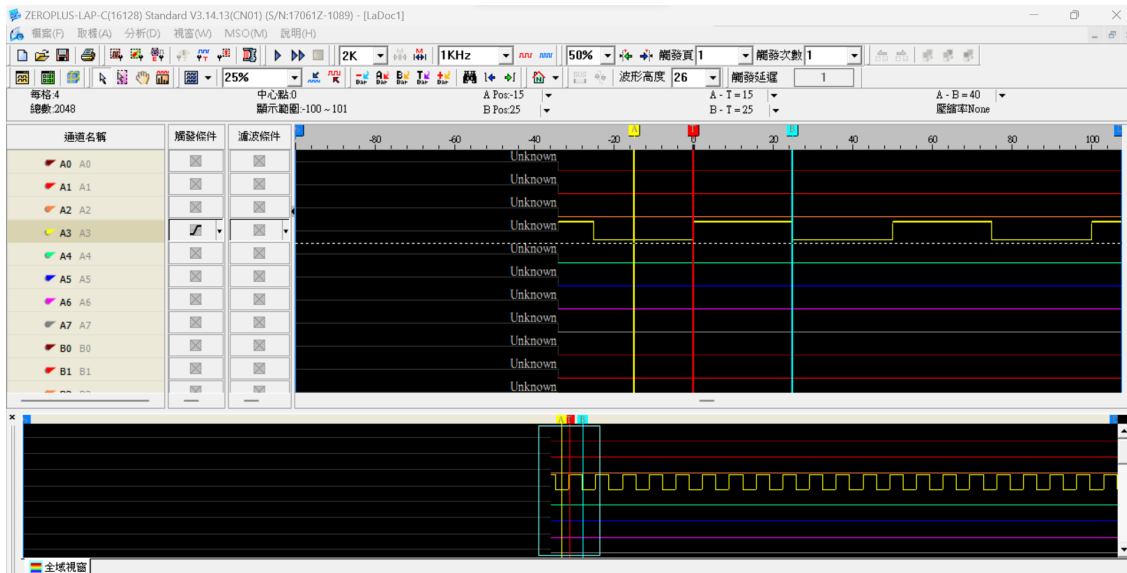


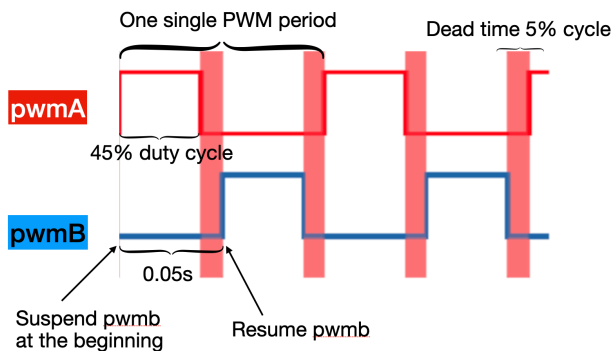
# HW-5 PWM and Logic Analyzer

1. Source code link: <https://github.com/stanthemaker/EmbeddedSystem/tree/main/hw5>

2. Logic Analyzer result:



3. Discuss how to generate complementary PWM with dead time insertion in STM32 platform.



```
pwmA.period(0.1); // 0.1 second period
pwmB.period(0.1);
// Assume dead time = 0.005s
// Set the initial duty cycle (45%)
pwmA.write(0.45f);
pwmB.write(0.45f);
```

```
pwmB.suspend()
delay(0.05s)
pwmB.resume()
```

While (1)

