PCB4109A; VL53L5CX-Satel.PrjPcb

VL53L5CX-Satel

PCB4109

03/05/2021

SHEET 01: Title

SHEET 02: Schematic

REVISION HISTORY

DATE	VERSION	NAME	DESCRIPTION
11/01/2021	000	David WARDLE	Initial Draft
11/01/2021	001	David WARDLE	No Fit components selected
18/01/2021	002	David WARDLE	Level shifters changed. Various pin and net names changed. Connector now 6 + 3 way
18/01/2021	003	David WARDLE	Signals on level shifters and connectors re-ordered
19/01/2021	004	David WARDLE	Label on Title sheet corrected Removed level shifters and associated components
19/01/2021	005	David WARDLE	Added solder bridge options for I2C signals
20/01/2021	006	David WARDLE	Added solder bridge options for AVDD supply
21/01/2021	007	David WARDLE	Changed solder bridge defaults for I2C pullups
25/01/2021	008	David WARDLE	Satellite connectors changed to 9-pin
26/01/2021	009	David WARDLE	Satellite connectors' nets reordered to aid layout
31/03/2021	010	David WARDLE	Added PCB symbol on Title page.
13/04/2021	011	David WARDLE	Corrected footprints on R4, R4 and MP1
29/04/2021	012	David WARDLE	Variant 00B. Changed LDO to "D" version. R3 made DNF





