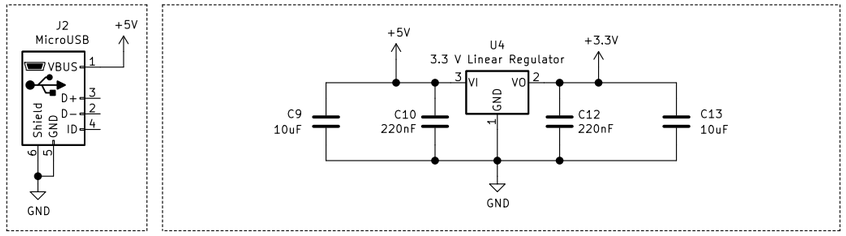
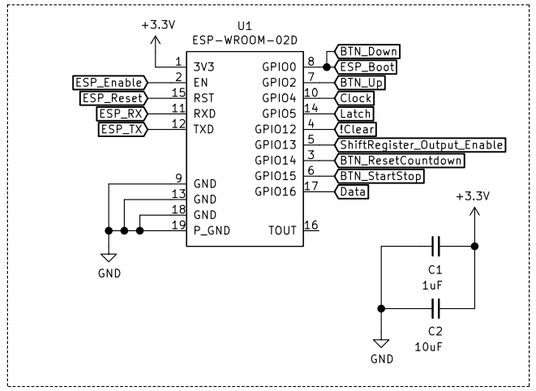
Voltage Regulation

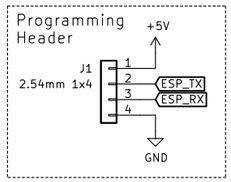
Low Drop Out Linear Regulator
High Power Loss with Higher input Voltage



Wifi Module

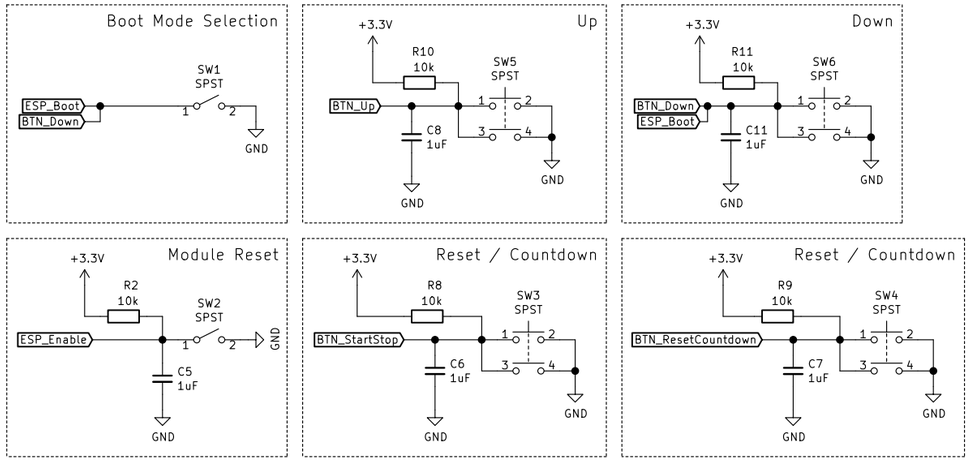
Programmed with UART
Needs to be forced into programming mode





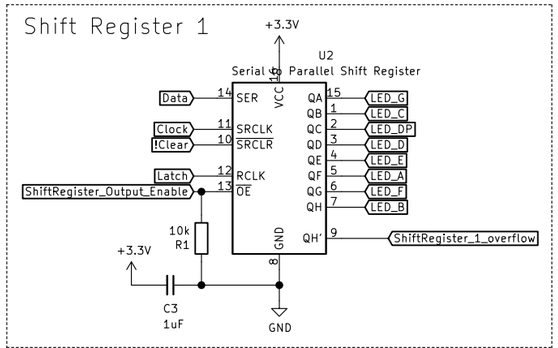
Buttons

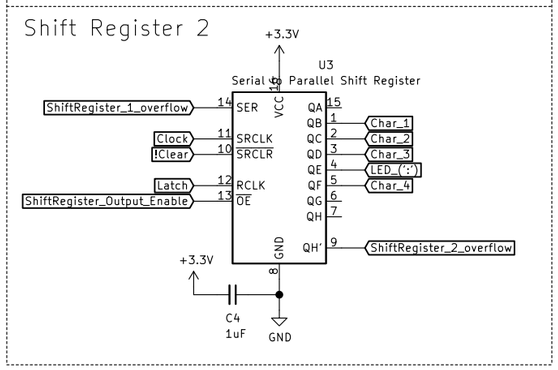
Active Low
Default Off



Serial to Parallel Output

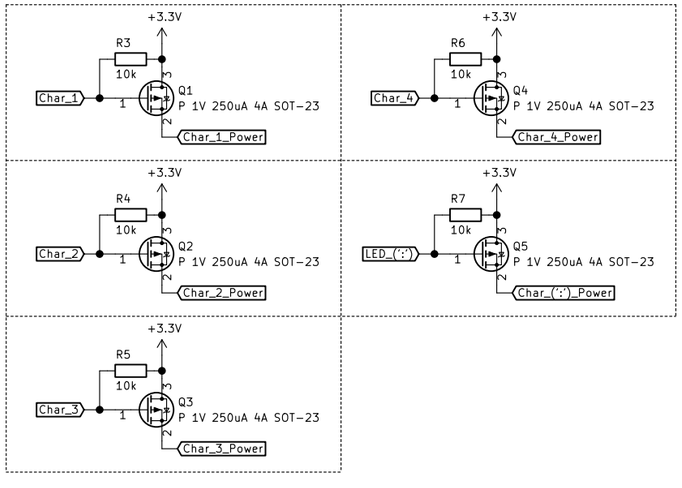
Active Low Output Enable
Default Off
use PWM to dim





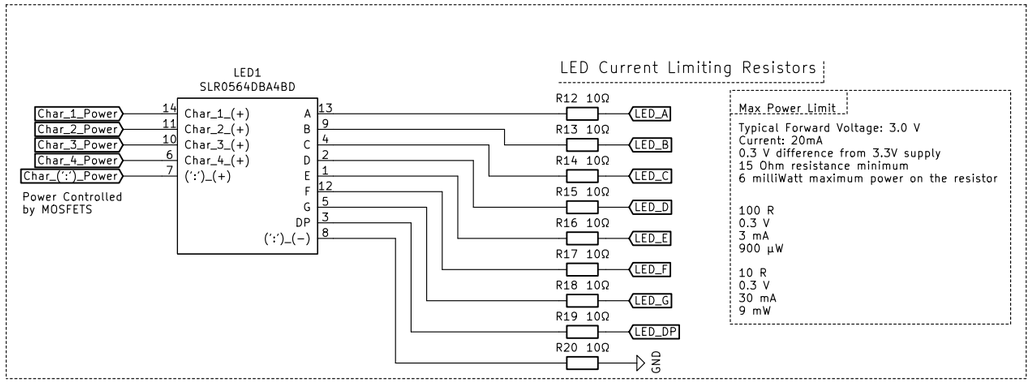
Digit Control

Pull up resistors with P Channel MOSFETS
High Side Switches, Active Low
Default Off



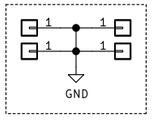
LED Display

Common Anode
One Digit At a Time



Mounting Holes

M3 with exposed Ground Pad



STASIS ELECTRONICS

KiCad E.D.A. kicad (5.1.10)-1

Kitchen Timer

Title

Revision: A01 | Sheet 1 of 1

https://github.com/stasiselectronics/KitchenTimer

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