AI-ANNE: (A) (N)eural (N)et for (E)xploration

KI-ENNA: (E)in (N)euronales (N)etz zum (A)usprobieren

Transparent and Efficient Deep Learning on Microcontrollers and Embedded Systems

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Abstract (ENG)

This article explores the integration of neural networks onto resource-constrained embedded systems like a Raspberry Pi Pico / Raspberry Pi Pico 2. A TinyML aproach transfers neural networks directly on these microcontrollers, enabling real-time, low-latency, and energy-efficient inference while maintaining data privacy. Therefore, AI-ANNE (A) (N)eural (N)et for (E)xploration will be presented, which facilitates the transfer of pre-trained models from high-performance platforms like TensorFlow and Keras onto microcontrollers, using a lightweight programming language like MicroPython. This approach demonstrates how neural network architectures, such as neurons, layers, density and activation functions can be implemented in MicroPython in order to deal with the computational limitations of embedded systems. Based on the Raspberry Pi Pico / Raspberry Pi Pico 2, two different neural networks on microcontrollers are presented for an example of data classification. As an further application example, such a microcontroller can be used for condition monitoring, where immediate corrective measures are triggered on the basis of sensor data. Overall, this article presents a very easy-to-implement way of using neural networks on energy-efficient devices such as microcontrollers.

Abstract (GER)

Dieser Artikel befasst sich mit der Integration neuronaler Netze in ressourcenlimitierte und eingebettete Systeme wie den Raspberry Pi Pico / Raspberry Pi Pico 2. Ein TinyML-Ansatz überträgt dabei neuronale Netze direkt auf einen Mikrocontroller und ermöglicht so eine latenzarme und energieeffiziente Datenanalyse bei gleichzeitiger Sicherung sensibler Daten. Zu diesem Zweck wird KI-ENNA: (E)in (N)euronales (N)etz zum (A)usprobieren vorgestellt, das die Übertragung von vortrainierten und rechenintensiven Modellen auf Basis von TensorFlow und Keras auf Mikrocontroller unter Verwendung einer leichtgewichtigen Programmiersprache wie MicroPython ermöglicht. Dieser Ansatz verdeutlicht dabei, wie die den neuronalen Netzwerken zugrundeliegende Struktur in Form von Neuronen, Schichten, Dichte und Aktivierungsfunktionen in MicroPython implementiert werden kann, um unter den Ressourcenlimitationen von eingebetteten Systemen funktionsfähig zu sein. Auf Grundlage des Raspberry Pi Pico / Raspberry Pi Pico 2 werden zwei verschiedene neuronale Netze auf Mikrocontrollern für ein Beispiel zur Datenklassifizierung vorgestellt. In der Praxis wäre mit einem solchen Microcontroller darüber hinaus eine Zustandsüberwachung möglich, bei denen auf Basis von Sensordaten sofortige Korrekturmaßnahmen ausgelöst werden. Dadurch stellt dieser Beitrag insgesamt eine sehr leicht umzusetzende Möglichkeit vor, wie neuronale Netze auf energieeffizienten Geräten wie Mikrocontroller zur Anwendung gebracht werden können.

Keywords

TinyML, EdgeAI, Microcontroller, Embedded Systems, Machine Learning, Deep Learning

(I) Introduction

Artificial Intelligence for Microcontrollers

Machine Learning and Deep Learning are increasingly driving innovation across various fields (LeCun et al. 2015), with a notable expansion into embedded systems, bringing their capabilities closer to data sources. This trend, known as TinyML, is especially prominent in microcontrollers and Internet of Things devices. TinyML offers several advantages over cloud-based artificial intelligence, including improved data privacy, lower processing latency, energy efficiency, and reduced dependency on connectivity (Ray 2022). One key application of TinyML is in condition monitoring, where neural networks can be used to detect anomalies directly within sensors so that immediate corrective actions can be taken automatically (Cioffi et al. 2020).

While many high-performance frameworks like TensorFlow and Keras for Python are designed for powerful hardware such as GPUs, these frameworks are unsuitable for embedded systems due to their limited computational resources (Delnevo et al. 2023). To address this issue, the architecture of neural networks can be reconstructed in a leightweight programming language like MicroPython. Thus, neural networks trained on high-performance hardware can be transferred onto resource-constrained devices like microcontrollers and achieve the same outputs and results (Ray 2022). However, training or developing neural networks directly on embedded systems remains a complex challenge. AIfES: (A)rtificial (I)ntelligence (f)or (E)mbedded (S)ystems is presented below as a comparable approach, which has shown initial success here (Wulfert et al. 2024).

As an alternative, AI-ANNE: (A) (N)eural (N)et for (E)xploration offers one way of transferring pre-trained neural networks onto a microcontroller. For this purpose, neurons, layers, density and activation functions as the underlying foundation of neural networks were coded in MicroPython in order to be able to reconstruct the original neural networks trained with TensorFlow and Keras. How the transfer works and a suitable application example are part of this article. The underlying foundation of neural networks and their counterparts in MicroPython are presented successively.

Comparable Approaches and Added Value

AIfES: (A)rtificial (I)ntelligence (f)or (E)mbedded (S)ystems is a flexible software framework designed by Fraunhofer to run machine learning models on small, low-power devices like microcontrollers (Wulfert et al. 2024). It simplifies the process of building, training, and running models directly on these devices, without needing powerful external systems. Users can customize the framework to fit their needs by choosing different model components, like types of layers or how data is processed. AIfES can run both pre-trained models and train new ones on the device itself, saving energy and protecting privacy (Wulfert et al. 2024). It outperforms similar tools in terms of speed and memory usage for certain tasks. Future improvements will focus on making it even more efficient and supporting new types of deep learning models.

AI-ANNE: (A) (N)eural (N)et for (E)xploration is a similar approach and, as an open framework, also enables the flexible expansion of the underlying activation functions, testing how they work while simultaneously the number of neurons and layers can be adjusted easily in MicroPython. This flexibility can also be used for fine-tuning directly on the microcontroller. As a result, AI-ANNE: (A) (N)eural (N)et for (E)xploration allows the learning behavior of the neural networks to be observed and creates a didactic value for its users. Two neural networks are already pre-installed as standard: One with six neurons in a total of three layers and one with eight neurons in a total of four layers. In the given example, the learning behavior of the various neural networks can be investigated with the pre-installed IRIS dataset. The transparent insight into the MicroPython code also opens up didactic application potential. The interaction with the microcontroller takes place via Thonny. All the necessary components are presented below.

(II) Requirements

Raspberry Pi Pico / Raspberry Pi Pico 2

The Raspberry Pi Pico is powered by the RP2040 microcontroller, which features a dual-core ARM Cortex-M0+ processor running at 133 MHz, with 264 KB on-chip SRAM and 2 MB onboard flash memory. It offers a wide range of connectivity options, including USB for power and data transfer, up to two I2C, SPI, and UART interfaces for communication, as well as 16 PWM channels for precise control of external devices.

The board also includes three 12-bit ADC channels for analog input, and it supports a range of peripherals such as a real-time clock (RTC), timers, and interrupt handling through a nested vectored interrupt controller (NVIC). For power, the Raspberry Pi Pico operates on a voltage range of 1.8V to 3.6V, with typical consumption between 20-100 mA, and can be powered via the micro-USB port or the VSYS pin for external power sources like batteries or regulated supplies.

In 2024 an updated Raspberry Pi Pico 2 was introduced, which is powered by the RP2350 microcontroller, which features a dual-core ARM Cortex-M33 processor running at 150MHz and 520 KB on-chip SRAM and 4 MB onboard flash memory. Both can be operated with MicroPython. It can therefore be assumed that the Raspberry Pi Pico 2 can calculate larger datasets and more complex neural networks with AI-ANNE: (A) (N)eural (N)et for (E)xploration.

MicroPython

MicroPython is an efficient, lightweight implementation of the Python programming language designed to run on microcontrollers and embedded systems with constrained resources (Delnevo et al. 2023). Unlike the full Python environment, MicroPython is optimized to operate within the memory and processing limits typical of small-scale devices, offering a streamlined interpreter and a subset of Python's standard libraries. MicroPython retains much of Python's high-level syntax and ease of use, making it accessible to developers familiar with Python. It is particularly well-suited for rapid prototyping, development, and deployment of machine learning and neural network models on embedded platforms, where resources such as memory, computational power, and storage are limited.

In the context of neural network applications, MicroPython is often used in edge computing scenarios, where deep learning models need to be deployed directly onto microcontroller-based systems for real-time, localized inference. Although MicroPython does not natively support the extensive numerical libraries found in full Python (e.g., TensorFlow and Keras), it is possible to reproduce the basic architecture of neural networks in MicroPython from scratch.

Thonny

Thonny is a simple, user-friendly program that works on all major computer systems. It makes it easy to connect with and program the Raspberry Pi Pico / Raspberry Pi Pico 2. Thonny enables users to quickly write and test code, manage files and fix any mistakes with helpful tools. It can be described as a tool for those just starting with MicroPython on microcontrollers and embedded systems. With Thonny, AI-ANNE: (A) (N)eural (N)et for (E)xploration can simply be flashed onto the microcontroller.

Thonny can be downloaded here: https://thonny.org/

(III) Basic Knowledge

Architecture of Neural Networks

A neural network is a computational model inspired by the way biological neural systems process information. It consists of interconnected layers of nodes, or neurons, which transform input data into output predictions through a series of mathematical operations (LeCun et al. 2015). In the context of implementing neural networks in MicroPython, the architecture must be designed to operate within the resource constraints of embedded systems (Ray 2022). Despite these constraints, the basic components of a neural network - neurons, layers, density, and activation functions - can still be effectively modeled (Sakr et al. 2021).

a) Neurons

A neuron in a neural network is a computational unit that receives inputs, applies a weight to each input, sums the weighted inputs, and passes the result through an activation function to produce an output. In MicroPython, each neuron can be represented as a mathematical operation involving inputs and weights, with the output being calculated via simple matrix operations.

The general form of the neuron's computation can be expressed with y as the output of the neuron, f as the activation function, x_i as the inputs, w_i as the corresponding weights and b as the bias:

$$y = f(\sum_{i} w_i x_i + b)$$

In a MicroPython implementation, these calculations can be done using basic array operations, making it computationally efficient for small-scale neural networks on microcontrollers.

b) Layers

A neural network is typically structured as a series of layers, where each layer consists of multiple neurons. There are three main types of layers in a typical neural network architecture:

Input Layer: This is the first layer of the network and receives the raw input data. Each neuron in the input layer corresponds to a feature in the input data. In MicroPython, this layer would typically read sensor values or external data directly.

Hidden Layers: Between the input and output layers, the network may contain one or more hidden layers. These layers are responsible for learning complex representations of the input data. Each hidden layer is composed of neurons that perform weighted sums of the outputs of the previous layer, followed by an activation function.

Output Layer: This is the final layer of the network, which produces the prediction or classification result. The output layer's structure depends on the problem being solved (e.g., binary classification, multi-class classification, regression).

MicroPython can represent the layers as a list of lists (or arrays) of neurons, where each element stores the weights, biases, and outputs of the neurons in the respective layer.

c) Density

The density of a layer refers to the number of neurons in that layer. In a fully connected (dense) layer, each neuron in the current layer is connected to all neurons in the previous layer. This is the most common configuration in neural networks. For example: In a dense layer with 5 neurons, each neuron in the layer receives input from all neurons in the previous layer, and the layer will contain 5 sets of weights and biases to be learned during training. MicroPython can implement a dense layer efficiently using matrix multiplication. Each layer's input and weight matrices are multiplied together, followed by the addition of a bias term, and the result is passed through an activation function.

d) Activation Functions

An activation function determines whether a neuron should activate, so that the neuron's output will be passed forward to the next layer. The activation function introduces non-linearity into the network, allowing it to learn complex patterns in the data. Common activation functions include:

 Sigmoid: The Sigmoid function outputs values between 0 and 1 and is often used in binary classification tasks.

$$Sigmoid(x) = \frac{1}{1 + e^{-x}}$$

ReLU (Rectified Linear Unit): ReLU outputs the input directly if it is positive; otherwise, it outputs
zero. It is widely used in hidden layers due to its simplicity and efficiency in preventing vanishing
gradients.

$$ReLU(x) = \max(0, x)$$

• Leaky ReLU: The Leaky ReLU activation function is a variant of the traditional ReLU activation function, which addresses a common issue known as the "dying ReLU" problem. This problem occurs when neurons become inactive and stop learning because their output is always zero (when the input is negative). Leaky ReLU overcomes this by allowing a small, non-zero gradient when the input is negative. The key difference between ReLU and Leaky ReLU is that when $x \leq 0$, instead of the output being zero (as in ReLU), the output is a small negative value. The parameter alpha controls the slope of this negative region, typically with small values such as 0.01 or 0.1.

$$LeakyReLU(x) = \begin{cases} x & \text{if } x > 0\\ \alpha x & \text{if } x \le 0 \end{cases}$$

• Tanh: The hyperbolic tangent function Tanh outputs values between -1 and 1. It is similar to the sigmoid but has a wider output range, making it useful for some applications where the network needs to model both positive and negative values.

$$Tanh(x) = \frac{e^x - e^{-x}}{e^x + e^{-x}}$$

• Softmax: Often used in the output layer of classification networks, the Softmax function normalizes the output to produce a probability distribution across multiple classes.

$$Softmax(x_i) = \frac{e^{x_i}}{\sum_j e^{x_j}}$$

In MicroPython, these activation functions can be implemented as simple functions, leveraging Python's built-in math library. Given the computational constraints of embedded devices, it is crucial to choose lightweight activation functions like ReLU, which avoid the more computationally expensive exponentiation operations used in sigmoid or tanh. AI-ANNE: (A) (N)eural (N)et for (E)xploration passes data through the neural network layer by layer, and each neuron's output is computed based on the inputs and weights. In a MicroPython environment, due to the limited processing power and memory, a smaller network with fewer layers and neurons might be preferred, and training could be done in advance on a more powerful system before deployment to the embedded device.

e) Weights and Biases

In neural networks, weights and biases are key parameters that the model learns during the training process to make accurate predictions.

Weights are the parameters that scale the input values as they pass through the network. They determine the strength of the connections between neurons in adjacent layers. Each connection between two neurons has its own weight, and the value of this weight influences how much the input from the previous layer affects the output of the current layer. In mathematical terms, if a neuron receives an input x and its corresponding weight is w, the contribution of that input to the neuron's output is w * x. Weights are adjusted during training using optimization techniques, such as gradient descent, to minimize the model's prediction error.

Biases, on the other hand, allow the model to shift the output independently of the weighted sum of inputs. They are added to the weighted sum before it is passed through the activation function. This allows the network to better model complex relationships in the data by shifting the activation function's threshold. For example, if the weighted sum of inputs to a neuron is represented as z = w * x + b, the bias term b shifts the output, enabling the model to learn more effectively. Like the weights, biases are also learned during training.

Confusion Matrix

A confusion matrix is like a scoreboard that indicates how well a neural network is doing at making predictions. It's especially useful for classification problems, where the goal is to assign items to categories. For the purpose of binary classification a confusion matrix can be a table with two rows and two columns. Each cell tells something about the model's predictions compared to the actual results, which can be interpreted as follows:

- True Positives (TP): The model correctly predicted the positive class.
- True Negatives (TN): The model correctly predicted the negative class.
- False Positives (FP): The model incorrectly predicted the positive class (Type I Error).
- False Negatives (FN): The model incorrectly predicted the negative class (Type II Error).

The Accuracy of the neural network based prediction can be calculated as a metric based on this. Accuracy as a percentage is a metric that provides information about how often the model is right overall. Below is an illustrative example (Tab. 1) of a confusion matrix with actual values in the rows and predicted values in the columns:

(Tab. 1) Confusion Matrix Example

	Predicted Positive	Predicted Negative
Actual Positive	09	01
Actual Negative	00	10

In this binary classification, 09 of the positive class are classified correctly and 01 incorrectly (Type II Error). Accordingly, 10 of the negative class are classified correctly and 00 incorrectly. This results in an Accuracy of 95% and is based on the second application example of a neural network with 6 neurons, which is presented below. In addition to this metric, there are other metrics for the evaluation of neural networks, e.g. Precision, Recall and F1 Score. For this example and as basic introduction to AI-ANNE: (A) (N)eural (N)et for (E)xploration, the focus on accuracy shall suffice.

(IV) Application Example

Binary Classification

The IRIS dataset is a widely recognized dataset in statistics, machine learning and deep learning and often used for classification problems. Introduced by the British biologist and statistician Ronald A. Fisher in 1936, it contains 150 instances of iris flowers, each with four attributes that describe their physical characteristics. These attributes include the sepal length, sepal width, petal length, and petal width, all measured in centimeters.

The objective of the dataset is to classify each flower into one of three species based on these four attributes: Setosa, Versicolor, and Virginica. The Iris dataset serves as an ideal example for demonstrating and testing machine learning algorithms and neural networks as deep learning models, particularly for supervised classification tasks. Its manageable size, clear distinctions between classes and straightforward nature make it a popular choice for exploring classification techniques. However, classifying some of the species, particularly Versicolor and Virginica, can be challenging due to their overlapping characteristics, making the task more complex for certain machine learning algorithms and neural networks as deep learning models.

In the pre-installed example, AI-ANNE: (A) (N)eural (N)et for (E)xploration was entrusted with the classification of Versicolor and Virginica accordingly. The result will be a percentage for the sake of accuracy.

Solution with 8 Neurons

First, with TensorFlow and Keras pre-trained weights and biases must be transferred to the microcontroller. The weights and biases of the neural network with 8 neurons need to be coded in a specific order in MicroPython (Tab. 2). The first layer, the so called input layer, contains two neurons that process the input from the four independent variables of the IRIS dataset. The weights w1 are coded accordingly with two columns and four rows in MicroPython. The two neurons are correspondingly provided with two biases b1. The weights and biases for the number of neurons in the other layers are coded accordingly: Three neurons follow in the first hidden layer, followed by two neurons in the second hidden layer and one neuron in the output layer.

(Tab. 2) Weights and Biases for 8 Neurons

```
w1 = [[-0.75323504, -0.25906014],
	[-0.46379513, -0.5019245],
	[2.1273055, 1.7724446],
	[1.1853403, 0.88468695]]
b1 = [0.53405946, 0.32578036]
w2 = [[-1.6785783, 2.0158117, 1.2769054],
	[-1.4055765, 0.6828738, 1.5902631]]
b2 = [1.18362, -1.1555661, -1.0966455]
w3 = [[0.729278, -1.0240695],
	[-0.80972326, 1.4383037],
	[-0.90892404, 1.6760625]]
b3 = [0.10695826, 0.01635581]
w4 = [[-0.2019448],
	[1.5772797]]
b4 = [-1.2177287]
```

For pre-training in TensorFlow and Keras the ReLU activation function was used in the input layer, followed by Sigmoid in the first hidden layer, ReLU in the second hidden layer and Sigmoid agein in the output layer. The Accuracy of this neural network with 8 neurons is 90%. A variation of the activation functions as well as the weights and biases can influence the accuracy accordingly. AI-ANNE: (A) (N)eural (N)et for (E)xploration enables direct customization of the code in MicroPython.

Solution with 6 Neurons

The next neural network differs in the number of neurons, the layers and the activation functions used. This time there are three neurons in the input layer (Tab 3.). Accordingly, the weights w1 of the neurons are coded in three columns and four rows, which result from the four independent variables of the IRIS dataset. This time three biases b1 need to be added. The code for the hidden layer and the output layer is written accordingly, so that there is again one neuron in the output layer for binary classification.

(Tab. 3) Weights and Biases for 6 Neurons

This time, the *ReLU* activation function was used during pre-training in TensorFlow and Keras for the input layer, followed by the *Sigmoid* activation function in the hidden layer and in the output layer. This results in an Accuracy of 95%, whereby flexible adjustments are also possible here. The different Accuracies of the two examples demonstrate the importance of the number of neurons and layers as well as the activation functions used.

(V) Summary

This article explored the integration of neural networks onto resource-limited microcontrollers and embedded systems like the Raspberry Pi Pico and Raspberry Pi Pico 2, using a TinyML approach. This method enabled the deployment of neural networks directly on microcontrollers, providing real-time, low-latency, and energy-efficient inference while ensuring data privacy. For this purpose, AI-ANNE: (A) (N)eural (N)et for (E)xploration was introduced, a open source framework that facilitated the transfer of pre-trained models from high-performance platforms like TensorFlow and Keras to microcontrollers, using MicroPython as lightweight and transparent programming languages. The approach demonstrated how key neural network components—such as neurons, layers, density, and activation functions—could be implemented in MicroPython in order to address the computational constraints of microcontrollers and embedded systems. Two neural network examples were presented, either with 8 neurons in 4 layers or with 6 neurons in 3 layers. Overall, the article offered a simple and practical method for deploying neural networks on energy-efficient devices like microcontrollers and how these can be used for didactic insights into the underlying technology and programming techniques of deep learning models.

About the Author

• Dennis Klinkhammer completed his doctorate and habilitation at Justus Liebig University Giessen (JLU) and focuses his teaching and research on the methodological foundations of machine learning and deep learning in the programming languages R and Python. Insights into his teaching and research are available at: https://www.statistical-thinking.de/

Open Source Code

• Both the presented and other examples of AI-ANNE: (A) (N)eural (N)et for (E)xploration are available on GitHub. This includes Jupyter Notebooks for pre-training with TensorFlow and Keras in Python, the parameters to be transferred and the corresponding code in MicroPython. The link to the repository is: https://github.com/statistical-thinking/KI.ENNA/

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