



# VSD-IAT Workshop Certificate

*RTL design using Verilog with SKY130 Technology*

27 April - 1 May 2022

*IRIVINTI SHIVA TEJA*

has successfully completed 5 Day Online Cloud based

Workshop on *VSD-IAT* conducted by

VLSI System Design

Github Repo	<a href="https://github.com/stativeboss/SKY130_RTLDSN_WRKSHP/blob/main/README.md">https://github.com/stativeboss/SKY130_RTLDSN_WRKSHP/blob/main/README.md</a>
% Completion	100