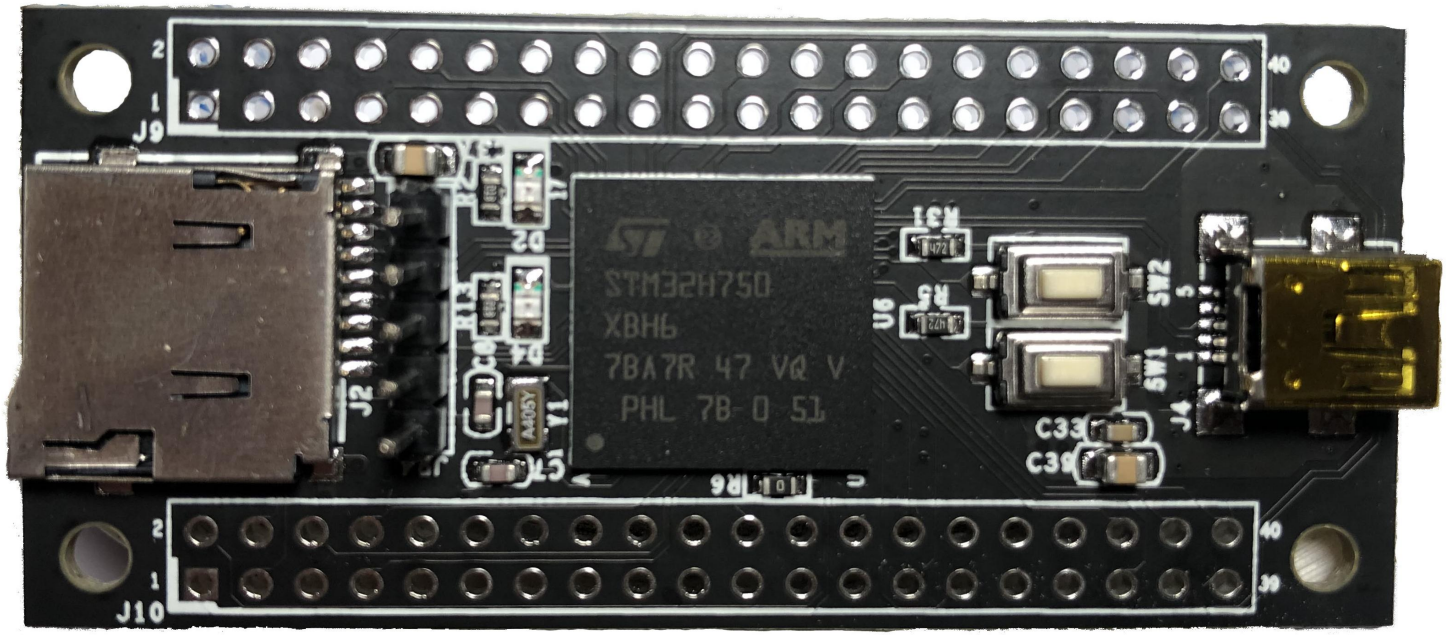
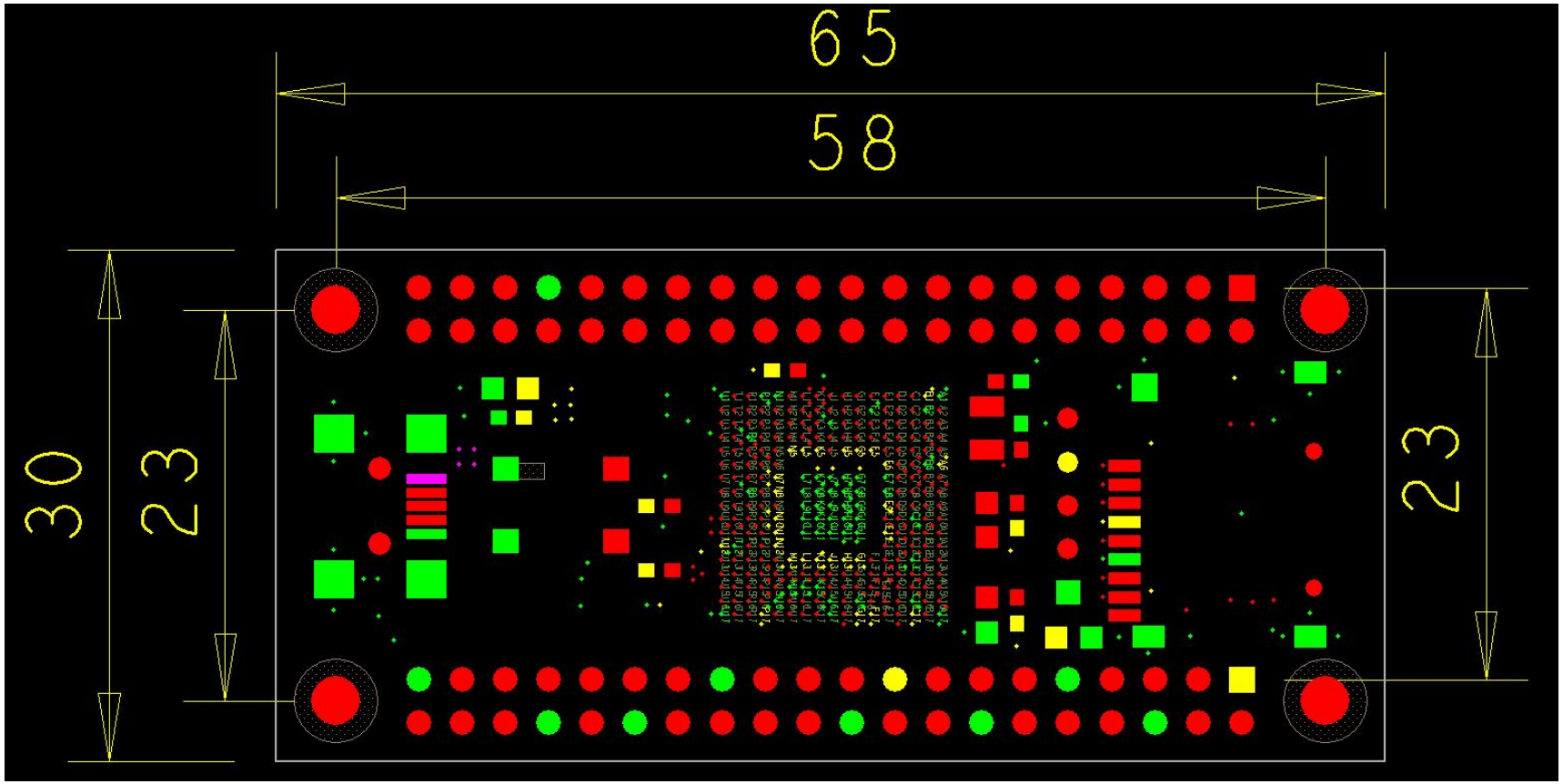
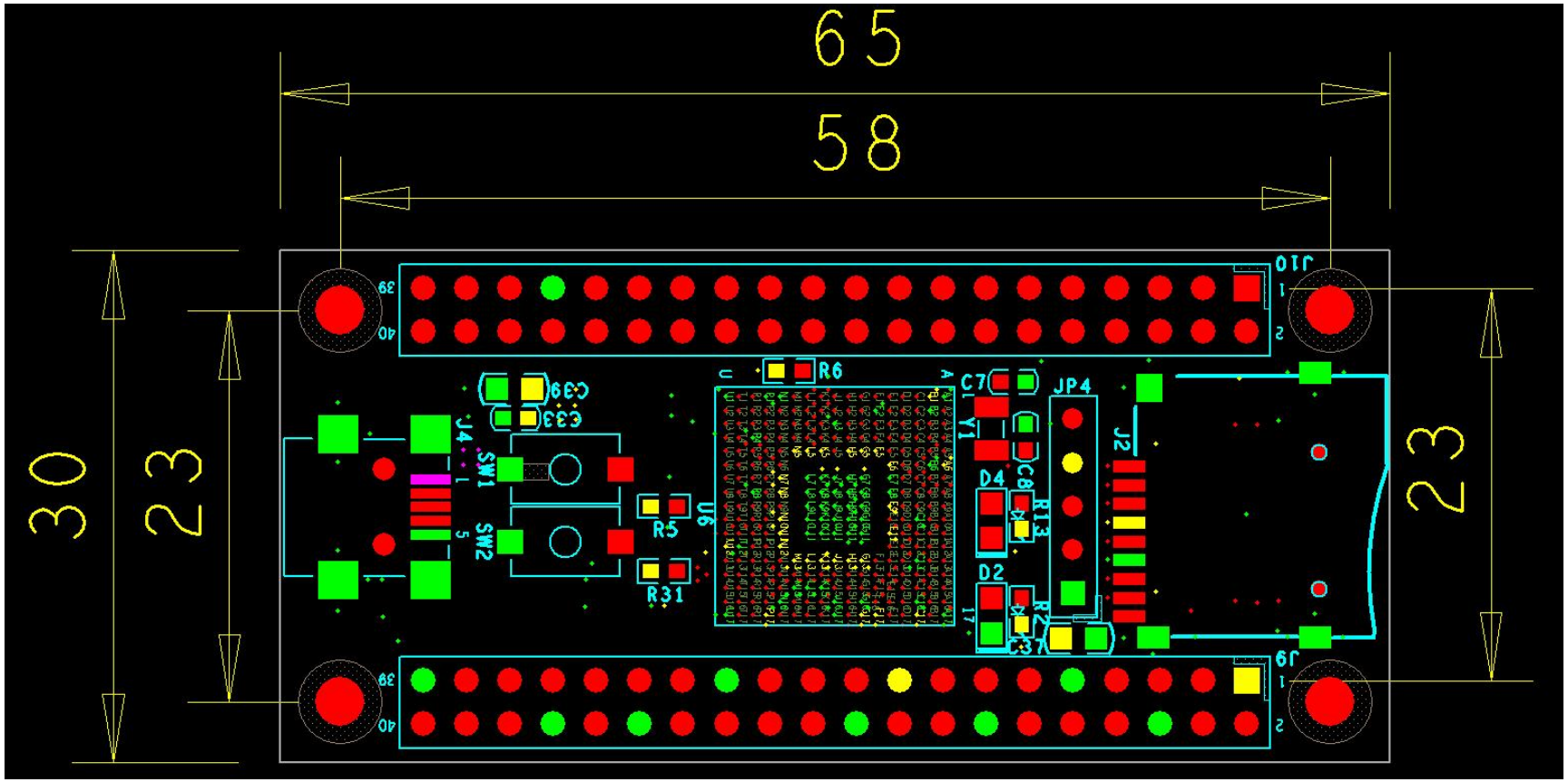
How to Drive QMTECH STM32H750XBH6 Core Board



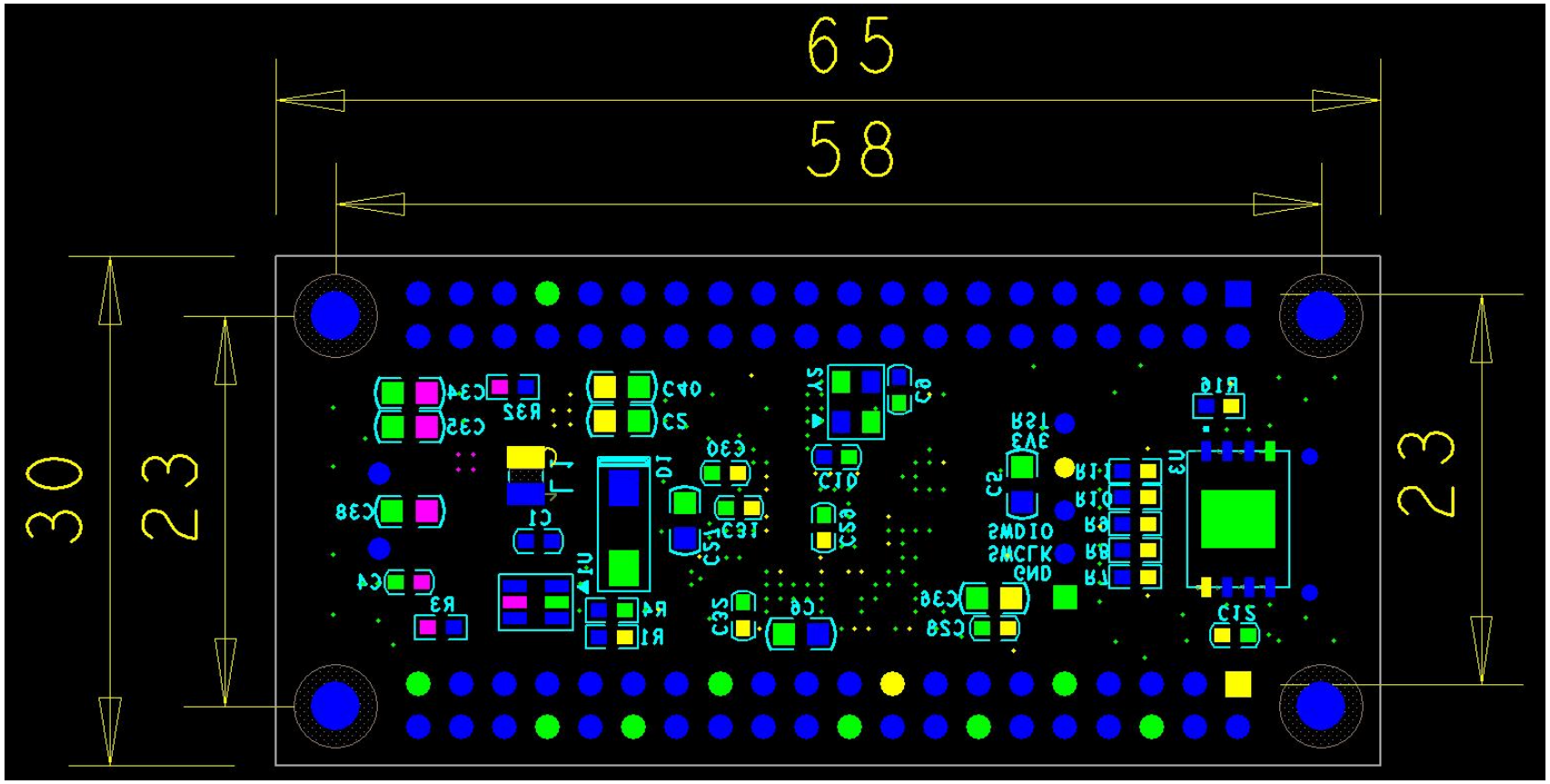
Dimension (mm)



Silk Screen Top



Silk Screen Bottom



Example ST-Link JTAG Cable

Mini USB Cable shall be connected to PC. Mini USB cable provides power supply and USB communication to the STM32H750XBH6 Core Board.

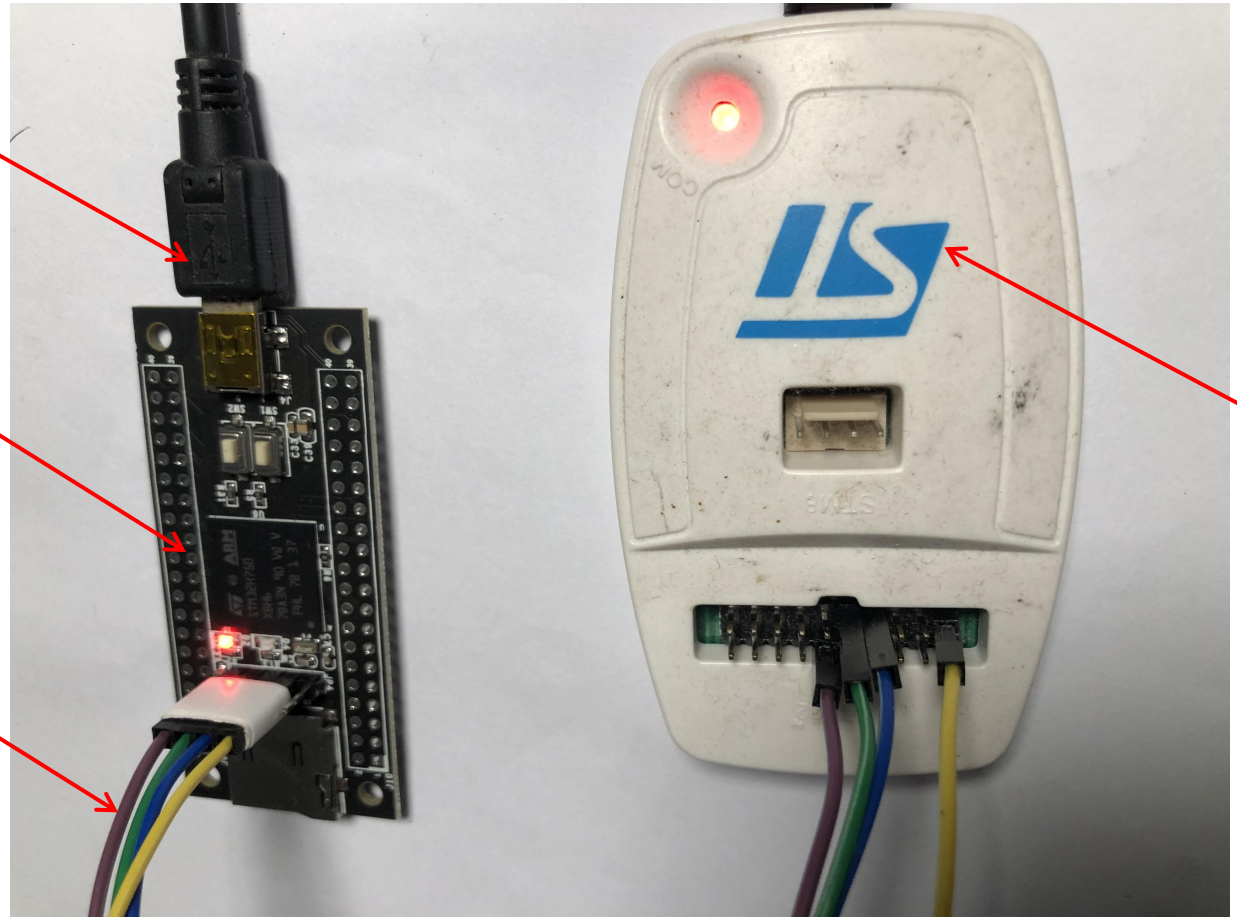
STM32H750XBH6 Core Board

GND

SWCLK

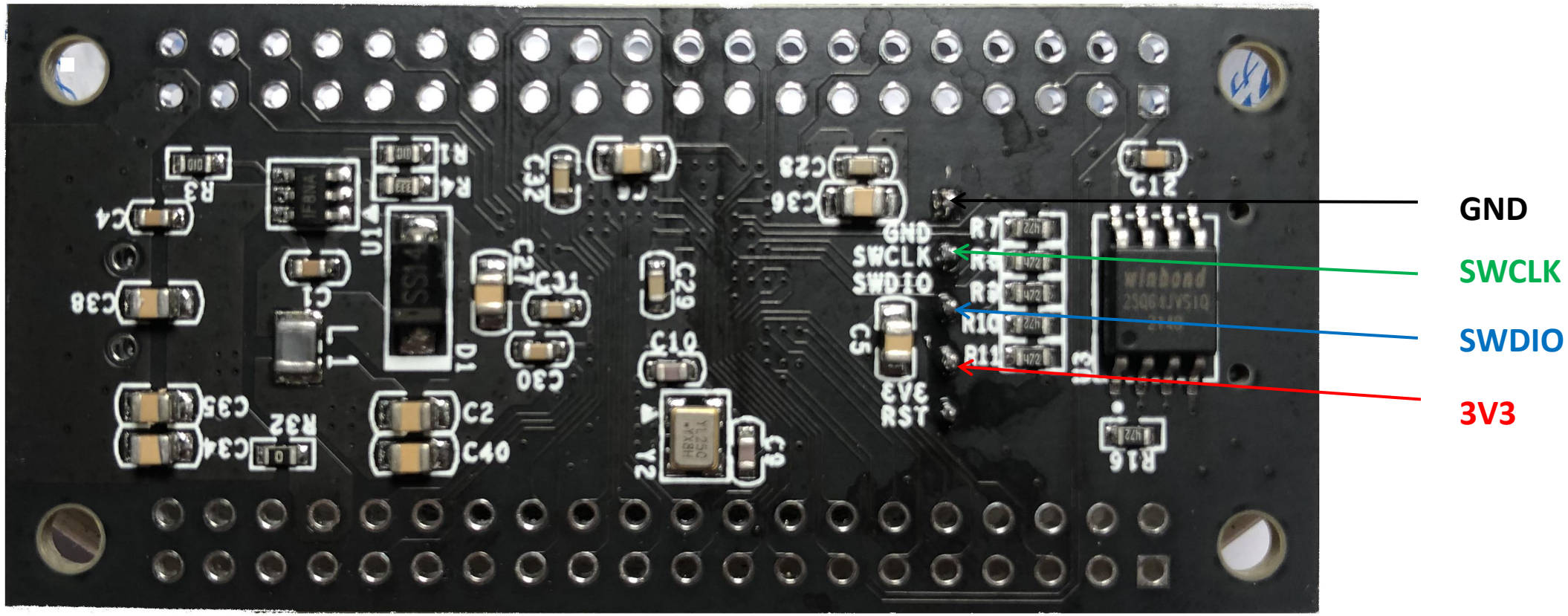
SWDIO

3V3



ST-Link

SWD PORT



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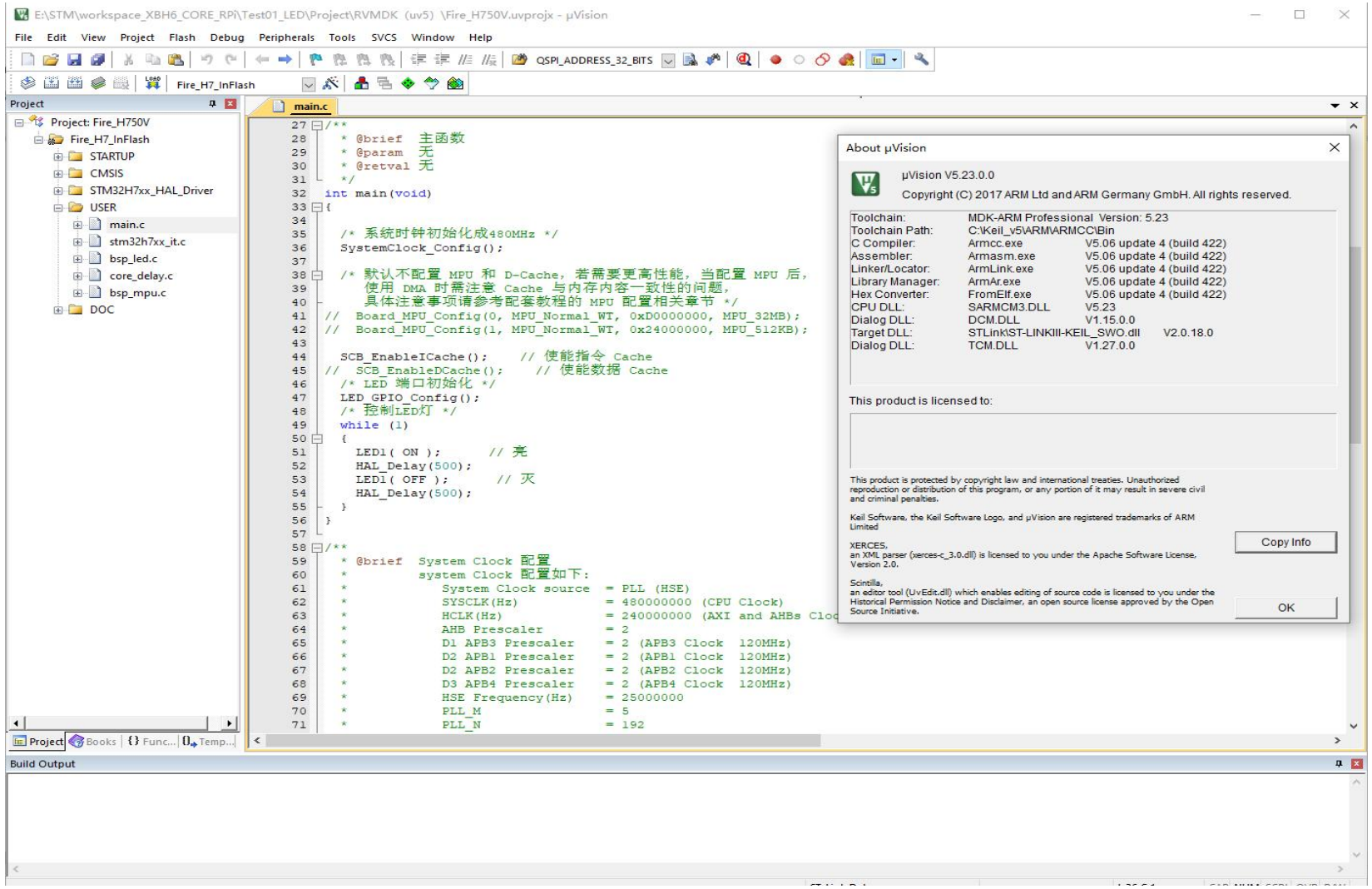
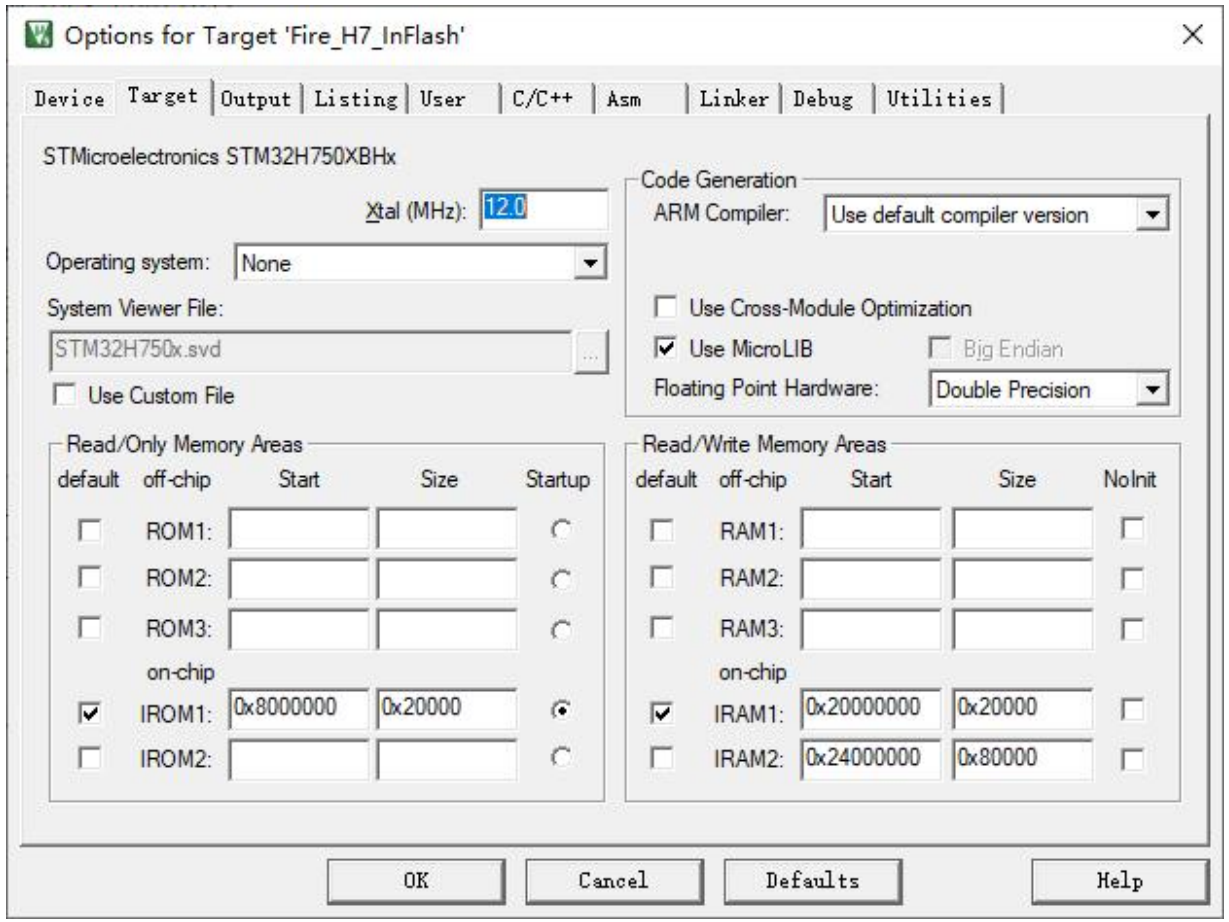
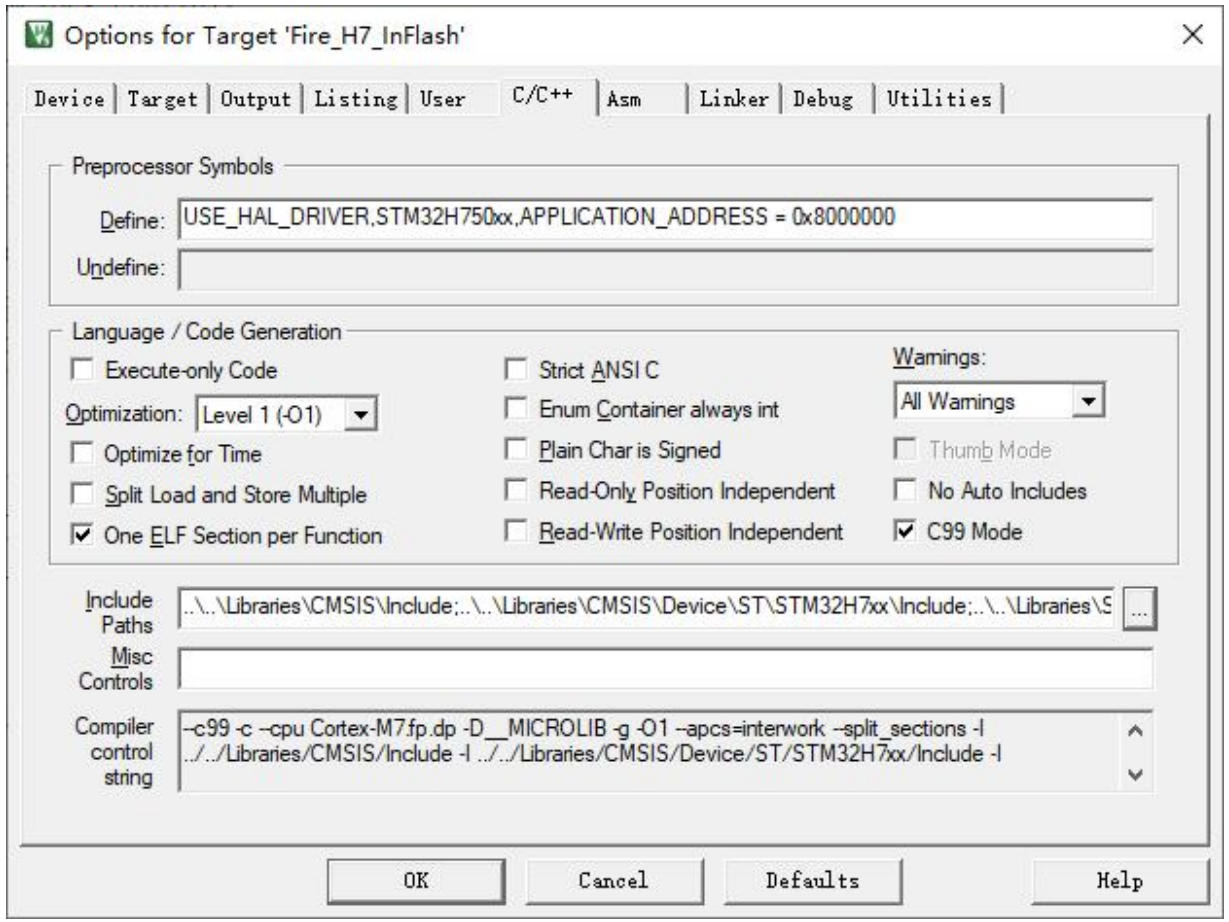


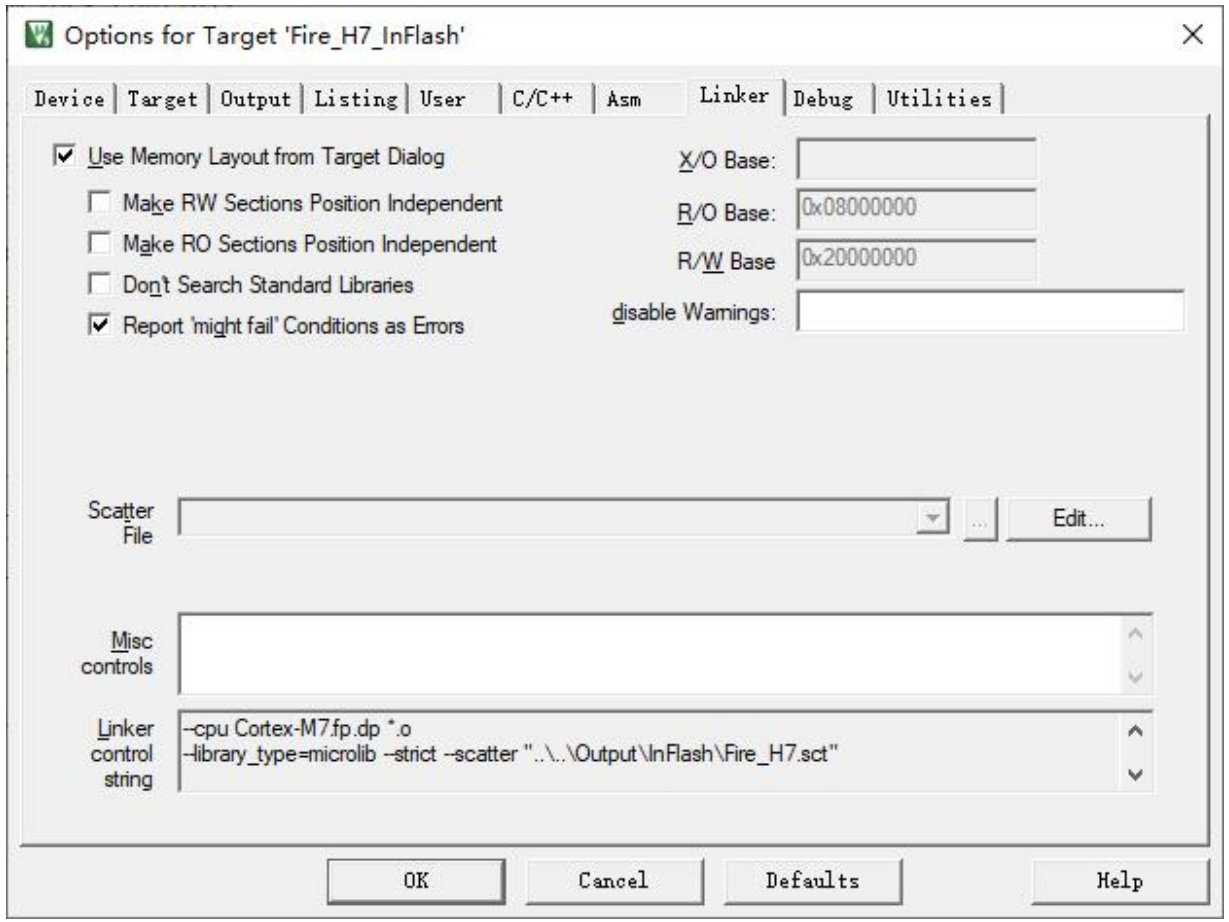
Example Settings (1)



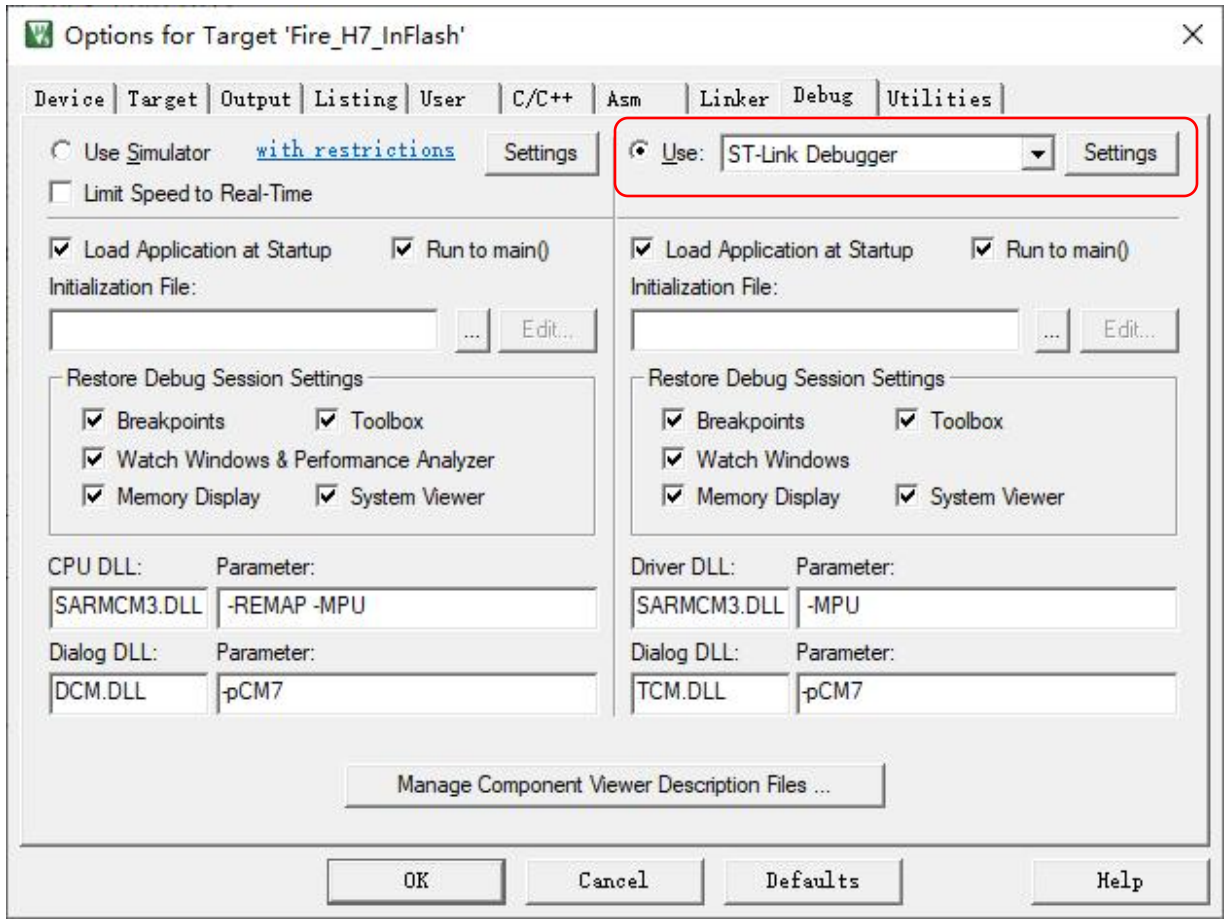
Example Settings (2)



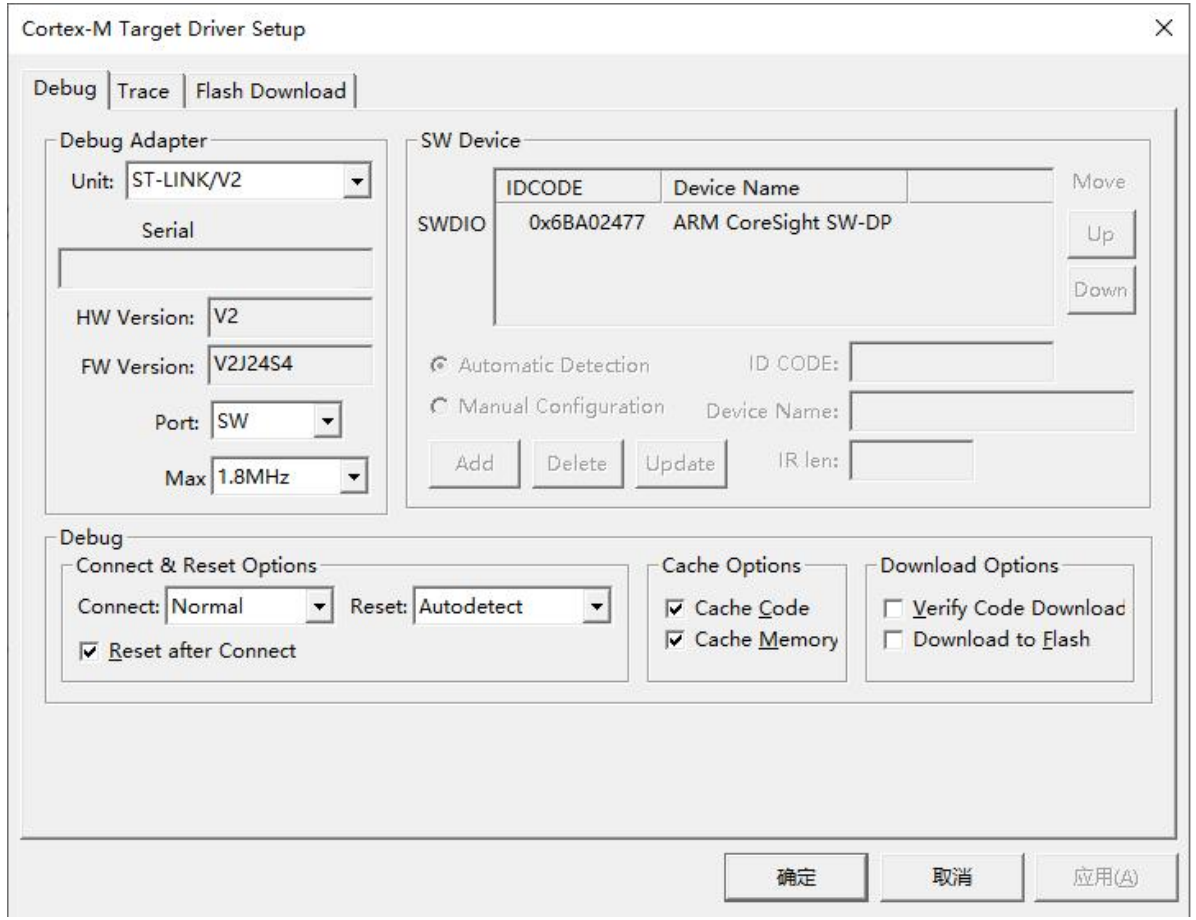
Example Settings (3)



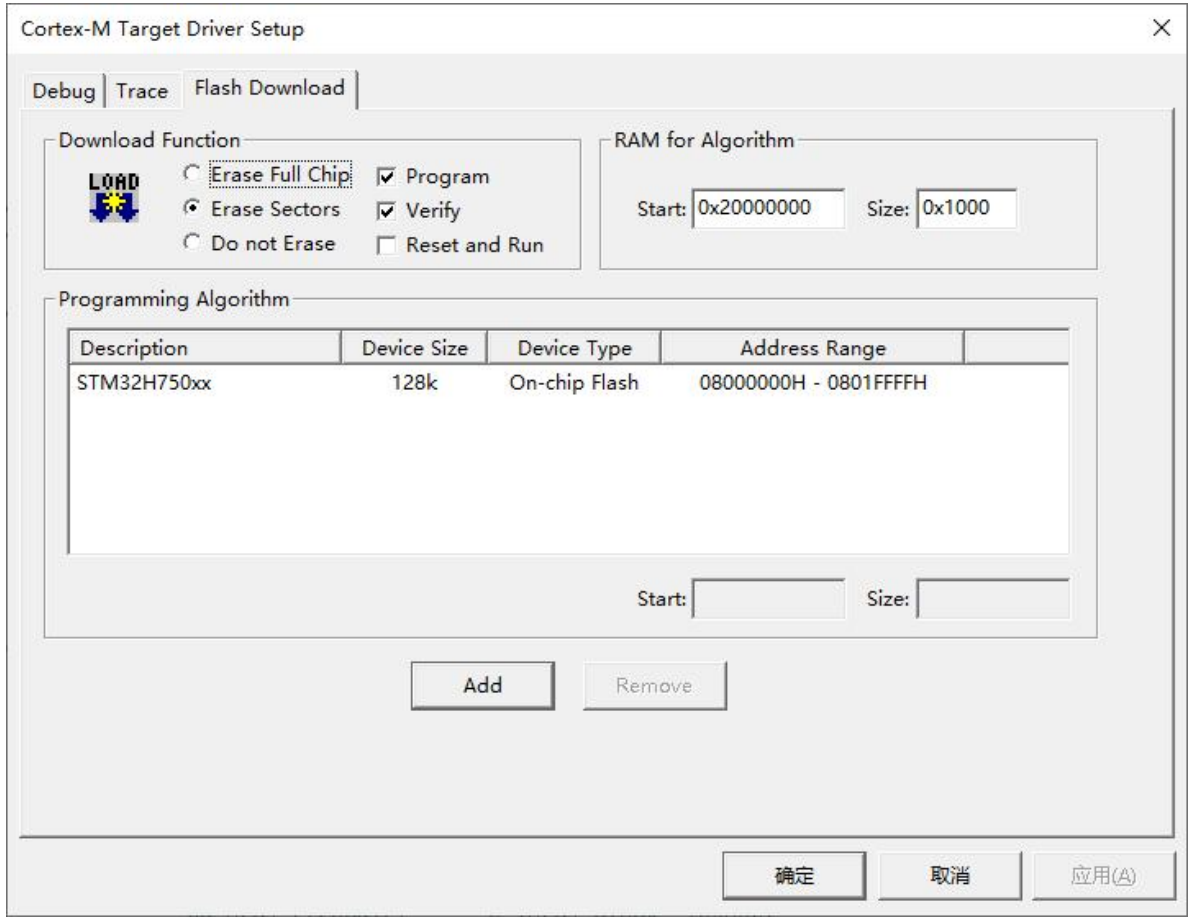
Example Settings (4)



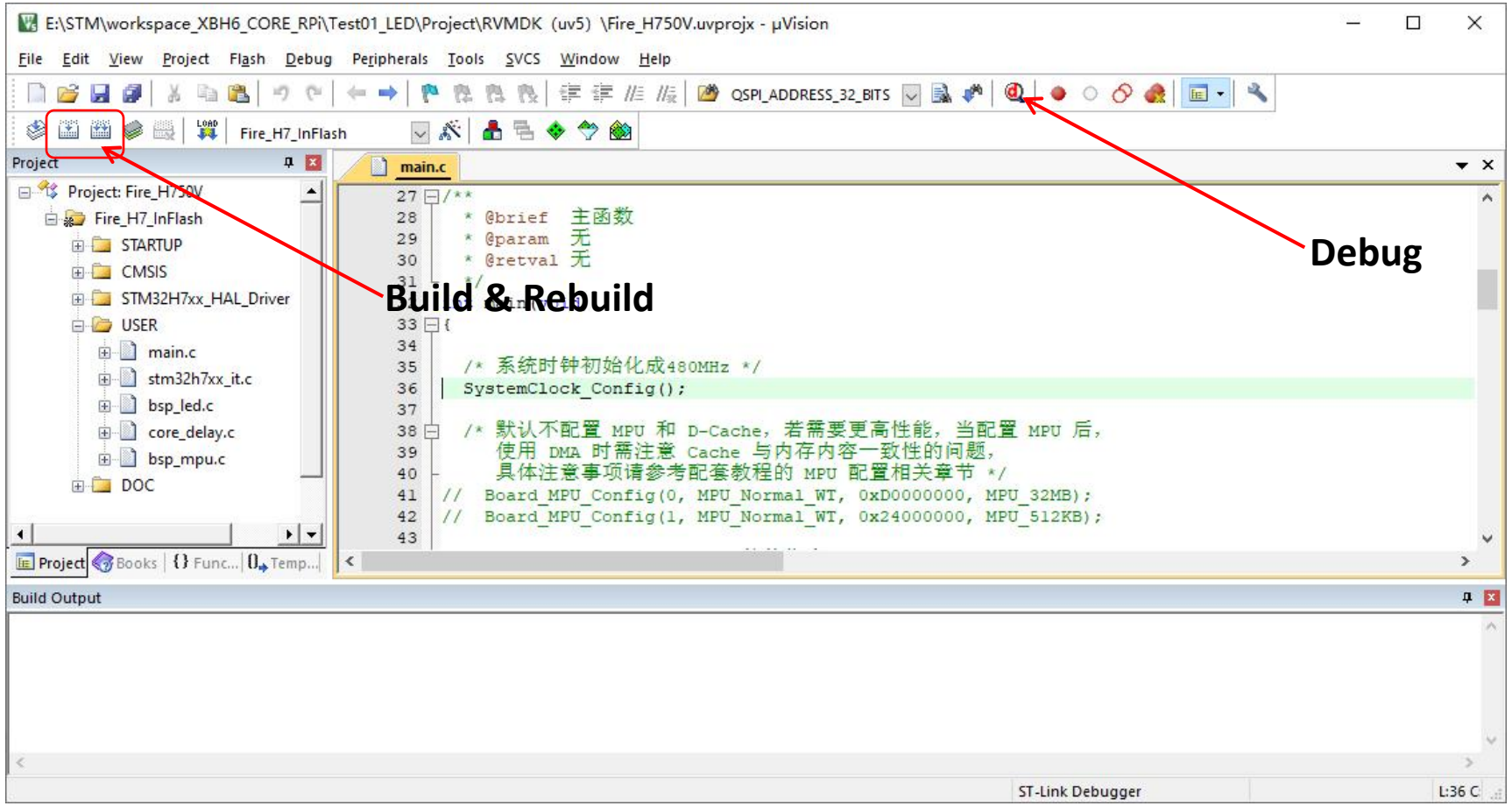
Example Settings (5)



Example Settings (6)



Main Icons


E:\STM\workspace_XBH6_CORE_RPi\Test01_LED\Project\RVMDK (uv5) \Fire_H750V.uvprojx - µVision
File Edit View Project Flash Debug Peripherals Tools SVCS Window Help
QSPI_ADDRESS_32_BITS
Fire_H7_InFlash
Project
Project: Fire_H750V
Fire_H7_InFlash
STARTUP
CMSIS
STM32H7xx_HAL_Driver
USER
main.c
stm32h7xx_it.c
bsp_led.c
core_delay.c
bsp_mpu.c
DOC
Project Books Func... Temp...
main.c
27 /**
28 * @brief 主函数
29 * @param 无
30 * @retval 无
31 */
33 {
34
35 /* 系统时钟初始化成480MHz */
36 SystemClock_Config();
37
38 /* 默认不配置 MPU 和 D-Cache，若需要更高性能，当配置 MPU 后，
39 使用 DMA 时需注意 Cache 与内存内容一致性的问题，
40 具体注意事项请参考配套教程的 MPU 配置相关章节 */
41 // Board_MPU_Config(0, MPU_Normal_WT, 0xD0000000, MPU_32MB);
42 // Board_MPU_Config(1, MPU_Normal_WT, 0x24000000, MPU_512KB);
43
Build Output
ST-Link Debugger
L:36 C
Build & Rebuild
Debug

Any further question, kindly send mail to:
zyjnumber@hotmail.com