ZDC custom FIFO chain proposal

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zugravel@to.infn.it

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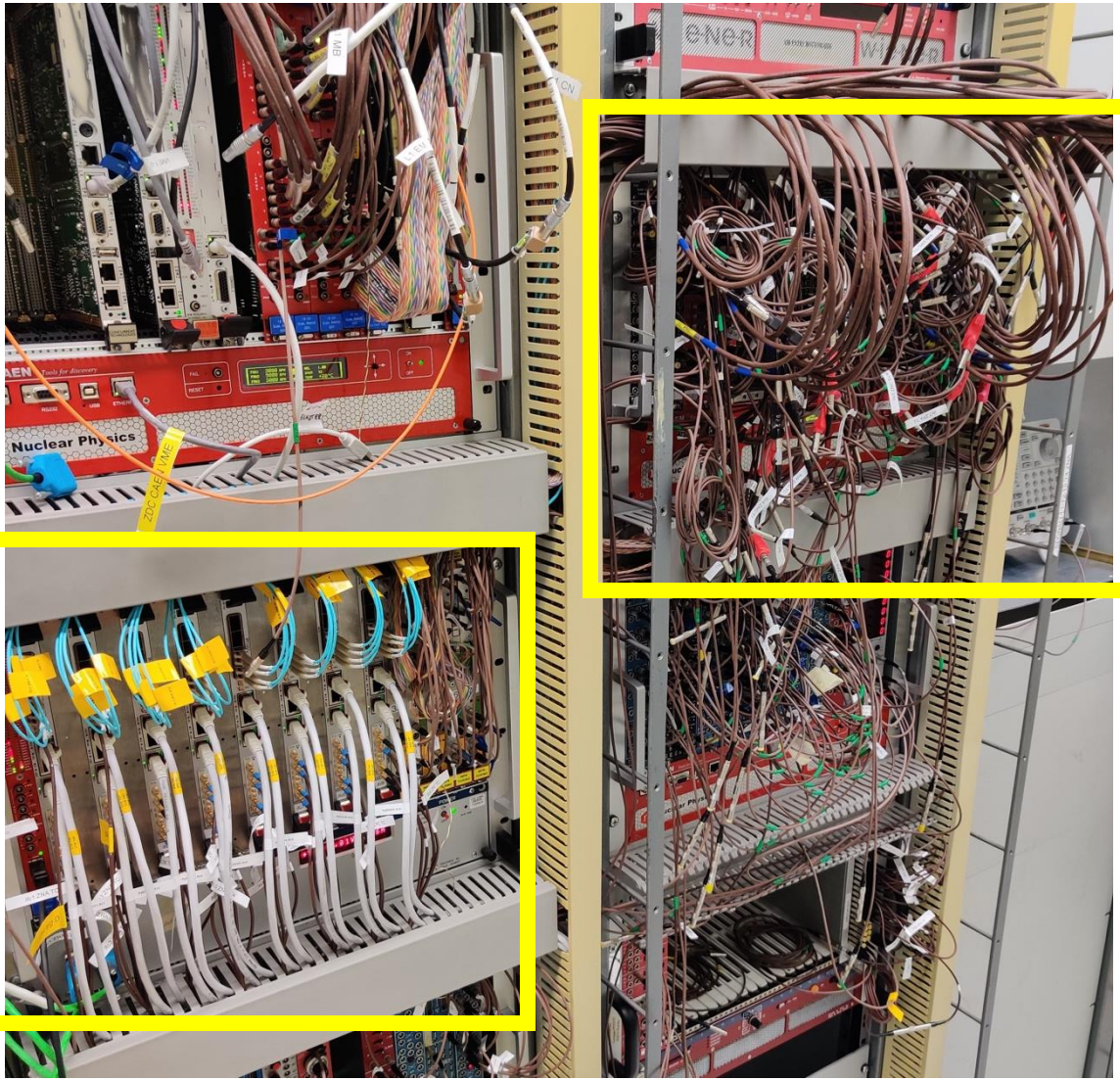
Current setup at P2



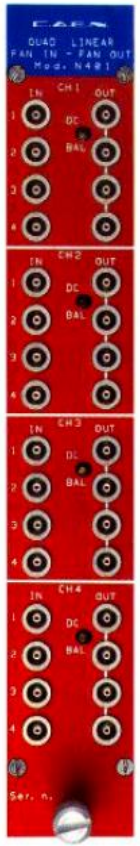
FPGA Readout system

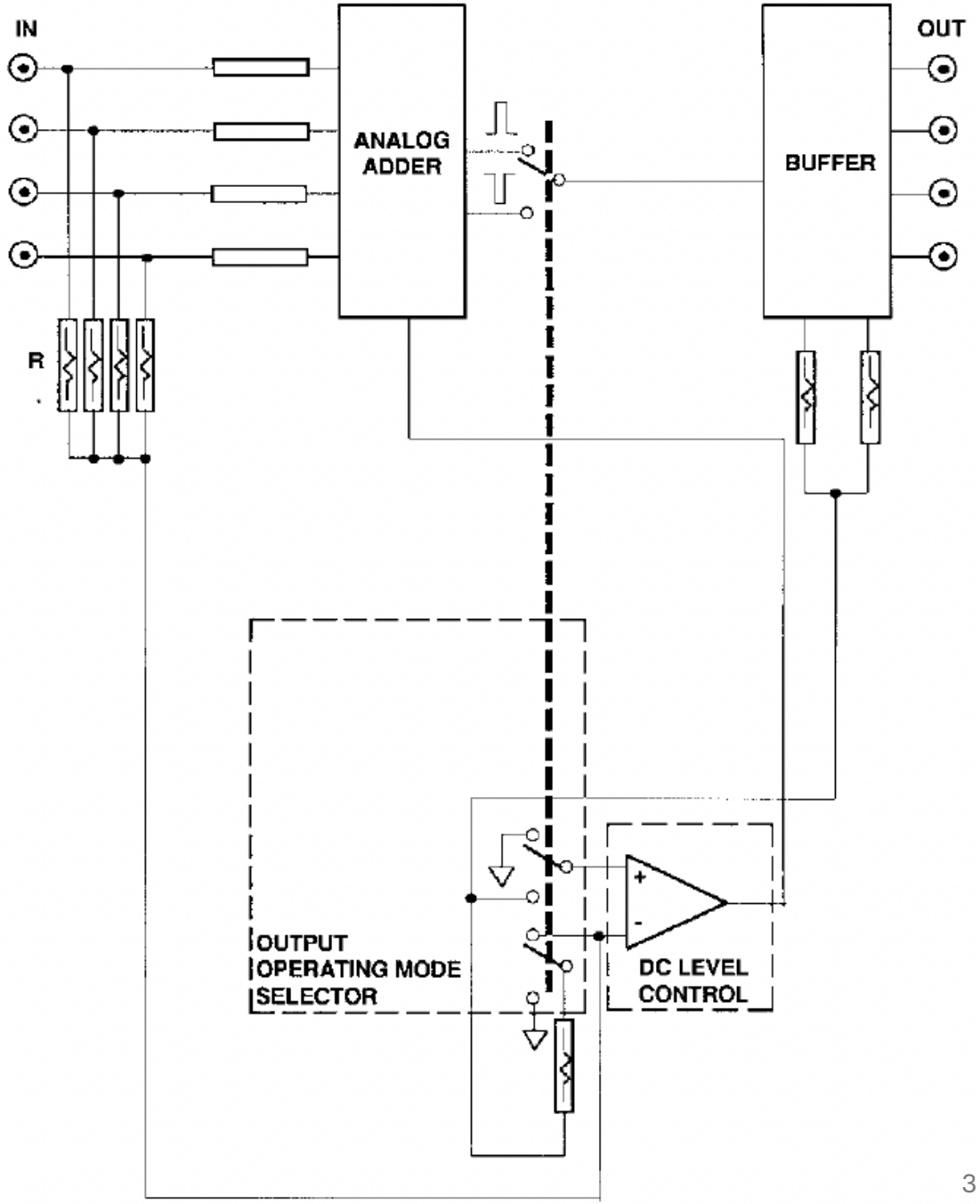
FIFO signal chain

CAEN N401

N401

Quad Linear FAN IN - FAN OUT



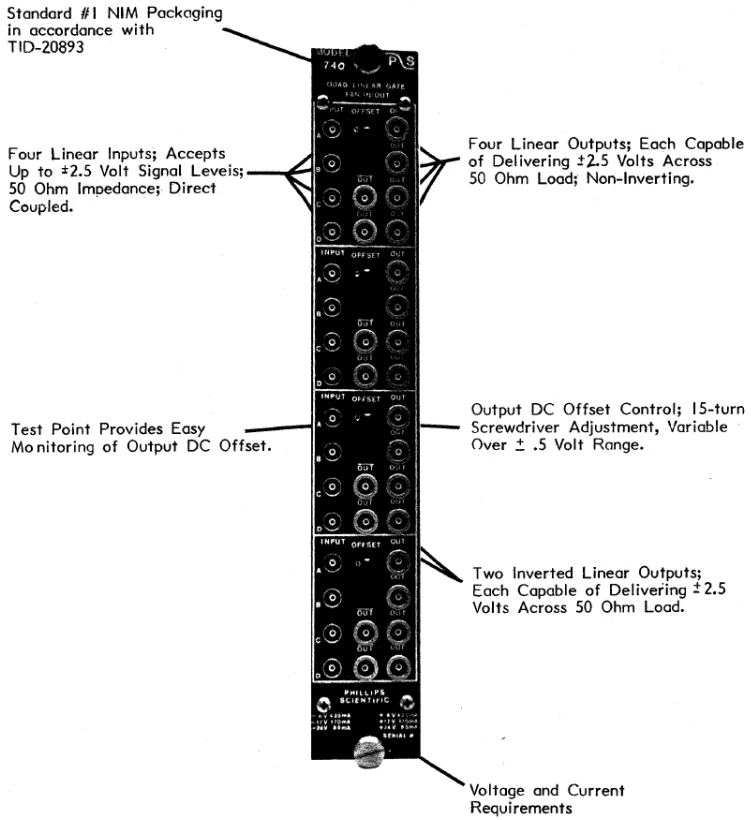


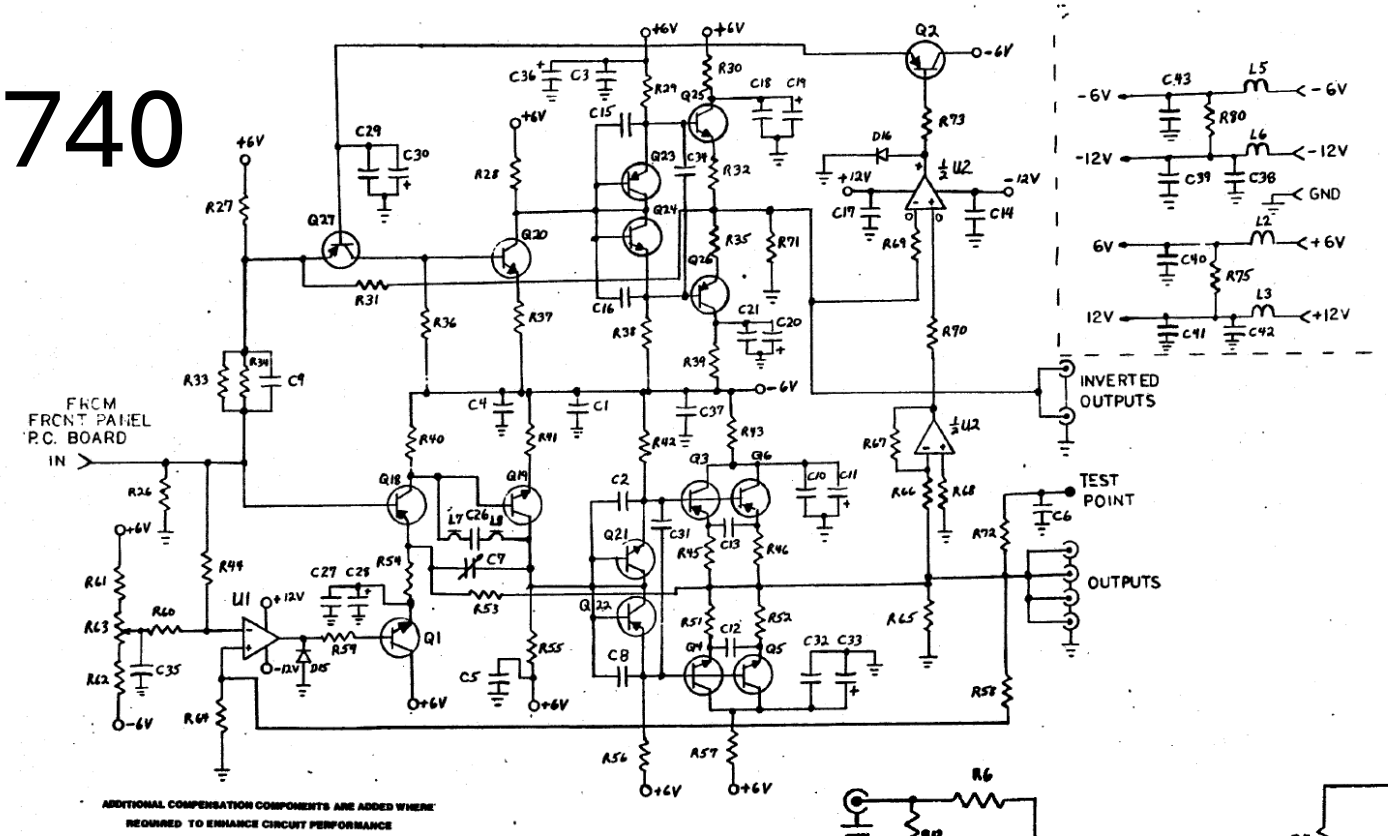
PHILLIPS model 740

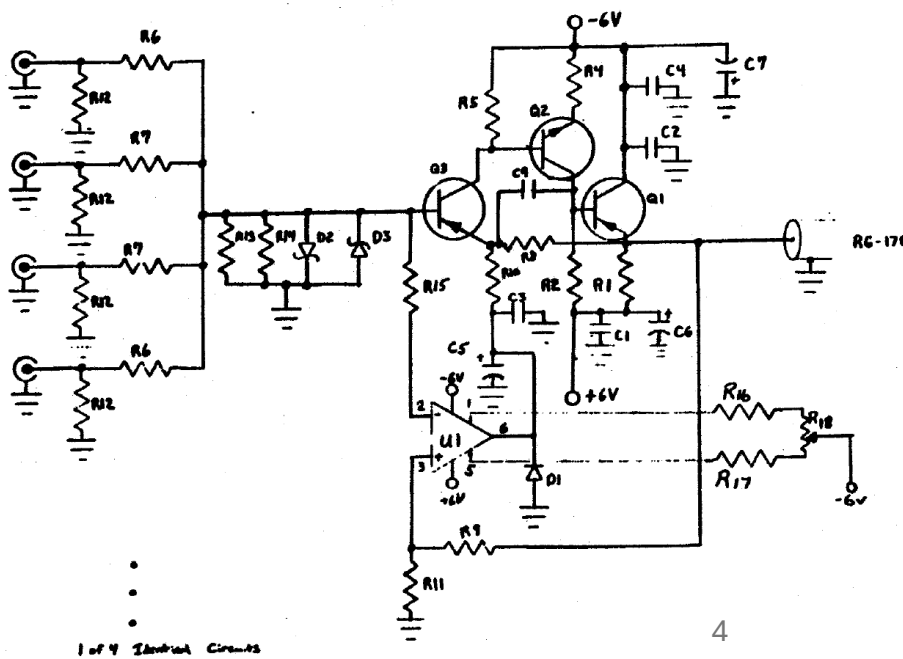
DATE: 6/18/89

MODEL 740 QUAD BIPOLAR LINEAR FAN-IN/FAN-OUT

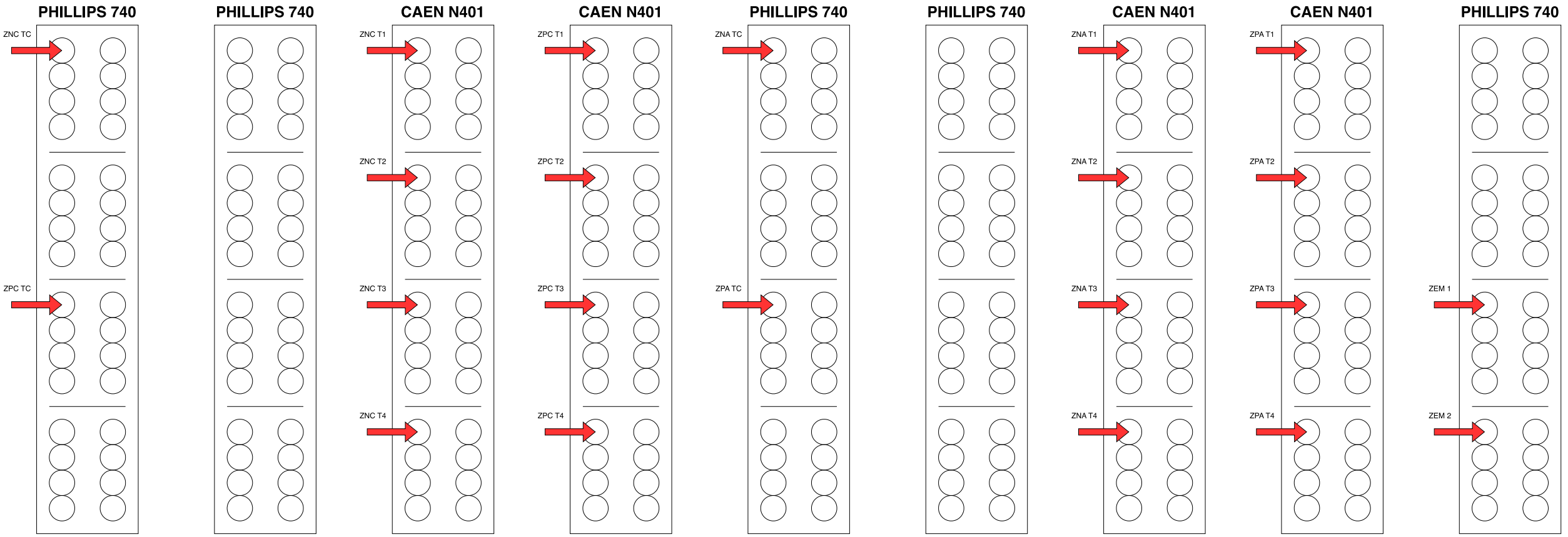
(Front Panel Description)





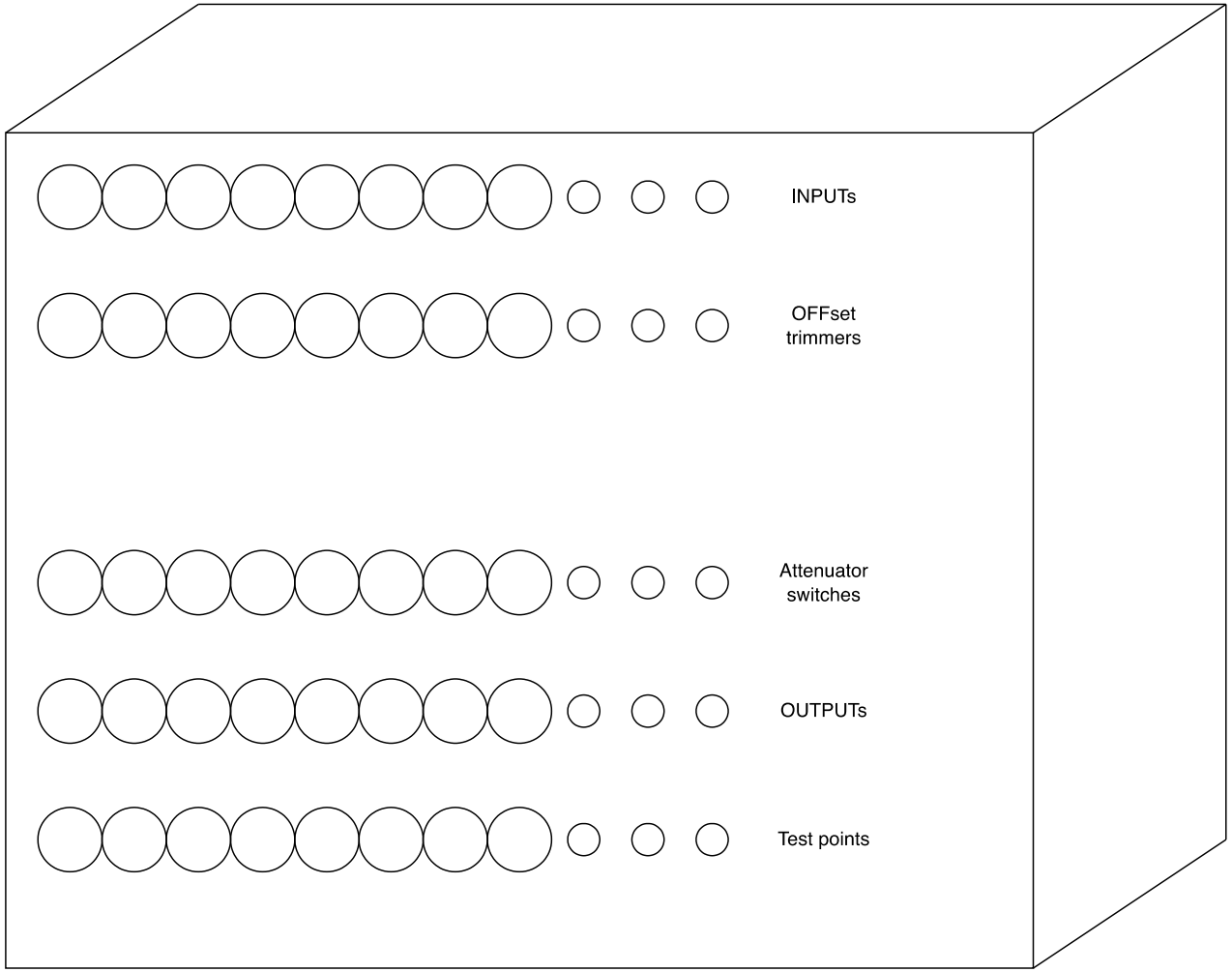


Current setup diagram



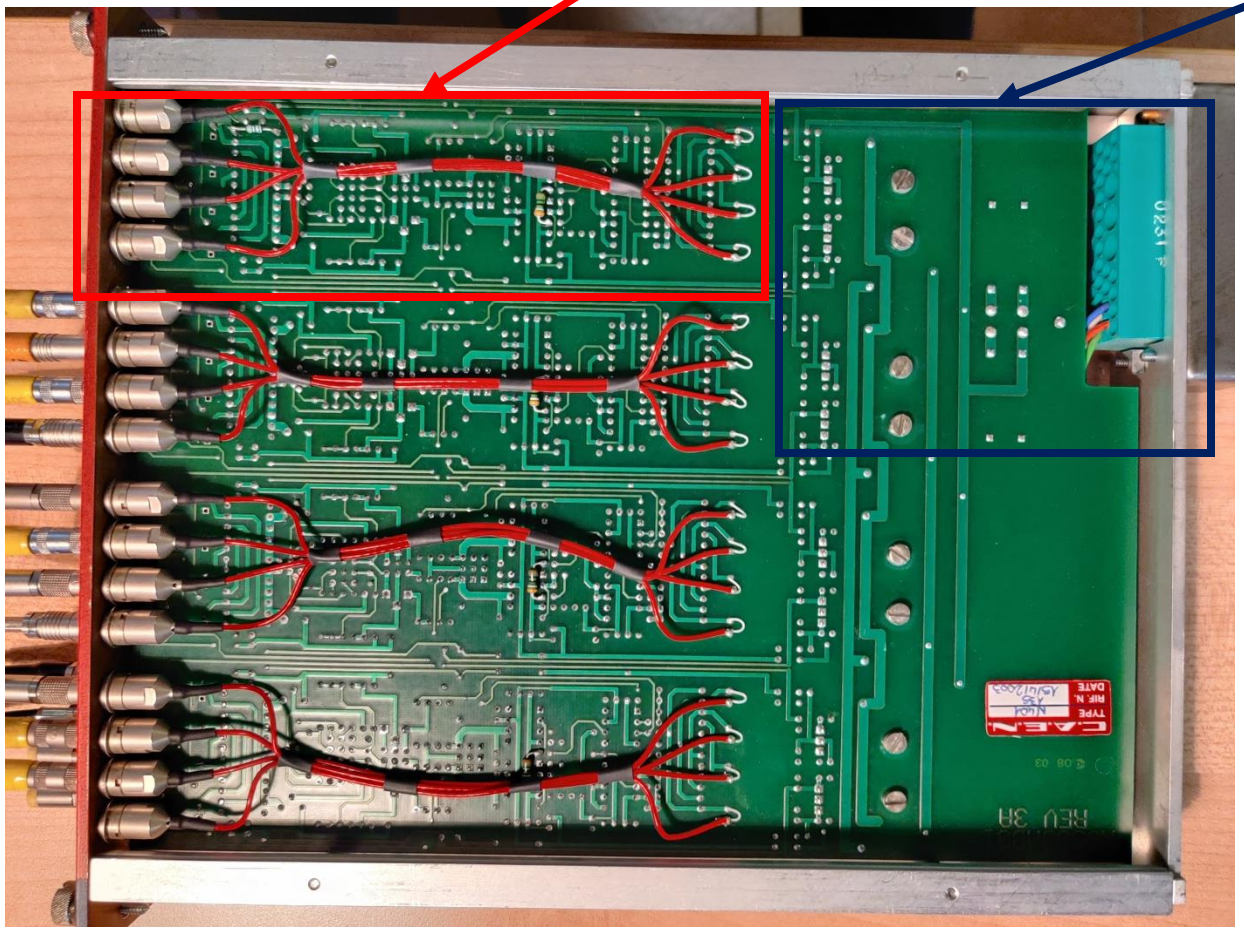
TO BE COMPLETED

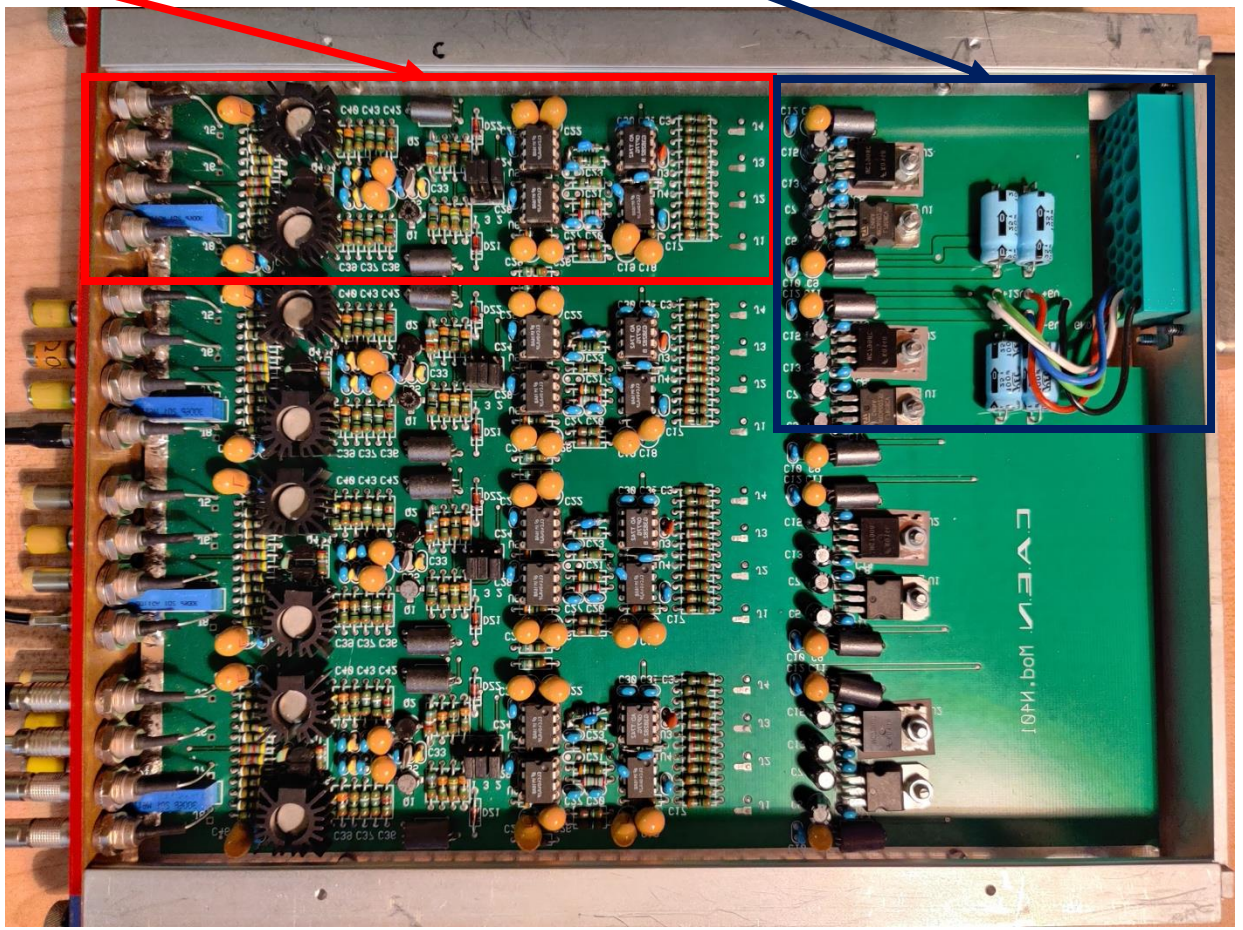
Proposal for custom FIFO module



Signal

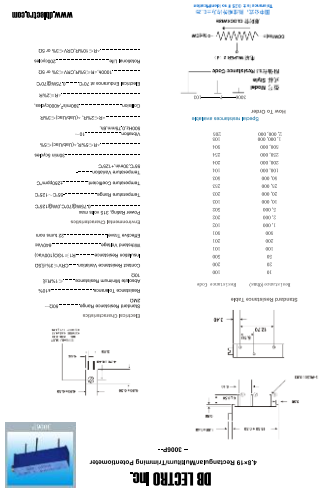
Power

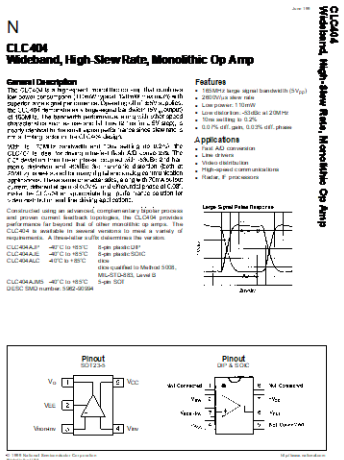


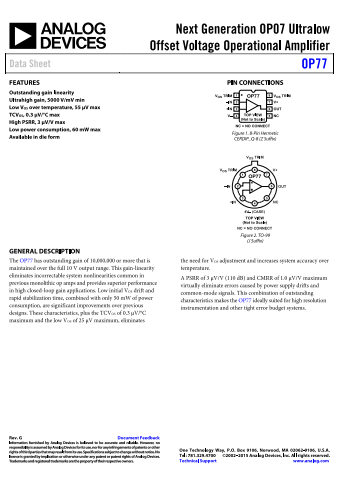


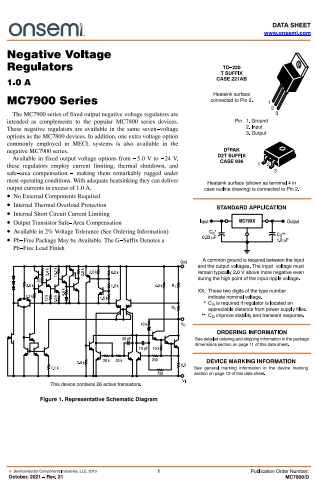
Components used

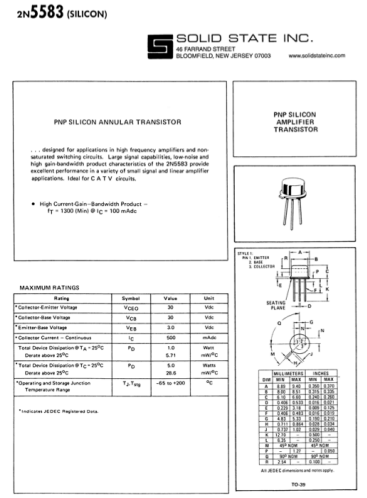
- T3006P
- CLC404
- OP77G
- MC7908C
- L7808CV
- 2N5583 0216

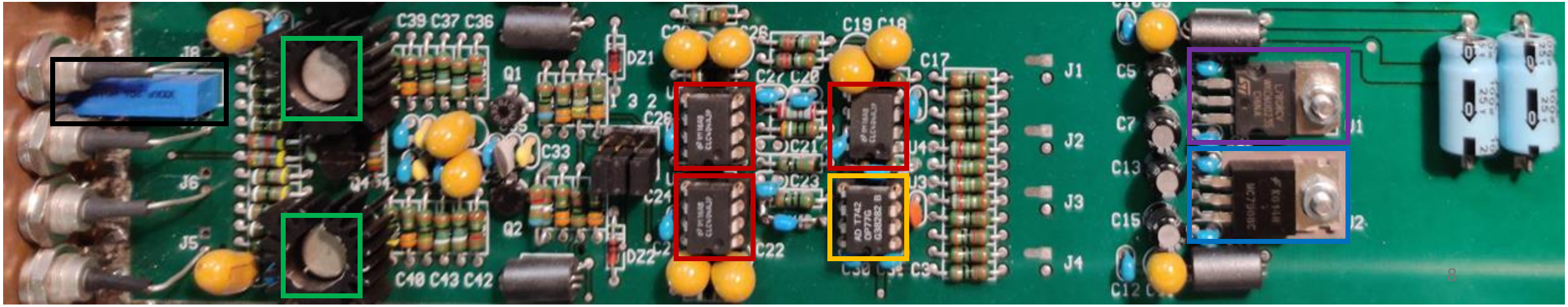


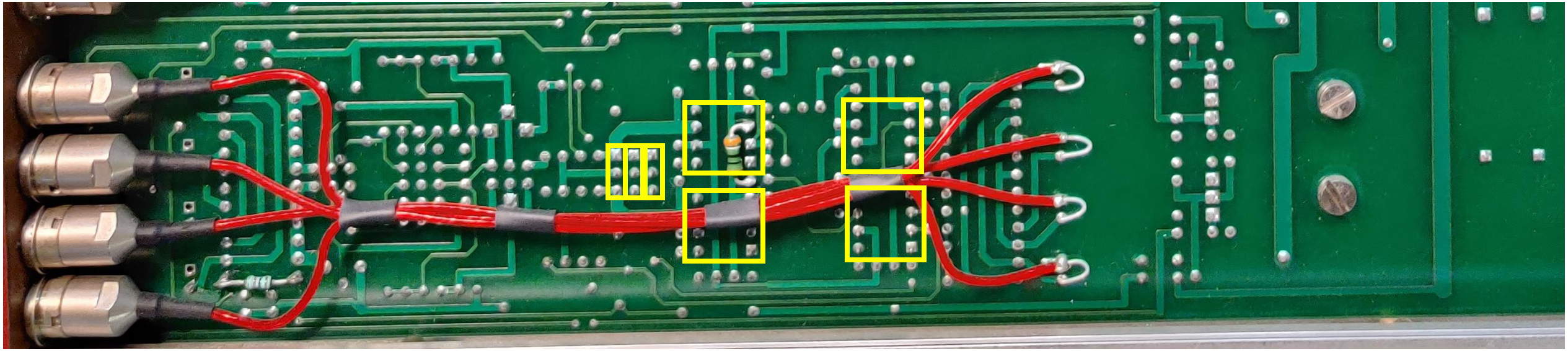


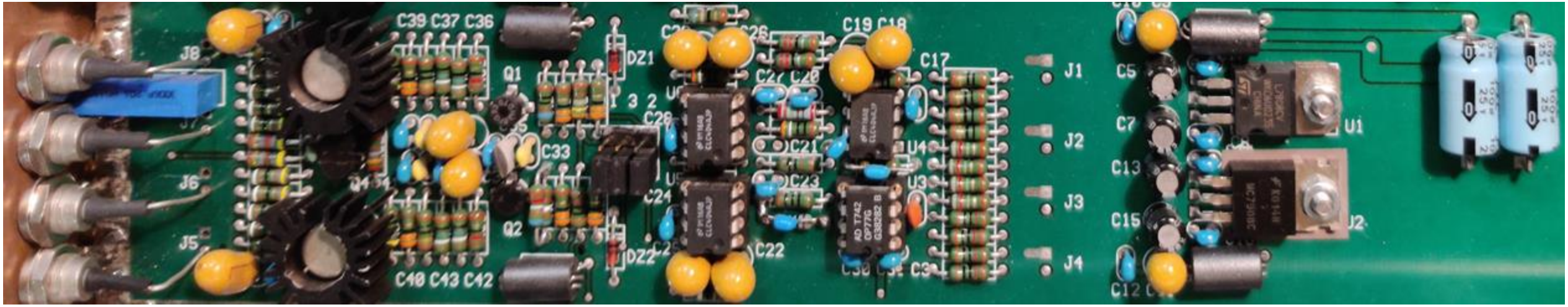








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J8
J6
J5
DZ1
Q1
C33
Q2
C18 C13 C12
C19 C18
C17
C22
J1
J2
J3
J4
C5
C7
C13
C15
U1
U2